

MODEL

TM14-20RH/RP

TM20-20RH/RP

TM20-30RH/RP

COLOR MONITOR

SERVICE MANUAL

Ikegami

INFORMATION TO USER FOR FCC

Warning – *This equipment generates, uses and can radiate radio frequency energy and if not installed and used in accordance with the instruction manual, may cause interference to radio communications.*

It has been tested with a class A computing device and found to comply with the limits for a Class A computing device in accordance with the specifications in subpart J of Part 15 of FCC Rules, which are designed to provide reasonable protection against such interference when operated in a commercial environment.

Operation of this equipment in a residential area is likely to cause interference in which case the user at his own expense will be required to take whatever measures may be required to correct the interference.

SAFETY PRECAUTIONS

1. Comply with caution and safety related notes located on the shield case in the receiver.

2. **WARNING**

Any alteration should not be made in the design or circuitry of this receiver.

Any design alterations or additions may alter the safety characteristic of this receiver and potentially create a hazardous situation for the user.

Any design alterations or additions will void the manufacturer's warranty and will further relieve the manufacturer of responsibility for personal injury or property damage resulting therefrom.

3. **CRT**

The picture tube in this receiver employs integral implosion protection. Replace with a tube of the same type number for continued safety.

4. **X-RADIATION AND HIGH VOLTAGE LIMITS**

The primary source of potential X-radiation in solid state receivers is the picture tube.

The picture tube is specially constructed to prohibit X-radiation emissions. For continued X-radiation protection, the replacement tube must be the same type as the original.

The shields and mounting hardware for picture tubes have an X-radiation protection function and must be properly in place.

High voltage must be checked each time any service is required that involves B+, horizontal deflection or high voltage.

Where used, X-radiation protection circuits must be checked for proper operation each time the X-radiation protection circuit is serviced.

Refer to the warning label on the shield case in the receiver and the schematic in the manual and, where used, X-radiation protection circuits specifications.

High voltage is maintained within specified limits by the use of close tolerance safety related components /adjustments in the high voltage circuit. If high voltage exceeds specified limits, check each component specified on the schematic diagram and take necessary corrective action.

5. **PRODUCT SAFETY NOTICE**

Many electrical and mechanical parts in receiver sets have special safety-related characteristics. These characteristics are often not evident from visual inspection nor can the protection afforded by them necessarily be obtained by using replacement components rated for higher voltage, wattage, etc.

Replacement parts which have these special safety characteristics are identified in this manual.

Electrical components having such features are identified by (*) on the parts list and the schematic diagram in this manual.

The use of a substitute replacement which does not have the same safety characteristics as the recommended replacement part shown in the parts list in this manual may create shock, fire, or other hazards.

Product Safety is continuously under review and new instructions are issued from time to time.

For the latest information always consult the current **Ikegami** Service Data. A subscription to, on additional copies of, **Ikegami** Service Data may be obtained at a nominal charge from **NY-Ikegami**.

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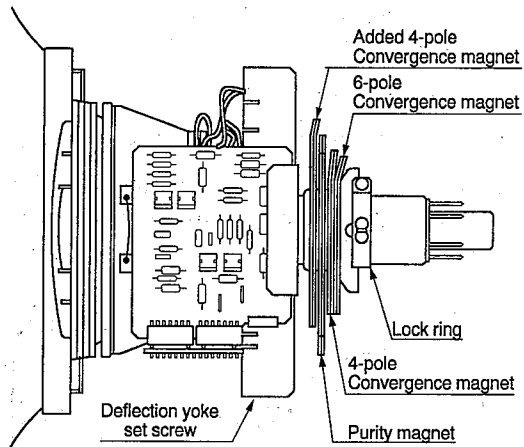
20/30 series Service Manual Maintenance Adjustment

When the specified performance can no longer be obtained with the adjusters on the front panel or when parts have been replaced due to a malfunction, perform

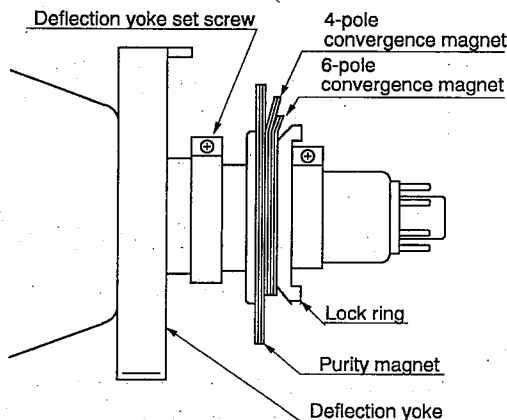
adjustment of the following parts.

When adjusting the board inserted into the slot section, use the EXTENDER BOARD(option).

1. MAIN CHASSIS



TM20-30RH/RP



TM14-20RH/RP
TM20-20RH/RP

(1) Purity Adjustment

- ① Select the "FLAT FIELD" with the **TEST** switch.
- ② Press the **DEGAUSS** switch to demagnetize the magnetized shadow mask.

- ③ Set the raster of the screen to a single green color with the **SCREENS** switches. (When the "SCREEN" mode of MENU 4 is the "MODE 1", set only the **G.SCREEN** switch to "ON" position, and when the "SCREEN" mode is the "MODE 2", set the **R.SCREEN** and **B.SCREEN** switches to "ON" position.)
- ④ Loosen the deflection yoke set screw, remove the silicon which holds the deflection yoke and CRT, and slide the deflection yoke all the way back.
- ⑤ Loosen the lock ring which holds the magnets.
- ⑥ Adjust the two purity magnets alternately so that there are green vertical lines at the center of the screen.
- ⑦ While watching the screen, slide the deflection yoke forward so that the screen is an even green color. If the screen does not become an even green color, perform adjustment again from step ④.
- ⑧ Set to blue and red, and confirm that the screen is a single color.
- ⑨ Set to white raster and if there is partial coloring of the raster, slightly shift the position of the deflection yoke either forward or back.
- ⑩ After completing adjustment, tighten the deflection yoke set screw and lock ring.

(2) Convergence Adjustment

Before performing convergence adjustment, allow the monitor to warm up for at least 30 minutes.

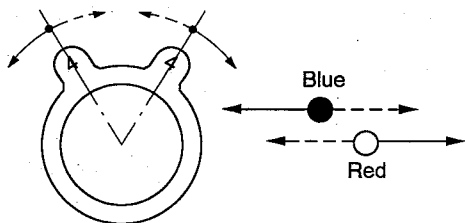
Select the "CROSS HATCH" with the **TEST** switch.

(a) Center convergence

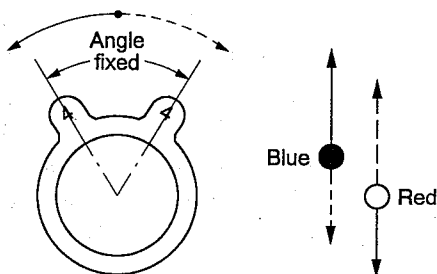
- ① Loosen the lock ring.

② Set the screen to red and blue with the **SCREENS** switch. (When the "SCREEN" mode of MENU 4 is the "MODE 1", set the **R.SCREEN** and **B.SCREEN** switches to "ON" position, and when the "SCREEN" mode is the "MODE 2", set only the **G.SCREEN** switch to "ON" position.)

③ While paying attention to the cross section in the center of the screen, adjust the angle of the two 4-pole magnets(TM14/20-20RH/RP) or the angle of the two added 4-pole magnets(TM20-30RH/RP) as shown below to adjust the shifting of the vertical blue and red lines.

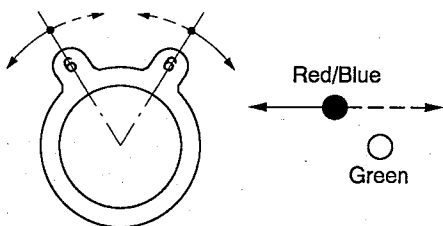


④ With the angle of step ③ remaining, rotate the two magnets simultaneously to adjust the shifting of the horizontal lines.

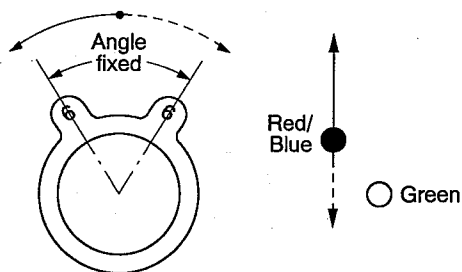


⑤ Set all the **SCREENS** switch to "OFF" position, and set to white screen.

⑥ Adjust the angle of the two 6-pole magnets and adjust shifting of the red and blue vertical lines and green vertical lines.



⑦ With the angle of step ⑥ remaining, rotate the two magnets simultaneously to adjust the shifting of the horizontal lines.

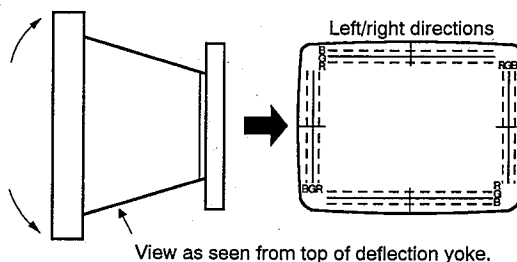
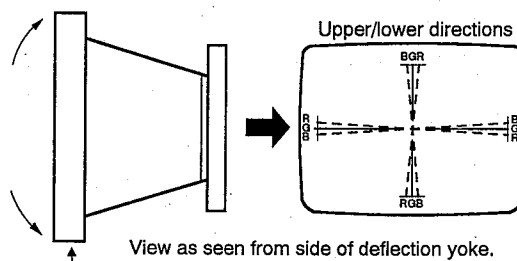


⑧ Tighten the rock ring after completing adjustment of the center convergence.

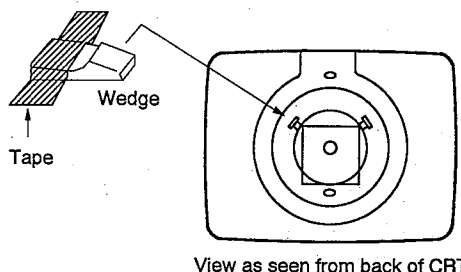
If there is poor peripheral convergence, perform the adjustment described in following (b).

(b) Peripheral convergence

- ① Slightly loosen the deflection yoke set screw.
- ② Move the deflection yoke up, down, and to the left and right as shown below to adjust peripheral shifting.



③ After the completion of adjustment, insert wedges into the space between the deflection yoke and CRT as shown below to fully lock the deflection yoke set screw.



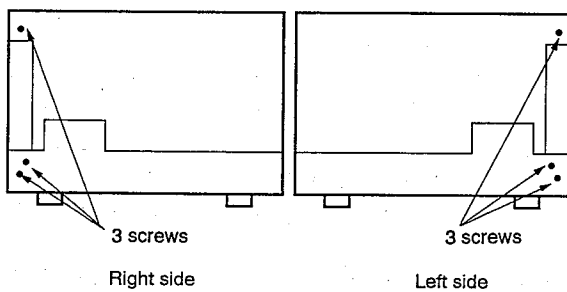
(3) Replacement of CRT

As the CRT for this monitor is supplied with the deflection yoke already attached, there is no need to readjust the purity and convergence.

Replacement of the CRT is performed in the following manner.

(a) For TM14-20RH/RP

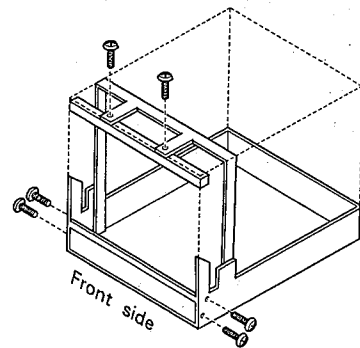
- ① Remove the four screws which hold the top cover, and remove the cover.
- ② Remove the four screws each which hold the right and left covers, and remove the covers.
- ③ Remove the anode cap of the CRT.
- ④ Remove the CRT SOCKET BOARD from the CRT.
- ⑤ Remove the deflection yoke connector (CN901) on the DEF BOARD.
- ⑥ Pull out the FRONT RIGHT PANEL on the right side and remove the connector connected to the MOTHER BOARD at the FRONT RIGHT PANEL side. Remove the FRONT RIGHT PANEL from the main unit.
- ⑦ Remove the connector which connects the FRONT LEFT PANEL on the left side and the DEF BOARD at the FRONT LEFT PANEL side. Pressing the upper nail, remove the FRONT LEFT PANEL.
- ⑧ Remove the six screws (see below) which hold the main unit and escutcheon, and remove the CRT from the main unit together with the escutcheon.
Make sure that the neck of the CRT does not touch the main unit at this time.



- ⑨ Place the CRT on a stable surface with the escutcheon down.
Place a cloth below the escutcheon to prevent it from being damaged.
Remove the four screws which attach the escutcheon to the CRT and attach to the new CRT.
- ⑩ Reassemble the unit by following steps ① through ⑧ above in reverse order.

(b) For TM20-20/30 RH/RP

- ① Remove the four screws which hold the top cover, and remove the cover.
- ② Remove the anode cap of the CRT.
- ③ Remove the CRT SOCKET BOARD from the CRT.
- ④ Remove the deflection yoke connector (CN901) on the DEF BOARD.
- ⑤ Remove the connector connected to the LED BOARD on the upper of the CRT.
- ⑥ Pull out the FRONT PANEL and remove the connector connected to the MOTHER BOARD at the FRONT PANEL side. Remove the FRONT PANEL from the main unit.
- ⑦ Remove the screw which holds the FRONT LEFT PANEL, pull out the FRONT LEFT PANEL, remove the two connectors connected at the FRONT LEFT PANEL side, and remove the FRONT LEFT PANEL.
- ⑧ Remove the six screws (see below) which hold the main unit and escutcheon, and remove the CRT from the main unit together with the escutcheon.
Make sure that the neck of the CRT does not touch the main unit at this time.

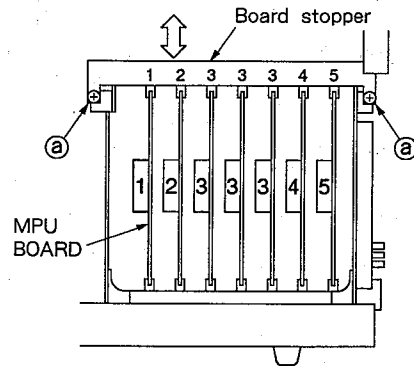


- ⑨ Perform steps ⑨ to ⑩ of section (a).

(c) Readjustment Items after CRT Replacement

- ① Center adjustment of screen
Refer to section 5-4(7) of the OPERATION MANUAL.
- ② Adjustment of screen size
Refer to section 5-5(1) of the OPERATION MANUAL.
- ③ Adjustment of focus
Refer to section 5-4(3) of the OPERATION MANUAL.

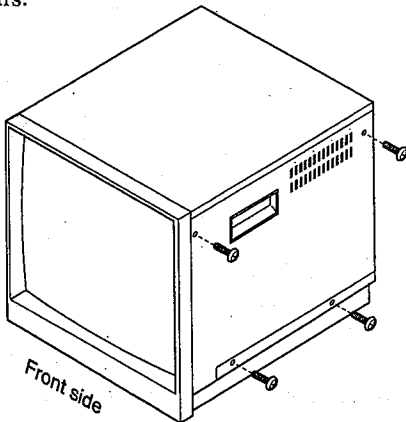
- ④ Adjustment of white balance
Refer to section 5-4 (1) of the OPERATION MANUAL.
- ⑤ Adjustment of deflection linearity
Refer to section 3-1 (3) of the SERVICE MANUAL.
- ⑥ Adjustment of side pin
Refer to section 3-1 (3) of the SERVICE MANUAL.



(4) Replacement of Battery

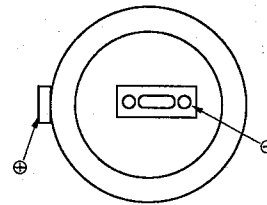
Before changing the battery, because of preparing for the worst, display the "STATUS" screen of MENU 5 (refer to section 5-6(7) of the OPERATION MANUAL) and note each preset data at the last page of the OPERATION MANUAL. As there are three kinds of side in HEIGHT and WIDTH by the scanning size, note each data.

- ① Set the **POWER** switch on the FRONT PANEL to "OFF" position.
- ② Remove the four screws which hold the right side cover, and remove the cover. The SLOT section appears.

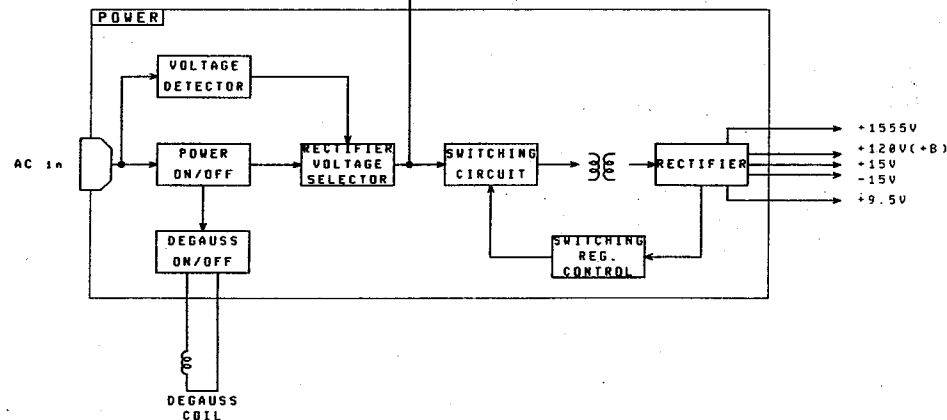
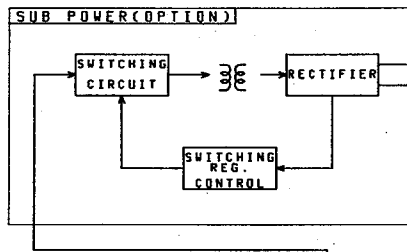
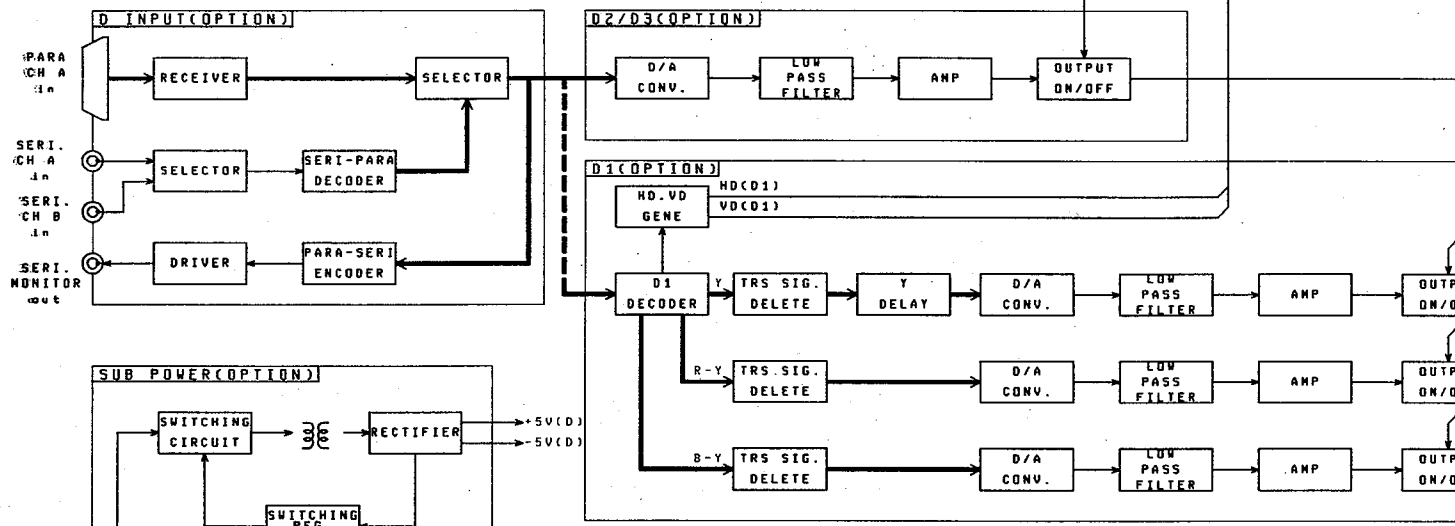
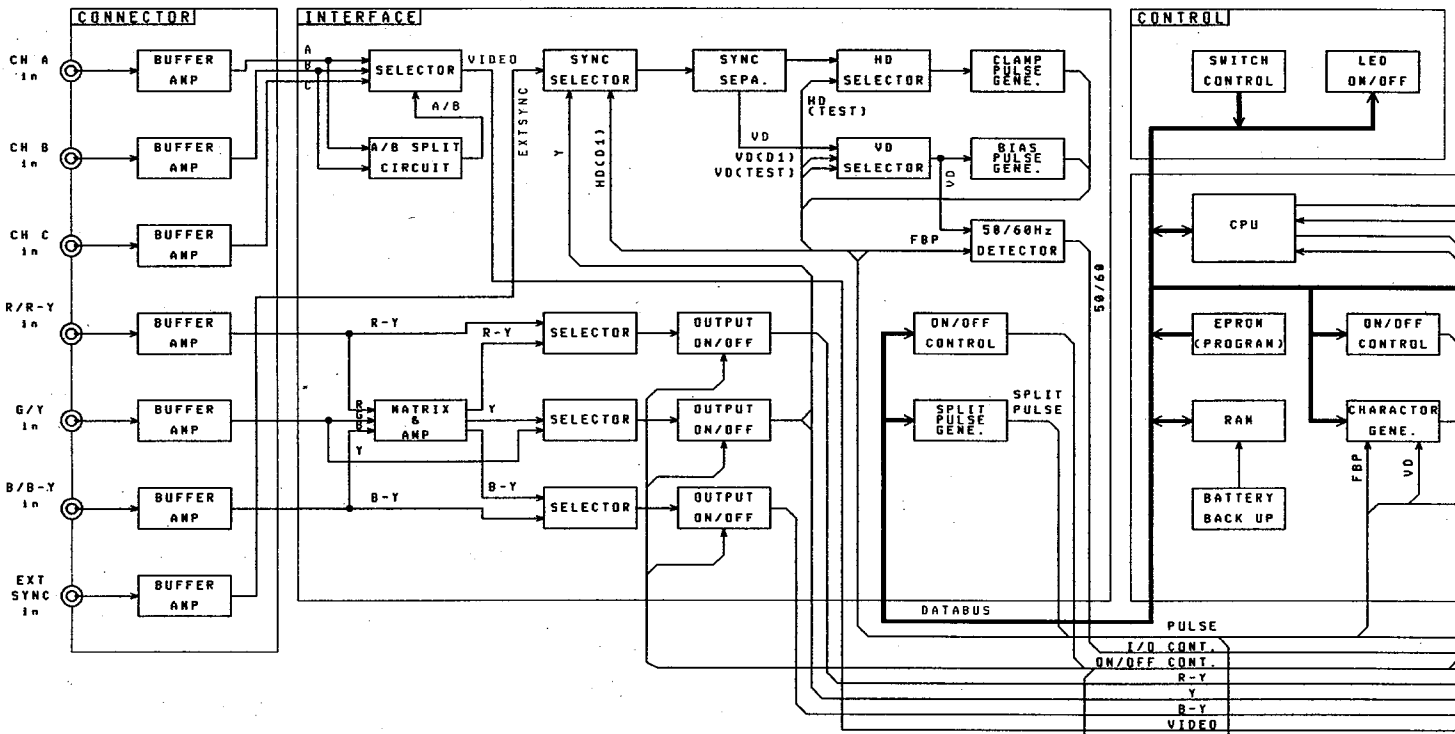


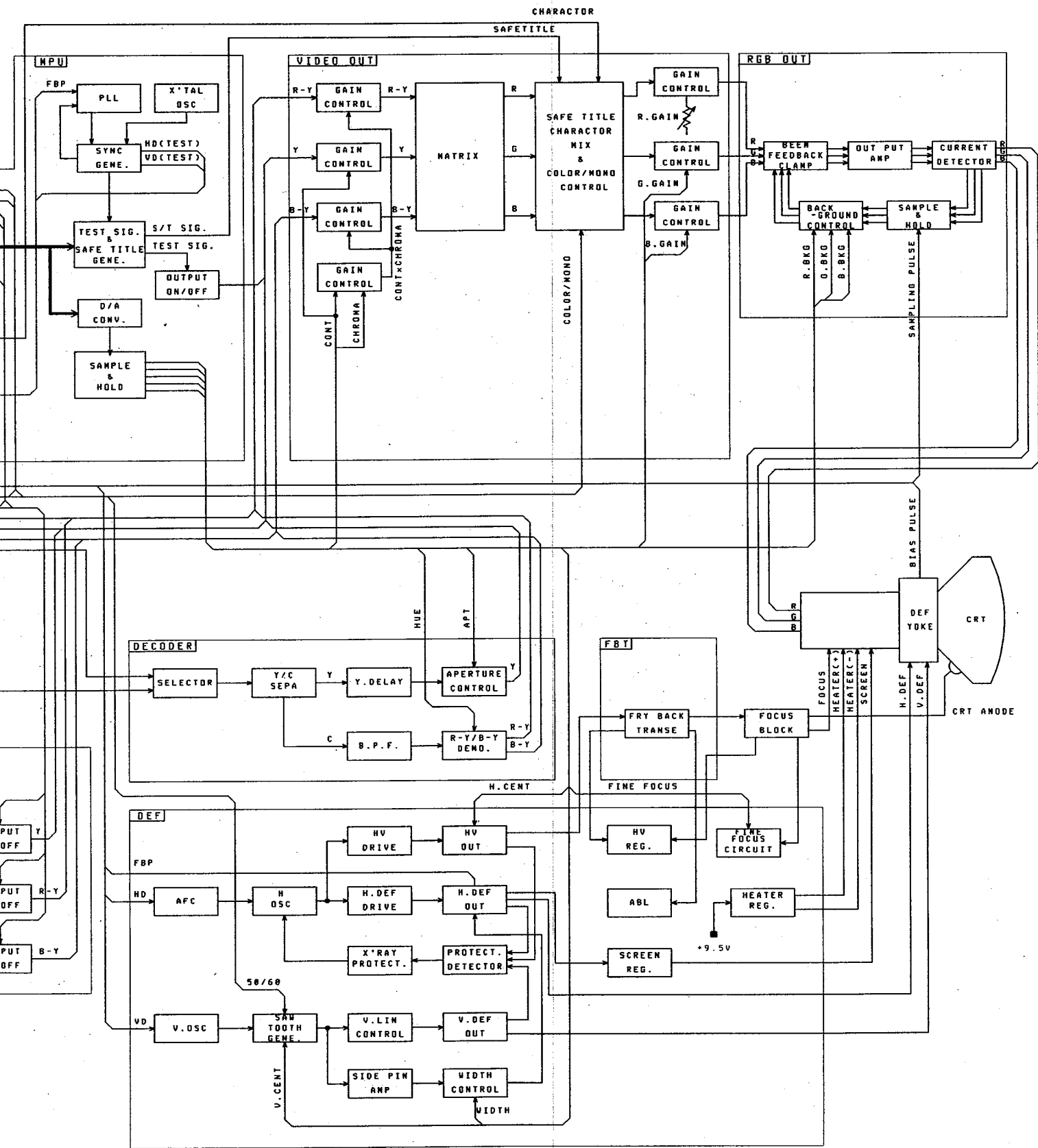
- ③ Loosen the screw shown by ② below so that the board stopper can be moved, and lift up the board stopper upward.
Tighten the screw of ② to hold the board stopper in that place.

- ④ Set the **POWER** switch to "ON" position. So the C125 capacitor on the MPU BOARD is charged.
- ⑤ Set the **POWER** switch to "OFF" position again.
At once, take out the MPU BOARD inserted into the SLOT No.1 and replace the BT101 battery with a new battery within **four minutes**.
Make sure that the battery is installed with the polarity aligned according to the marking on the board.
Replacing the battery within four minutes retains each data.

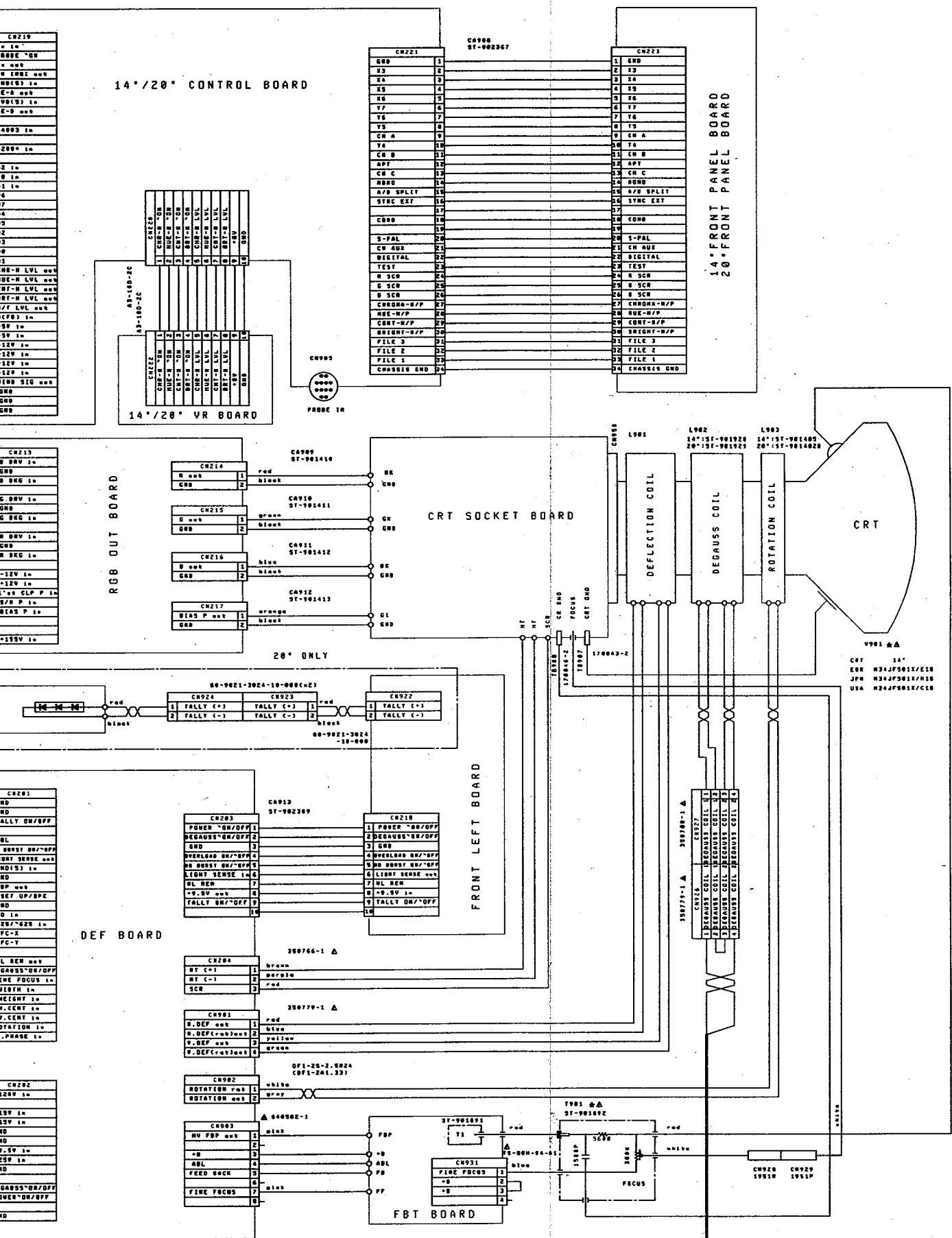


- ⑥ Insert the MPU BOARD into the SLOT No.1.
- ⑦ Set the **POWER** switch to "ON" position and confirm that the screen is normal.
When the screen does not appear normally, possible causes include accidental shorting of capacitor C125 for RAM data backup during battery replacement or more than four minutes were used for replacement of the battery and the RAM data was lost.
In this case, reset each preset data due to data noted before battery replacement, and confirm each setting of the MENU.
- ⑧ Reassemble the unit by following steps ② through ③ above in reverse order.

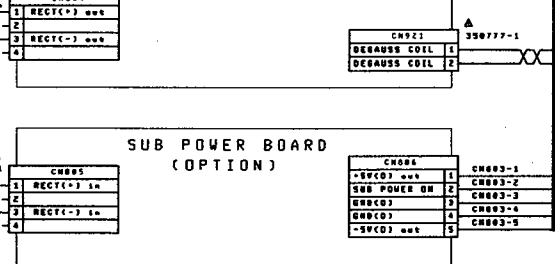
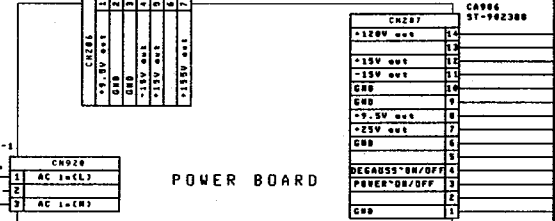
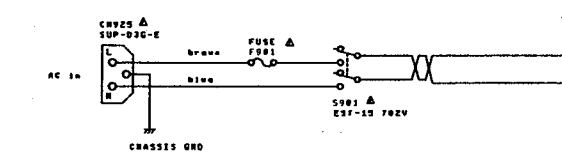
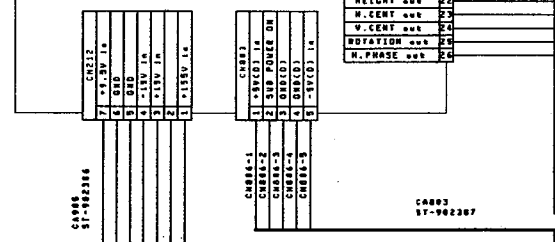
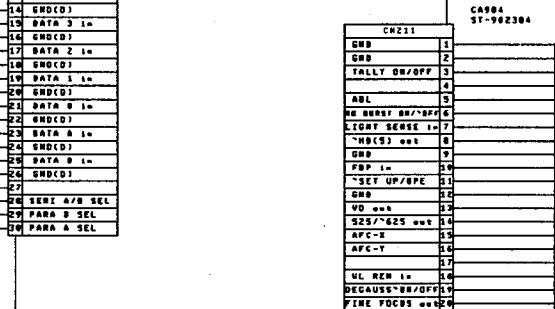
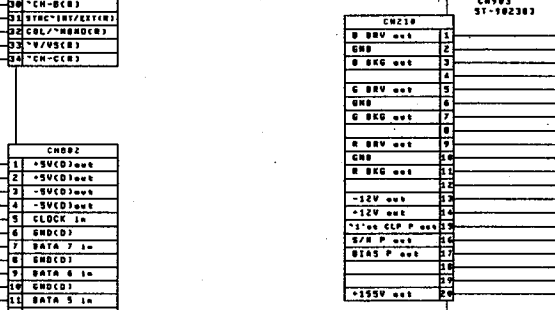
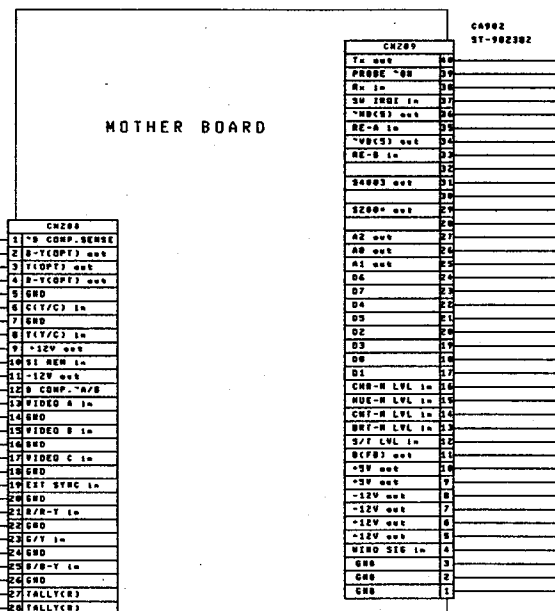
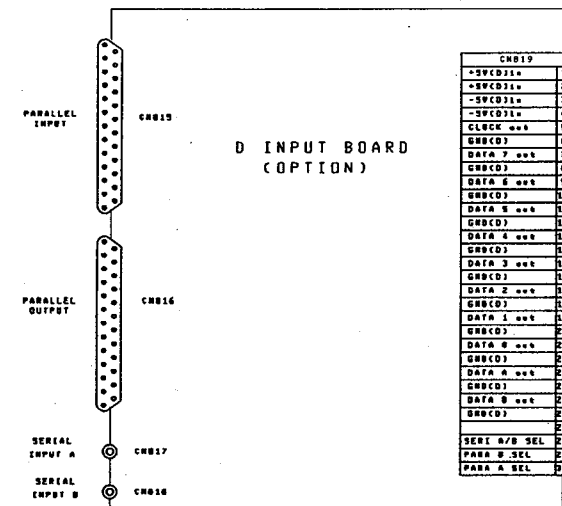
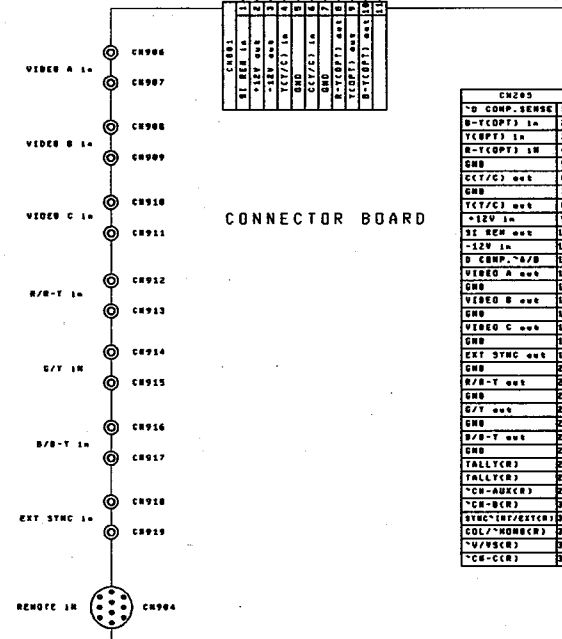
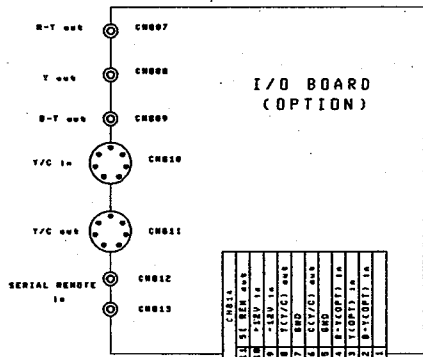




20/30 SERIES
 COLOR MONITOR
 GENERAL
 Block Diagram
 C2-904354



**20 SERIES
COLOR MONITOR
MAIN CHASSIS
Schematic Diagram
C1-904260A**



2. VIDEO PROCESS

2-1. MOTHER BOARD

(1) Outline

The functions of this board is interfacing with the signal and control line between various boards.

(2) Adjustment Procedure

(a) VR1 (+12V ADJ)

- ① Connect the minus side of the voltmeter to TP4 and the plus side to TP1.
- ② Adjust VR1 so that the DC voltage is +12V.

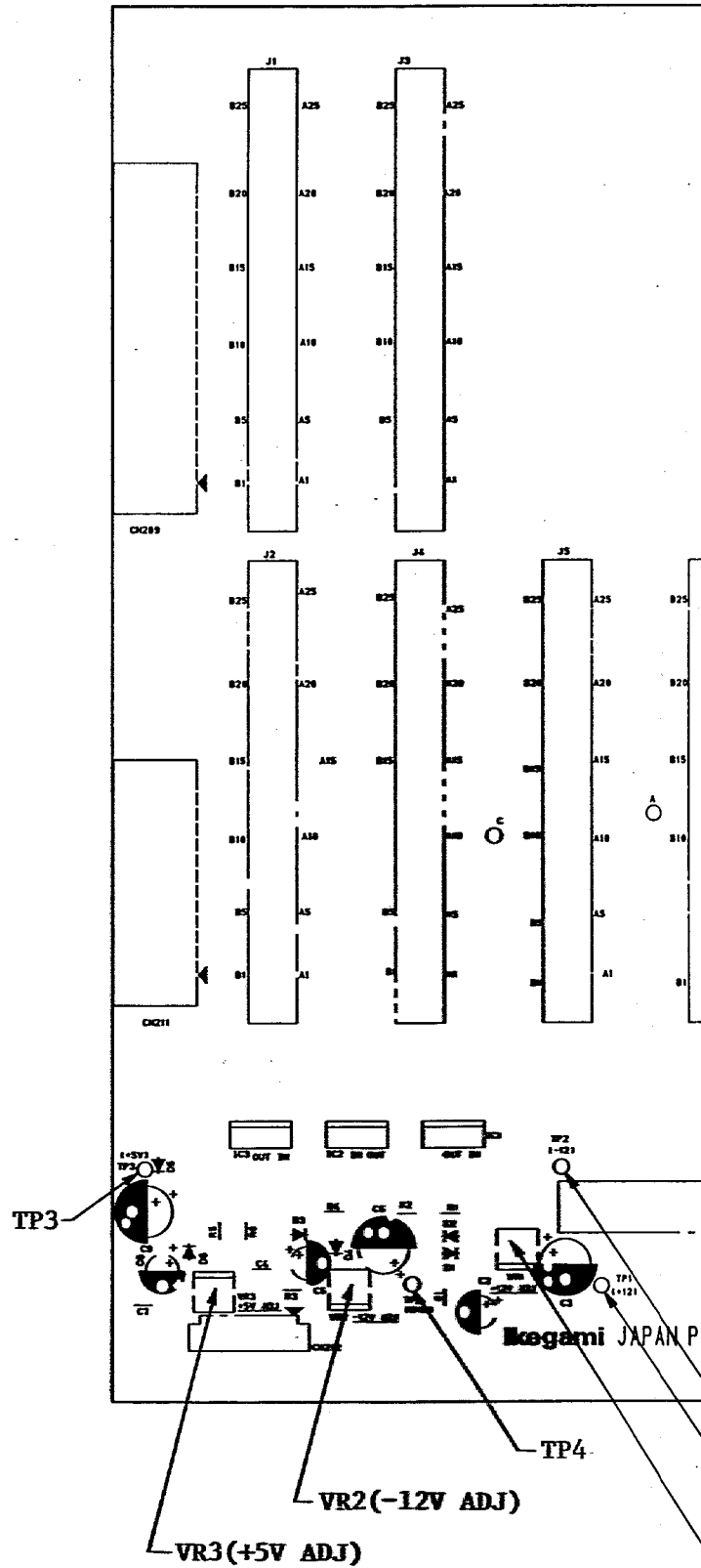
(b) VR2 (-12V ADJ)

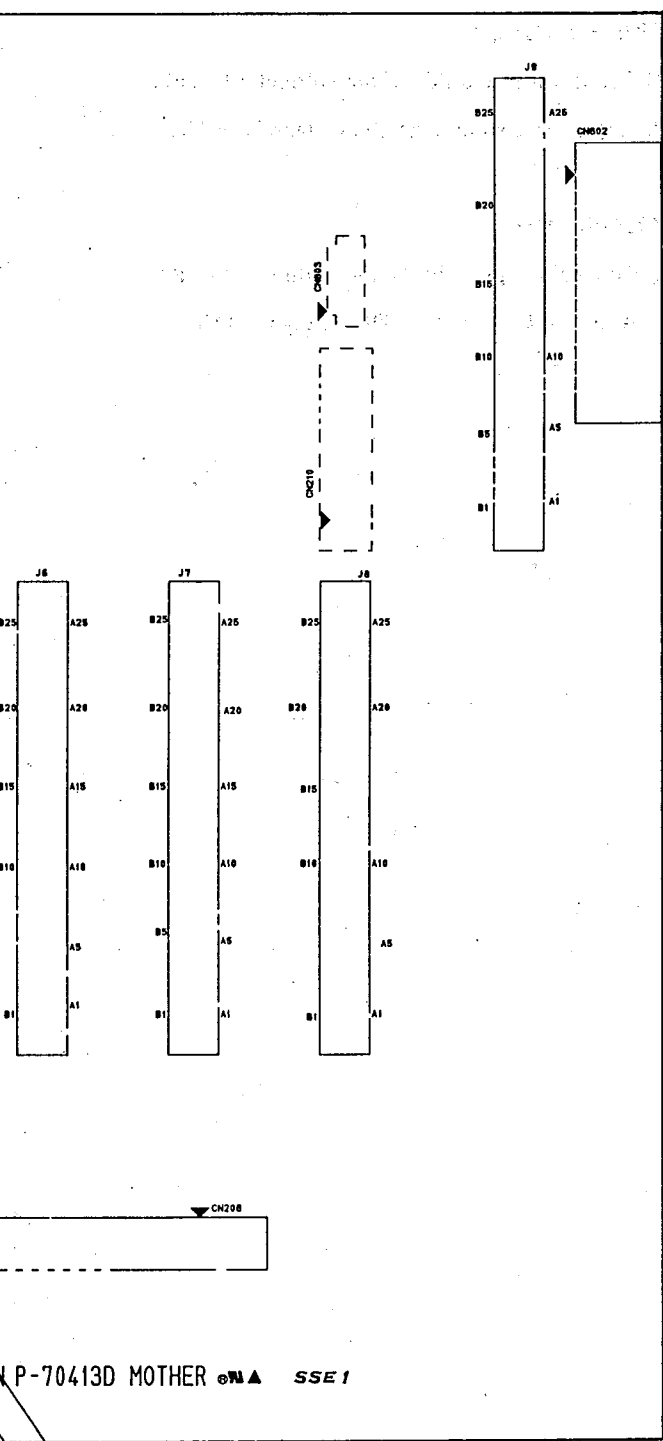
- ① Connect the plus side of the voltmeter to TP2.
- ② Adjust VR2 so that the DC voltage is -12V.

(c) VR3 (+5V ADJ)

- ① Connect the plus side of the voltmeter to TP3.
- ② Adjust VR3 so that the DC voltage is +5V.

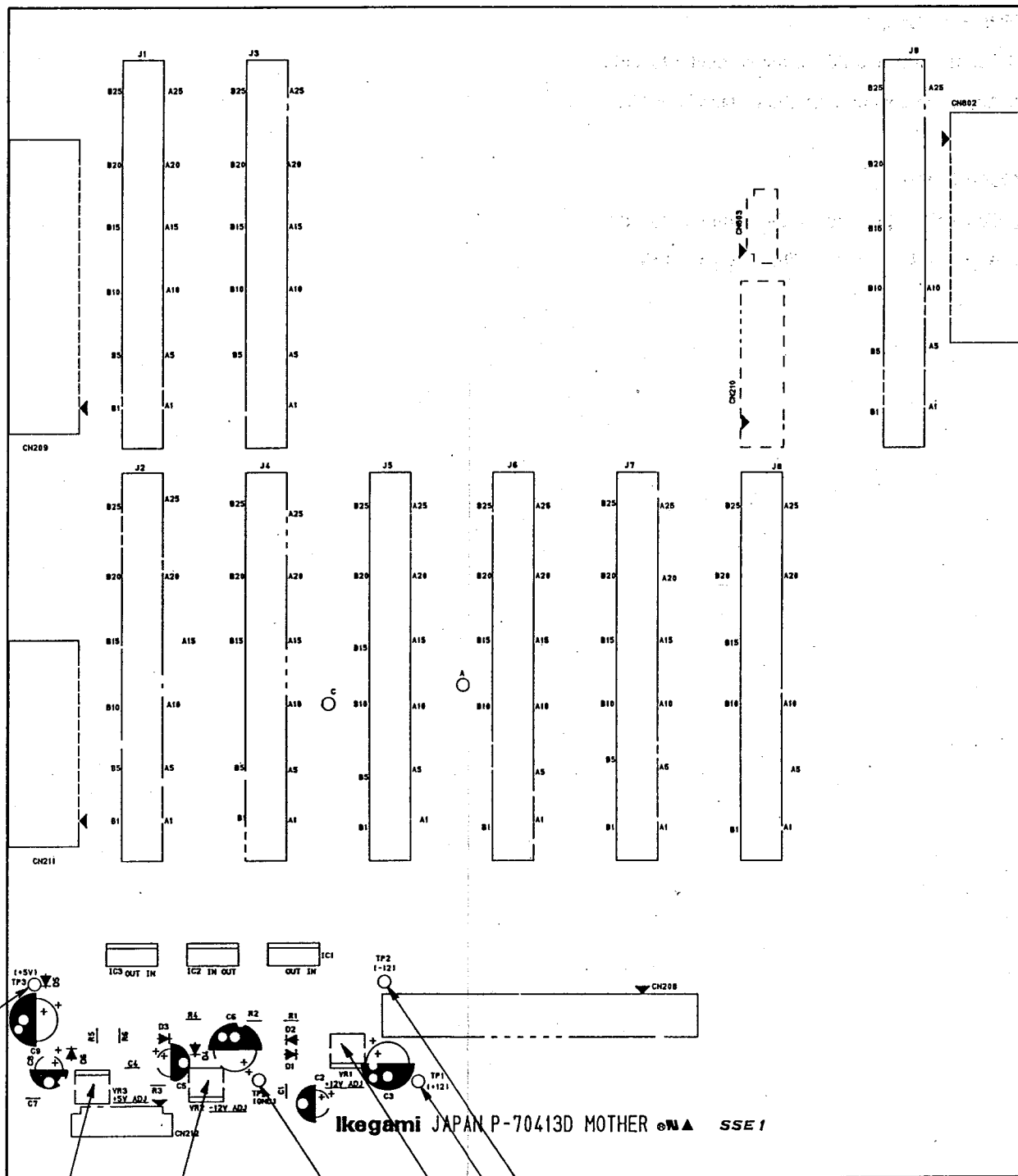
**20/30 SERIES
MOTHER BOARD
PARTS LOCATION
P-70413D**





P-70413D MOTHER SSE 1

- TP2
- TP1
- VR1 (+12V ADJ)



TP3

TP4

TP2

TP1

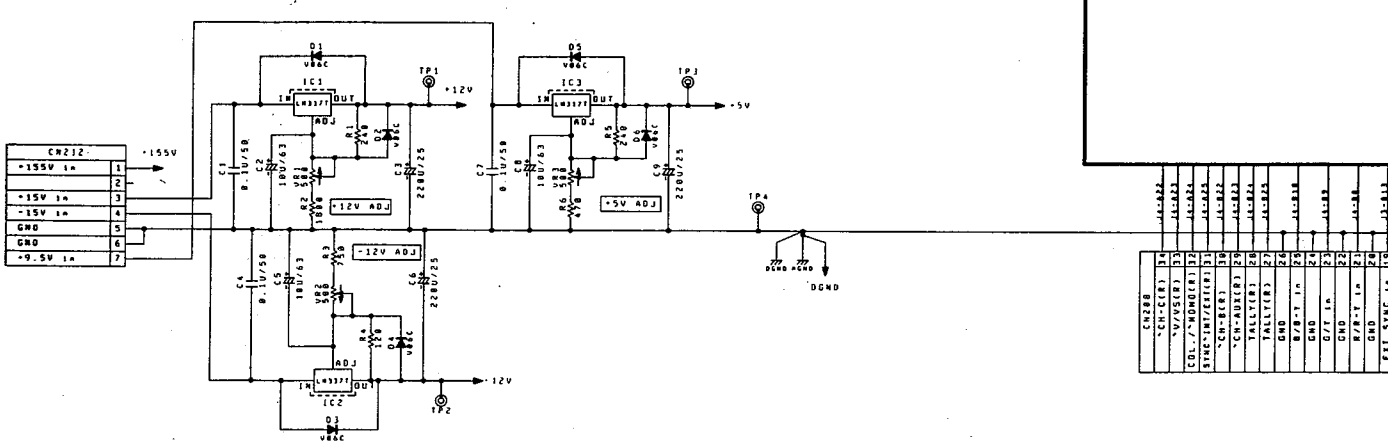
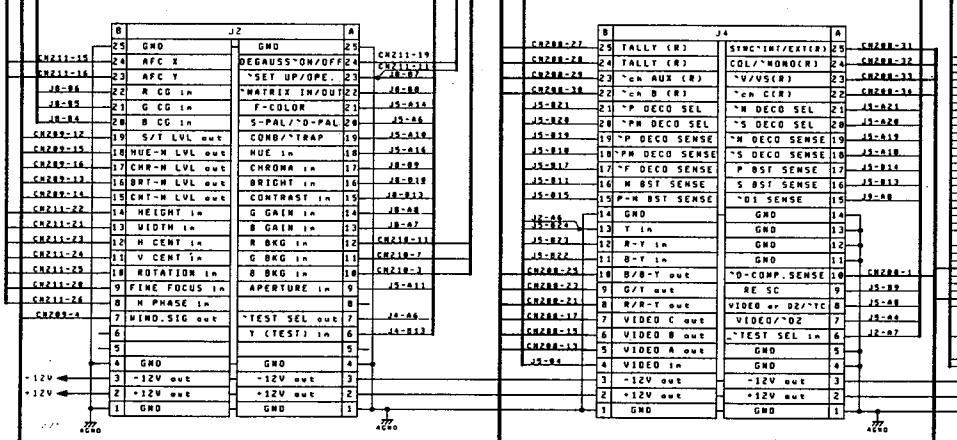
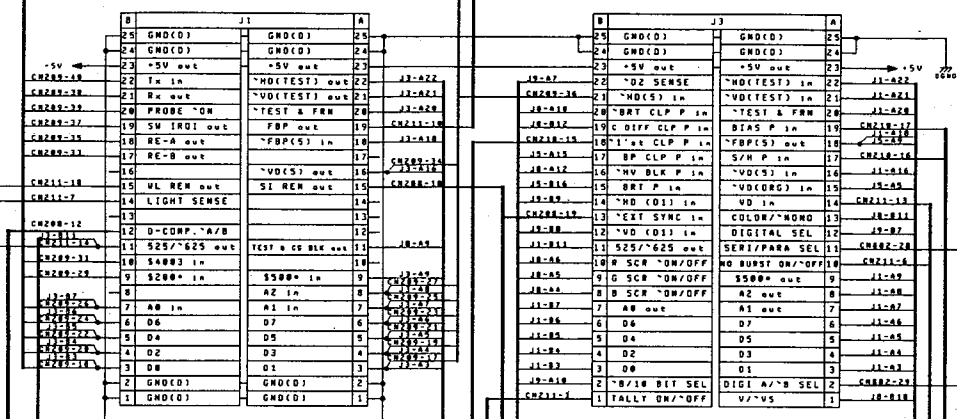
VR2(-12V ADJ)

VR3(+5V ADJ)

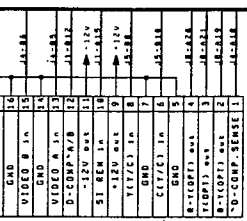
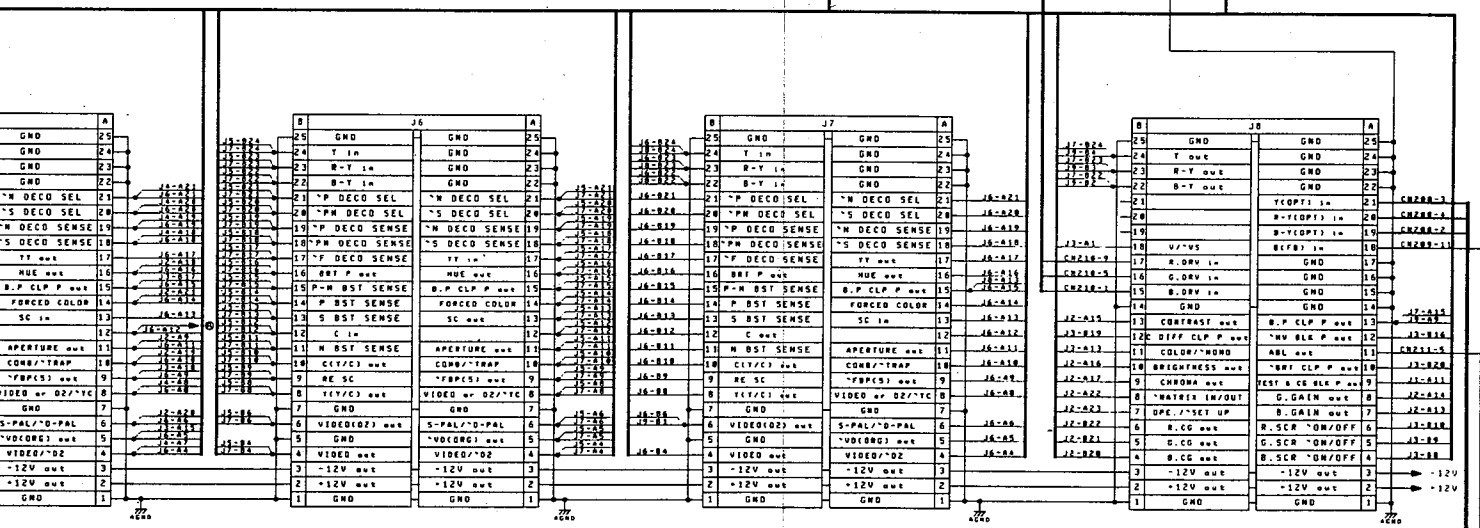
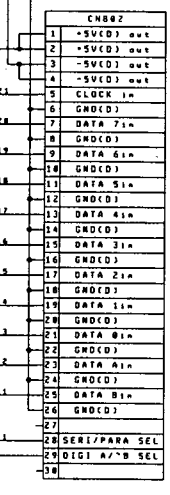
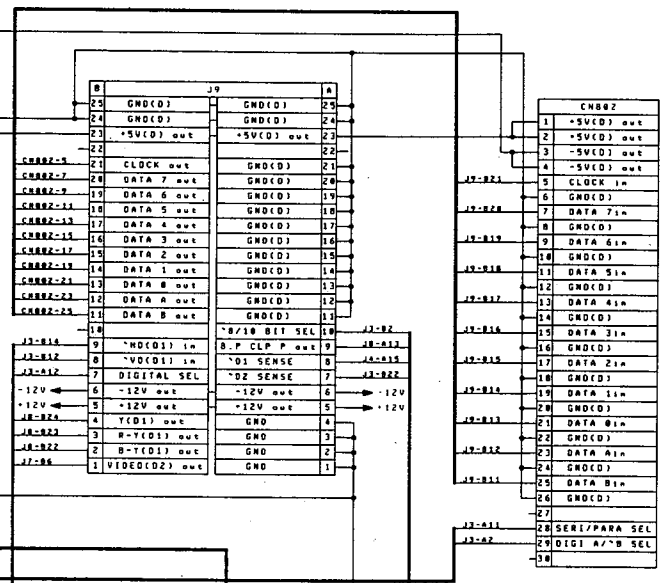
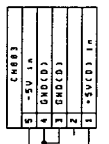
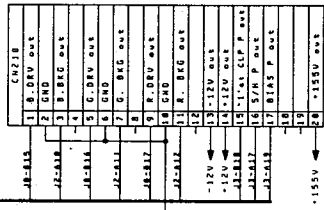
VR1(+12V ADJ)

CN209		
7a out	40	J1-222
PROBE "ON"	39	J1-222
Ra in	38	J1-221
SW IRDI in	37	J1-215
"HD(5) out	36	J1-215
RE-A in	35	J1-214
"VD(5) out	34	J1-212
RE-B in	33	J1-212
S4883 out	32	J1-214
S288* out	29	J1-28
A2 out	28	J1-28
A8 out	27	J1-27
A1 out	25	J1-27
D6	24	J1-28
D7	23	J1-28
D4	22	J1-28
D5	21	J1-28
D2	20	J1-28
D3	19	J1-28
D8	18	J1-28
D1	17	J1-28
CHM-H LVL in	16	J2-212
HUE-M LVL in	15	J2-212
CNT-M LVL in	14	J2-212
BRT-M LVL in	13	J2-212
S/T LVL in	12	J2-212
BCFB out	11	J2-212
"-5V out	9	J2-212
"-5V out	8	J2-212
"-12V out	7	J2-212
"-12V out	6	J2-212
"-12V out	5	J2-212
VIDEO SIG in	4	J2-212
GND	3	
GND	2	
GND	1	

CN211		
H PHASE out	26	J2-28
ROTATION out	25	J2-212
V. CENT out	24	J2-212
H. CENT out	23	J2-212
HEIGHT out	22	J2-214
WIDTH out	21	J2-212
FINE FOCUS	20	J2-28
DEGAUSS/ON/OFF	19	J2-224
UL RER in	18	J1-215
	17	
AFC-T	16	J2-222
AFC-X	15	J2-224
"S2S/625 out	14	J1-211
VD out	13	J2-214
GND	12	
"SET UP/OPE	11	J2-222
FBP in	10	J1-215
GND	9	
"HD(5) out	8	CN209-36
LIGHT SENSE in	7	J1-214
NO SUBST ON/OFF	6	J2-212
ABL in	5	J2-212
TALLY ON/OFF	4	J2-212
GND	3	
GND	2	
GND	1	



CN208		
"CP-C(1)"	1	J2-212
"CP-C(2)"	2	J2-212
COL. SYNC	3	J2-212
CH-ADIC(R)	4	J2-212
CH-ADIC(B)	5	J2-212
TALLY(R)	6	J2-212
GND	7	
B/R-T in	8	J2-212
D/T in	9	J2-212
R/R-T in	10	J2-212
EL. SYNC in	11	J2-212
VIDEO C in	12	J2-212
VIDEO B in	13	J2-212
VIDEO A in	14	J2-212
GND	15	



20/30 SERIES
 COLOR MONITOR
 MOTHER BOARD
 Schematic Diagram
 C1-904207A

2-2. MPU BOARD (Fixed in SLOT No.1)

(1) Outline

This board is provided for processing the digital control lines via the MOTHER BOARD from the CONTROL BOARD and generating the various test signals and character signals.

(2) Circuit Description

(a) MPU

The MPU(HD6303YF) of IC101 is an 8-bit CMOS microcomputer comprising CPU, timer, RAM (256 byte), SCI (Serial Communication Interface) and I/O on one chip.

The MPU is operated by a program memorized in the external EPROM IC105 (32kbyte). Each data is memorized in the static RAM IC106 (8kbyte).

The RAM is designed to hold data by a lithium battery BT101 when power is off.

(b) Memory, I/O map

The respective addresses sent from MPU are transformed into address control lines by the address decoder of IC108 and IC109 and these control lines latch each input/output data of the data bus. The respective addresses are mapped in the memory as shown in the table below:

Address	Latch IC	Board used	Content
\$0000 \$1FFF		MPU	S-RAM
\$2000	IC9	CONTROL	LED ON/OFF OUTPUT
\$2001	IC10		
\$2002	IC11		
\$2003	IC12		
\$2004	IC13		
\$2005	IC14		
\$2006	IC4		
\$2007	IC6	REMOTE NO. INPUT	
\$3000	IC112	MPU	CHARACTER IC CONTROL OUTPUT
\$4000	IC208		SAFE TITLE PHASE CONTROL OUTPUT
\$4001	IC210		SAFE TITLE, TEST SIGNAL SELECT OUTPUT
\$4002	IC116		VARIOUS ON/OFF OUTPUT
\$4003	IC7	CONTROL	MANUAL SWITCH DATA INPUT
\$5000	IC10	INTERFACE	VARIOUS ON/OFF OUTPUT
\$5001	IC11		VARIOUS DECODER BOARD SENSE INPUT
\$5002	IC22		A/B, COLOR/MONO SPLIT PHASE OUTPUT
\$5003	IC13		VARIOUS ON/OFF OUTPUT
\$5004	IC12		VARIOUS DECODER BURST SENSE INPUT
\$5005	IC28		PARALLEL REMOTE DATA INPUT
\$5006	IC29		VIDEO SYSTEM ON/OFF OUTPUT
\$5007	IC27	DIGITAL SYSTEM ON/OFF OUTPUT	
\$6000	IC118	MPU	D/A LOWER 8-BIT DATA OUTPUT
\$7000	IC119		D/A HIGHER 2-BIT DATA OUTPUT, CHANGE DATA
\$8000 \$FFFF			EP ROM

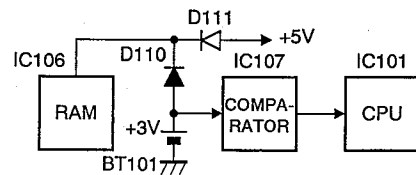
(c) RAM backup circuit

All digital control data are stored in the 8k byte static RAM (IC106) and held for about 10 years by a lithium battery (BT101).

When power to the monitor is on, D110 is off and D111 is on to supply 5V power to RAM.

When power to the monitor is off, D110 is on and D111 is off to supply 3V power (BT101) to RAM and the memory is held.

Output of IC107 (Comparator) is "L", when the battery voltage is lower than 2.4V. The data is read by CPU and given as a message by blinking each screen LED on the front panel.

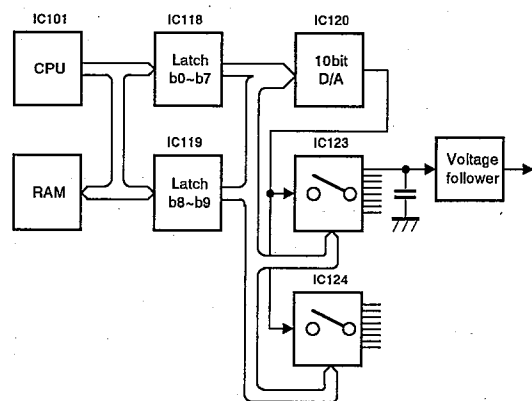


(d) TV display generator and SAFE TITLE color control

The TV display generator (IC113) operated by HD and VD outputs each character signal of R, G and B and a background signal by control with data from CPU. DC level that is preset with the SAFE TITLE LEVEL control in the pull-out panel controls 8 colors at IC114 by data sent from CPU. A character signal and SAFE TITLE level are selected with the analog switch (IC115).

(e) D/A, S/H circuit

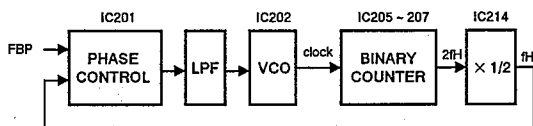
The lower 8-bit and higher 2-bit data are sent from RAM in twice. The respective data is latched at IC118 and IC119 and input to the D/A converter IC120 as 10 bit data. Since data are continuously sent, the analog multiplexers IC123 and IC124 are provided as circuits to separate these data. A sample & hold circuit is made by the ICs and the holding capacitors C139 ~ 155 to control each circuit by DC.



(f) SAFE TITLE PLL circuit

The flyback pulse sent from DEF BOARD and HD that is made by using as a clock the pulse which is oscillating at VCO (IC202) are subject to phase comparison at Pins ① and ③ of IC201.

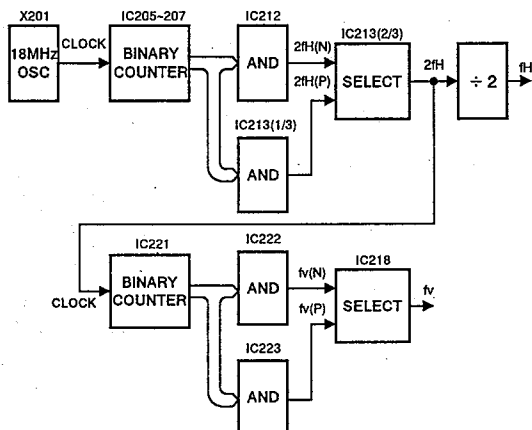
The phase difference passes through the low-pass filter to control the oscillation of VCO of IC202.



(g) Horizontal and vertical pulse generating circuit

A crystal resonator (X201) of 18MHz is used as a base clock. The binary counter of IC205, IC206 and IC207 is operated by the crystal resonator. The AND circuit of IC212 for NTSC or of IC213 (1/3) for PAL serves to make a twofold horizontal pulse (2fH). The 2fH horizontal pulse is halved by D-flip-flop of IC214(1/2) to make a horizontal pulse fH.

To generate a vertical pulse, the 2fH pulse is used as a clock to operate the binary counter of IC221. A vertical pulse (fv) is made by the AND circuit of IC222 for NTSC or of IC223 for PAL.



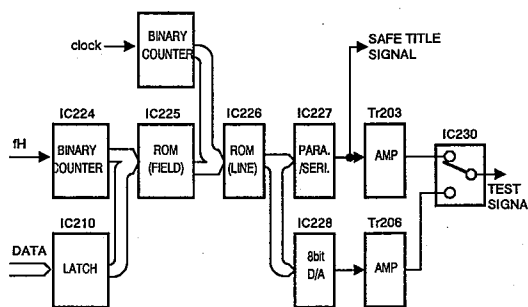
(h) TEST, SAFE TITLE signal generating circuit

The test signal and the safe title signal are made by reading data of ROM (IC225, 226).

The EPROM IC226 serves to store pattern data equivalent to one line (64 byte) in each address, which is controlled by IC225. IC225 stores 27 kinds of address data on the vertical direction at a unit of one field (512 byte) to read the pattern of IC226.

Each data is selected by using addresses A9 ~ A13. The lower A0 ~ A8 addresses are counted up line by line with the binary counter of IC224 which uses HD as the clock.

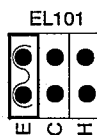
The signal data output from IC226 is divided into a signal that is subject to analog conversion by IC228 (8 bit D/A converter) and a signal that is subject to parallel-serial conversion for 8-bit digital data by IC227.



(3) Adjustment Procedure

(a) EL101

- ① Set the short bar at the connector EL101 according to the phosphor of the CRT as follows.



Destination	
H	Japan
C	Ereas of NTSC format except for Japan
E	Ereas of PAL format

(b) VC201(TEST FREQUENCY)

- ① Press the **TEST** switch to select a TEST signal.
- ② Connect the frequency counter to TP201.
- ③ Adjust VC201 so that the frequency is 18.000MHz.

(c) VR201(TEST-S LVL)

- ① Press the **TEST** switch to select the "FLAT FIELD" signal.
- ② Connect the probe to TP205.
- ③ Adjust VR201 so that the level is 0.64Vp-p.



(d) VR202(DA LVL)

- ① Press the **TEST** switch to select the "WINDOW" signal.

② Connect the probe to TP205.

③ Adjust VR202 so that the level is 1.28Vp-p.



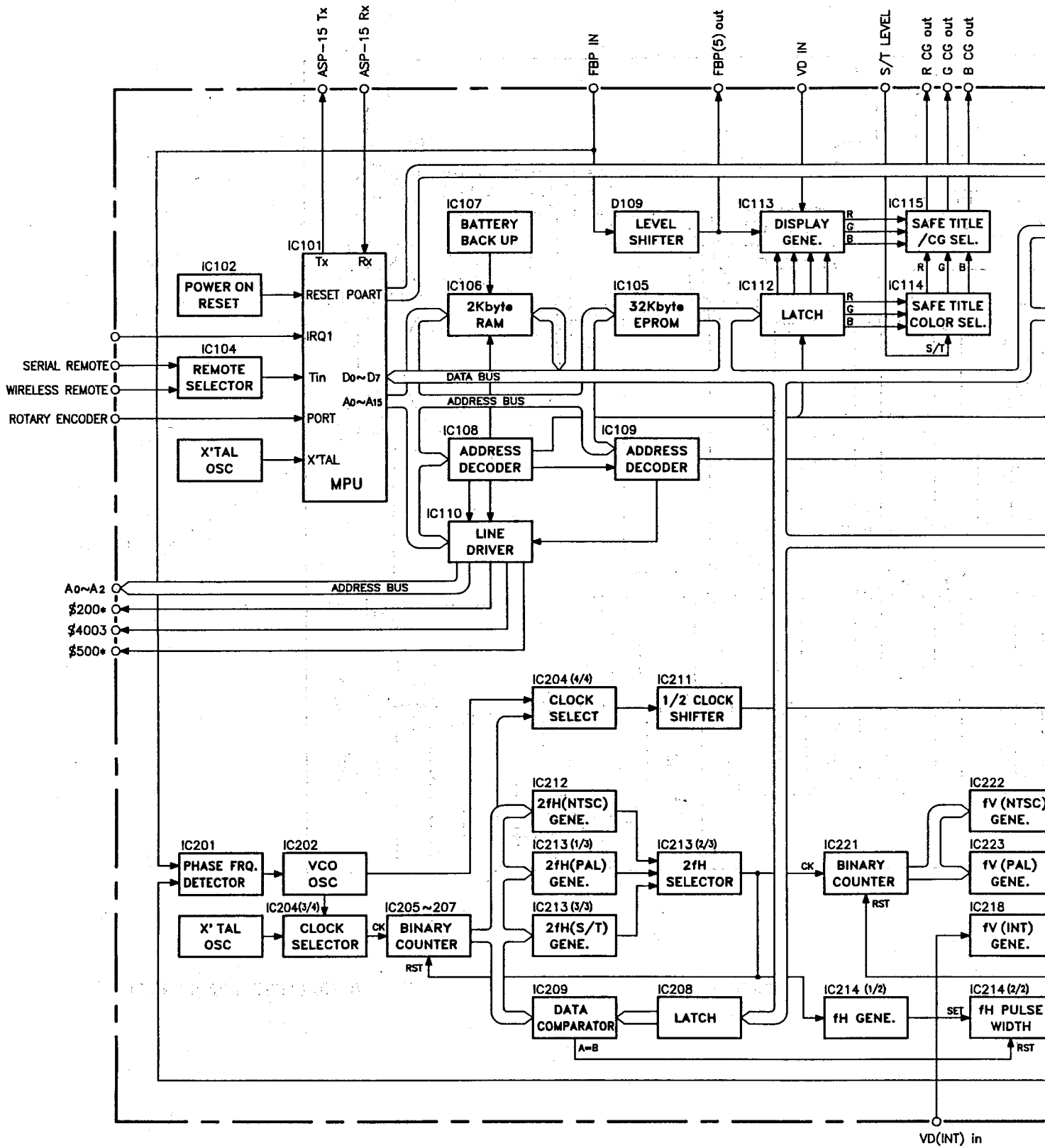
(e) *VR203(WIND. LVL)*

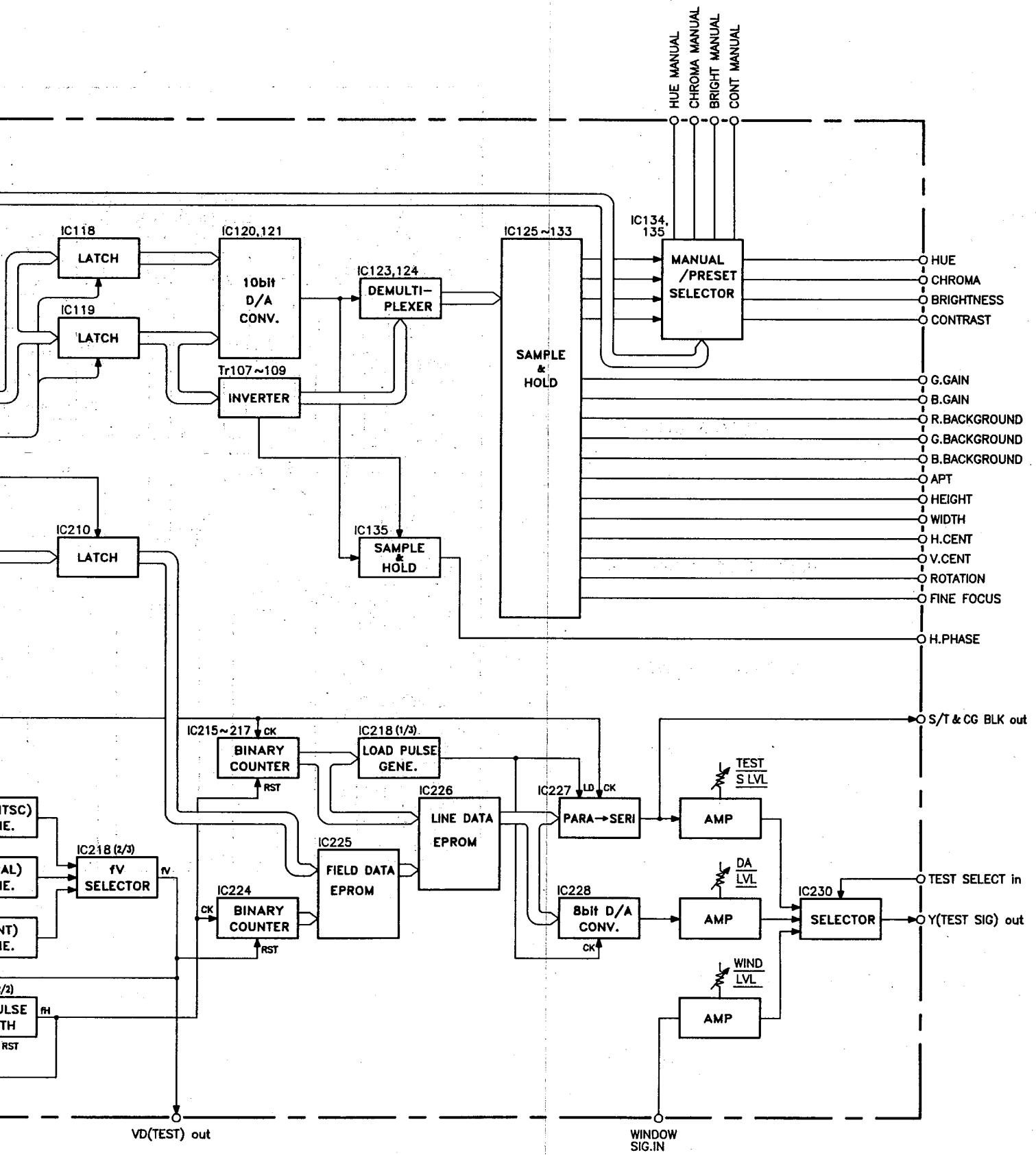
① Connect the AUTO SET UP PROBE(ASP-15) to the connector on the pull-out panel.

② Connect the probe to TP205.

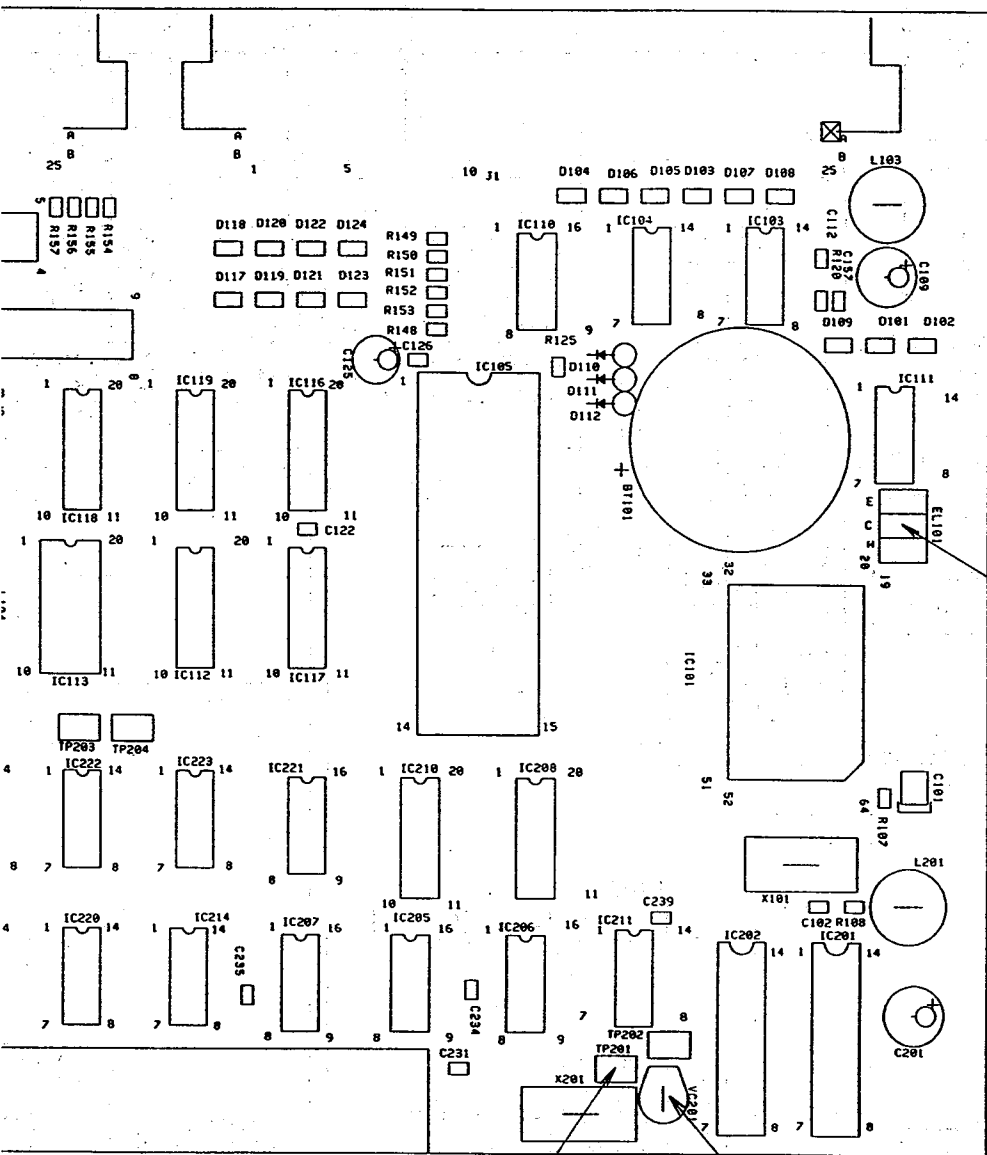
③ Adjust VR203 so that the level is 1.28Vp-p.







20/30 SERIES
COLOR MONITOR
MPU BOARD
Block Diagram
C2-904338

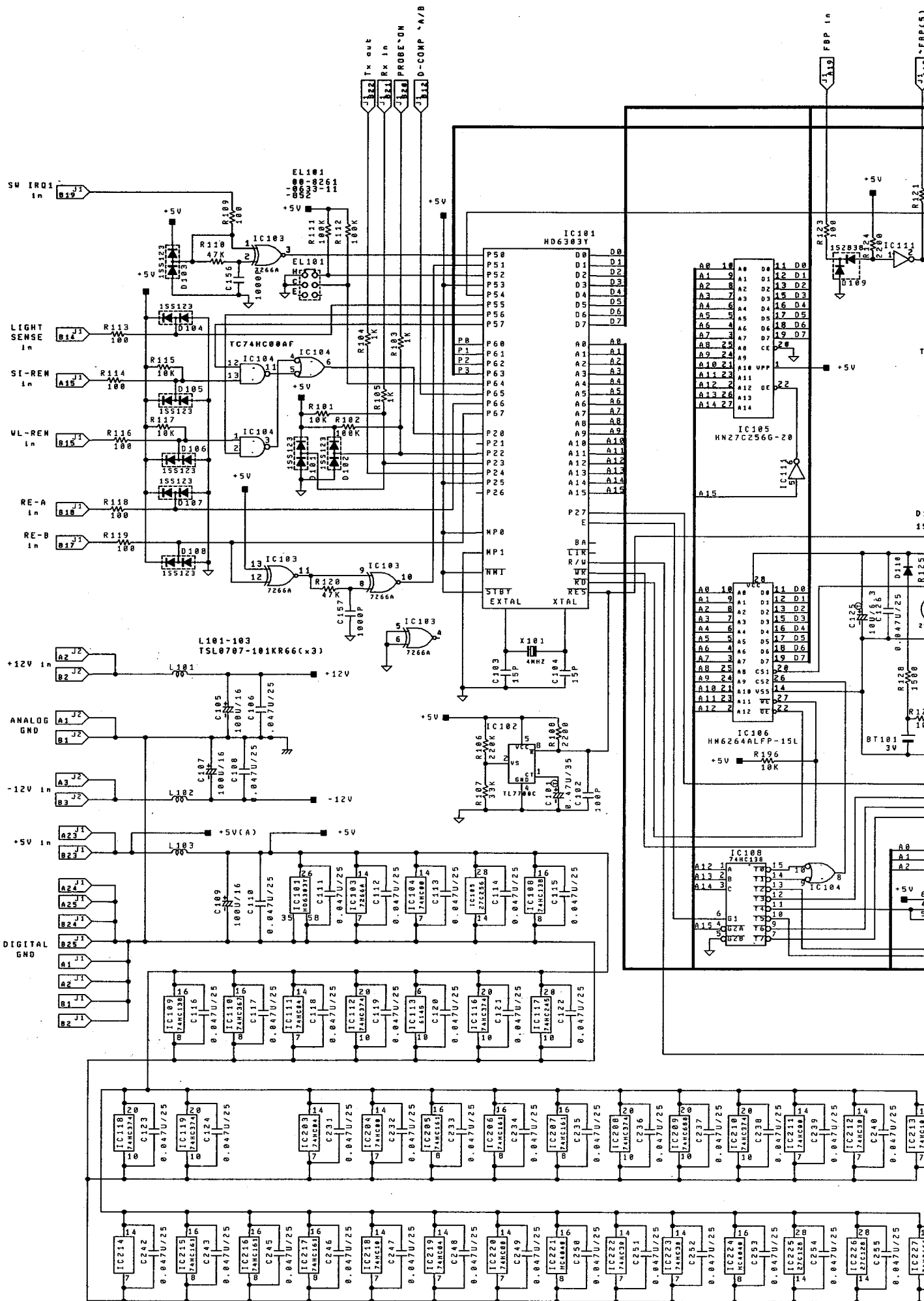


EL101

TP201

VC201 (TEST FREQUENCY)

20/30 SERIES
 MPU BOARD
 PARTS LOCATION
 PC1Y29



SW IRQ1
In

LIGHT
SENSE
In

SI-REM
In

WL-REM
In

RE-A
In

RE-B
In

+12V
In

ANALOG
GND

-12V
In

+5V
In

DIGITAL
GND

Tx out
Rx in
PROBE*OH
D-COMP *A/B

F8P In

F8P (S)

EL101

IC103

TC74HC00AF

IC104

IC103

IC103

IC103

IC103

IC103

IC103

IC103

IC103

IC103

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IC103

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IC103

IC103

IC103

IC103

IC103

IC103

IC103

IC103

IC101
HD6303Y

IC102
TL74C02

IC103
74HC03

IC104
74HC04

IC105
HN27C2566-20

IC106
HM6264ALFP-15L

IC108
74HC138

IC109
74HC138

IC110
74HC138

IC111
74HC138

IC112
74HC138

IC113
74HC138

IC114
74HC138

IC115
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IC116
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IC117
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IC118
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IC119
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IC120
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IC121
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IC122
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IC123
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IC124
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IC200
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IC201
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IC202
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IC223
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IC224
74HC138

IC225
74HC138

IC226
74HC138

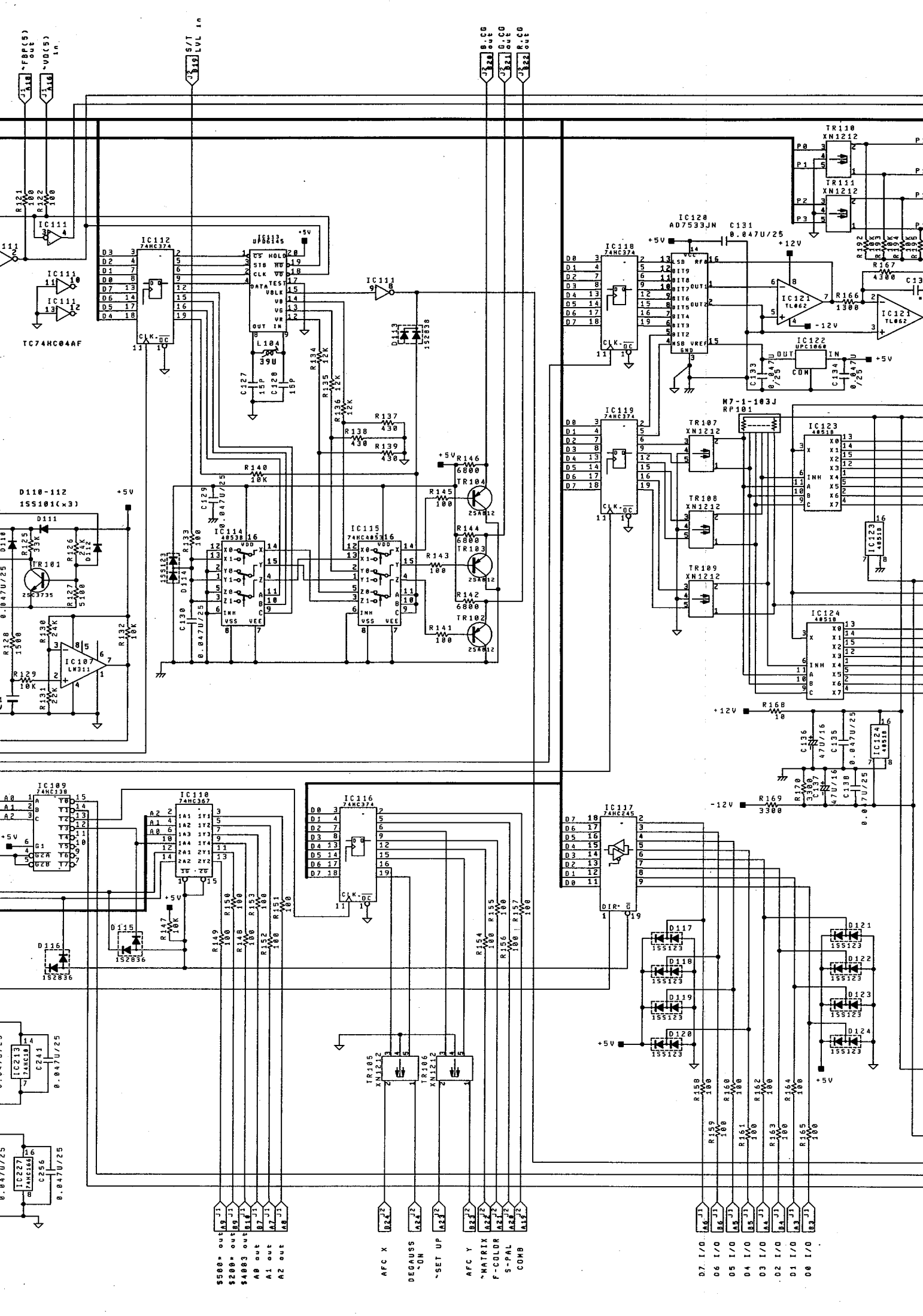
IC227
74HC138

IC228
74HC138

IC229
74HC138

IC230
74HC138

IC231
74HC138



FBP(S) out
 ~V(D5) in

IC111
 IC111
 IC111
 IC111

D110-112
 155101(x3)

IC109
 74HC138

IC213
 74HC125

5500* out
 5200* out
 5400* out
 5800* out
 A8 out
 A1 out
 A2 out

IC112
 74HC374

IC114
 74HC08

IC110
 74HC374

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC113
 74HC05

IC115
 74HC04

IC116
 74HC374

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC114
 74HC08

IC115
 74HC04

IC116
 74HC374

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC115
 74HC04

IC116
 74HC374

IC117
 74HC125

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC116
 74HC374

IC117
 74HC125

IC118
 74HC374

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC117
 74HC125

IC118
 74HC374

IC119
 74HC374

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC118
 74HC374

IC119
 74HC374

IC120
 74HC125

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC119
 74HC374

IC120
 74HC125

IC121
 TL062

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC120
 74HC125

IC121
 TL062

IC122
 UP1069

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC121
 TL062

IC122
 UP1069

IC123
 4558

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC122
 UP1069

IC123
 4558

IC124
 4558

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC123
 4558

IC124
 4558

IC125
 4558

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC124
 4558

IC125
 4558

IC126
 4558

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC125
 4558

IC126
 4558

IC127
 4558

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC126
 4558

IC127
 4558

IC128
 AD7533JN

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC127
 4558

IC128
 AD7533JN

IC129
 4558

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

IC128
 AD7533JN

IC129
 4558

IC130
 4558

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB

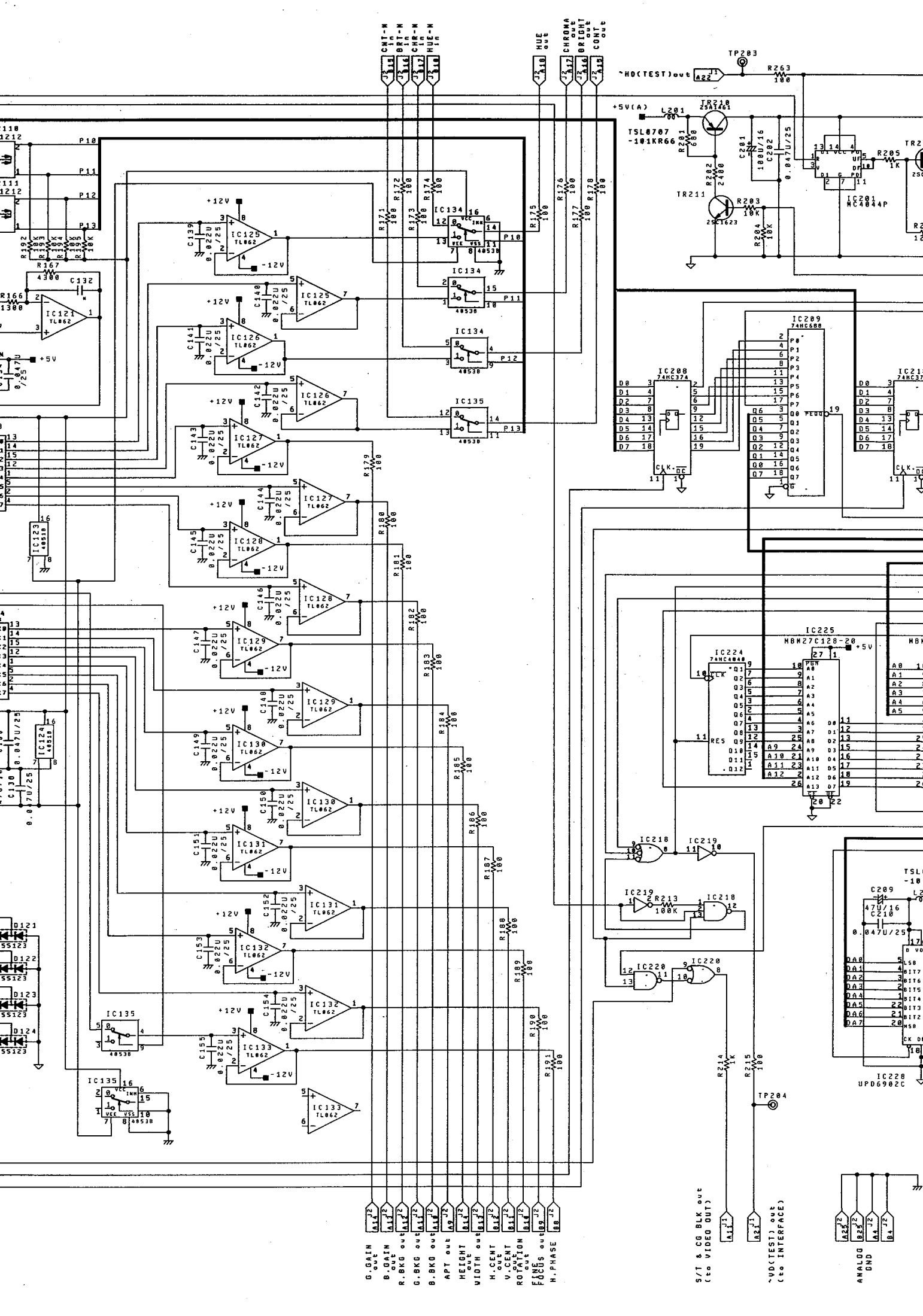
IC129
 4558

IC130
 4558

IC131
 0.847U/25

IC213
 74HC125

AFC X
 DEGAUSS
 *ON
 *SET UP
 AFC Y
 *MATRIX
 F-COLOR
 S-PAL
 COMB



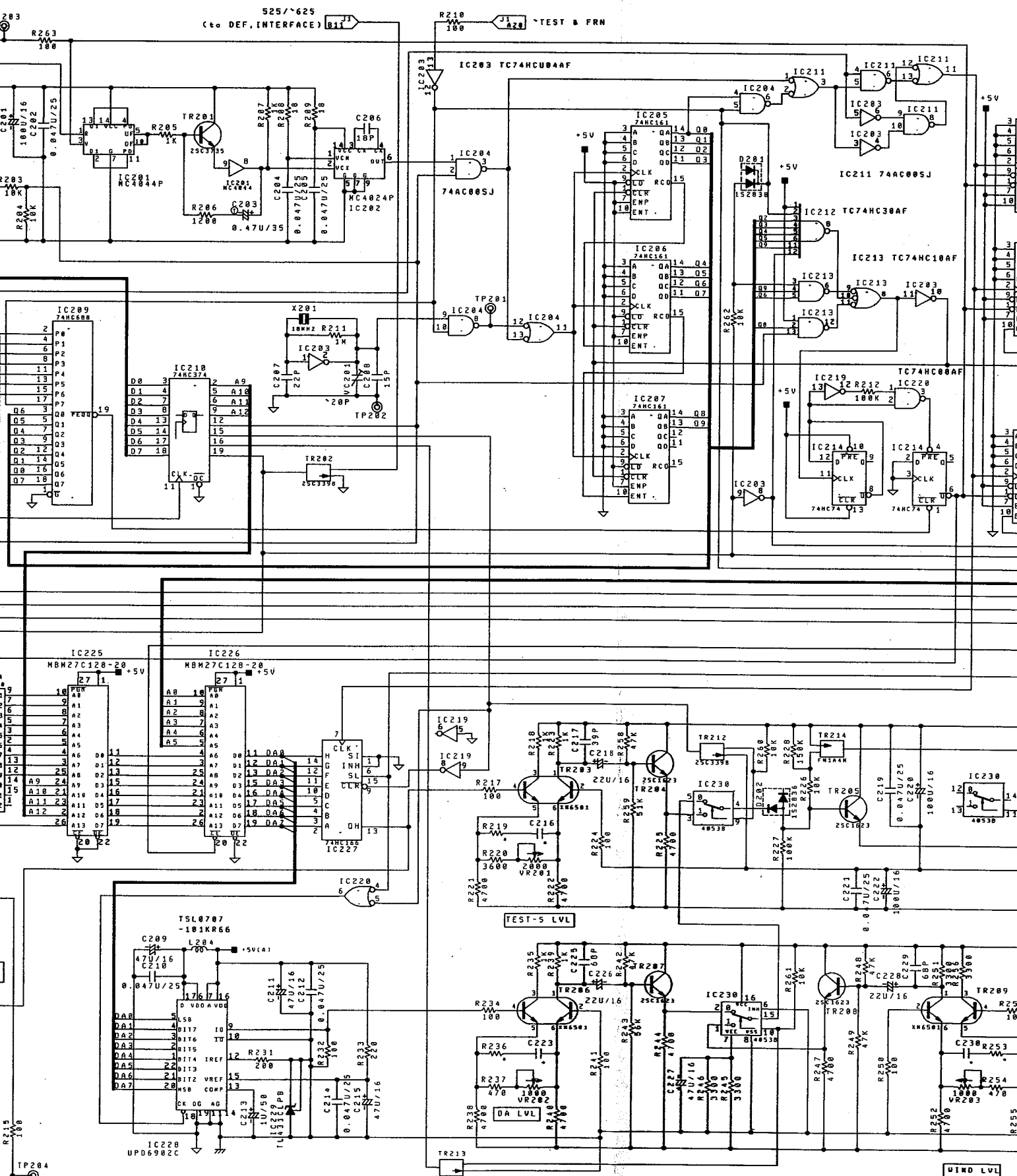
- 12 CH1-M
- 13 CH1-H
- 14 CH2-M
- 15 CH2-H
- 16 CH3-M
- 17 CH3-H
- 18 CH4-M
- 19 CH4-H

- 20 HUE
- 21 GUE
- 22 CHROMA
- 23 OUT
- 24 BRIGHT
- 25 OUT
- 26 CONTR
- 27 OUT

- 0 G.GAIN
- 1 GAIN
- 2 R.BKG out
- 3 G.BKG out
- 4 B.BKG out
- 5 APT out
- 6 HEIGHT
- 7 WIDTH out
- 8 H.CENT
- 9 V.CENT
- 10 ROTATION
- 11 BKG out
- 12 FINE
- 13 FOCUS out
- 14 H.PHASE

- 15 S/T & CG BLK out
- 16 (see VIDEO OUT)
- 17 VD(TESt) out
- 18 (see INTERFACE)
- 19 ANALOG
- 20 GND

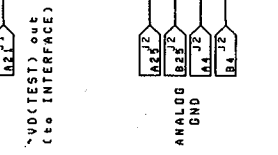
- 21 A2
- 22 B2
- 23 A3
- 24 B3



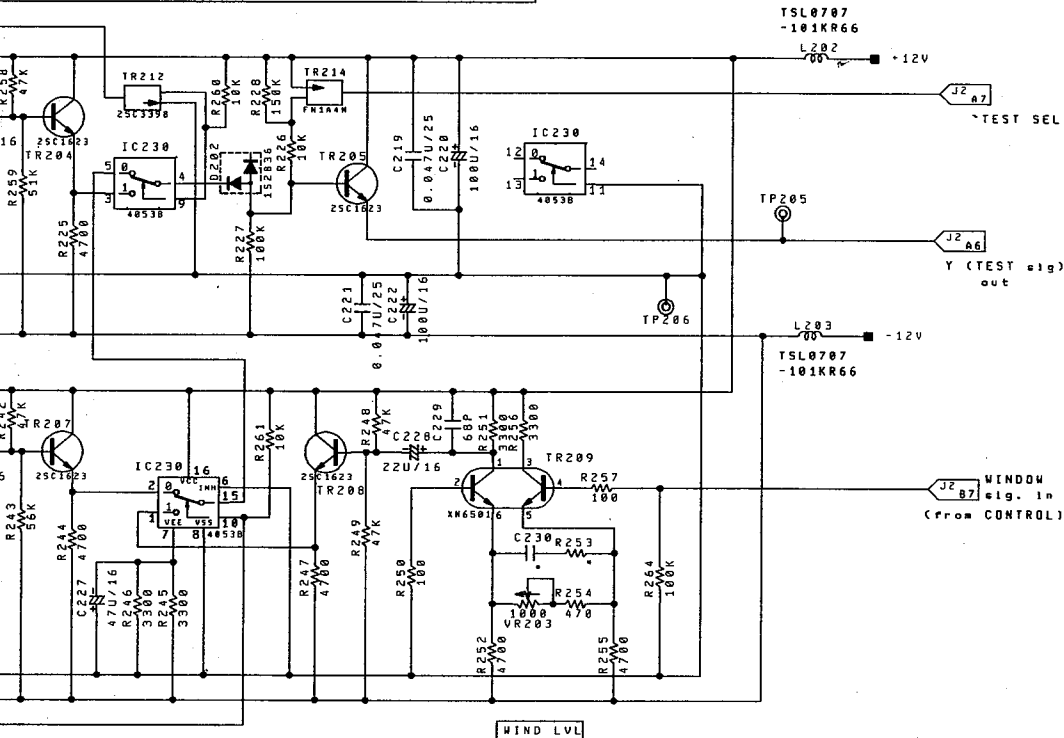
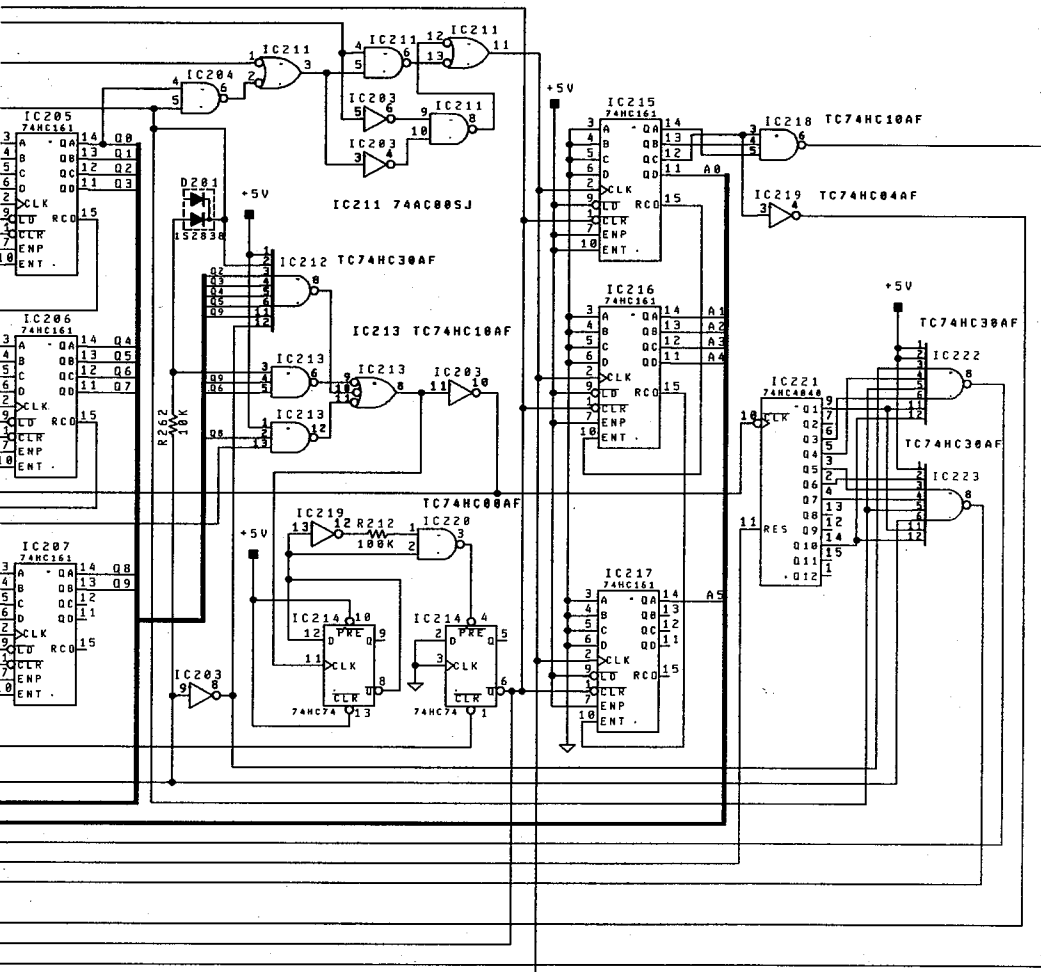
LAST NO.	LOST NO.
IC 135 230	R216, 229, 230
R 196 264	C244
C 157 256	
Tr 111 214	
D 124 202	
L 104 204	

NOTE:

1. All resistors are in ohms 5% (parts marked F:1%), 1/10 watt unless otherwise specified.
2. All capacitors are in farads, 300V unless otherwise specified.
3. All inductors are in henry unless otherwise specified.
4. Waveforms are taken with a color bar signal input.
5. Parts marked * are factory selected value.
6. Parts marked * are critical components for X-radiation.



5V(D, TEST) & E (to INTERFACE)



re in ohms 5% (parts marked
 unless otherwise specified.
 are in farads, 300V unless
 fied.
 re in henry unless otherwise
 taken with a color bar signal
 are factory selected value.
 are critical components

20/30 SERIES
COLOR MONITOR
MPU BOARD
Schematic Diagram
C11-904374

2-3. INTERFACE BOARD (Fixed in SLOT No.2)

(1) Outline

In this board, there are functions of selecting one of three composite video input channels, converting the AUX input signals (RGB or YPBPr) to output YPBPr signals, detecting boards inserted in the SLOT section, and creating various pulses.

(2) Circuit Description

(a) Interface system

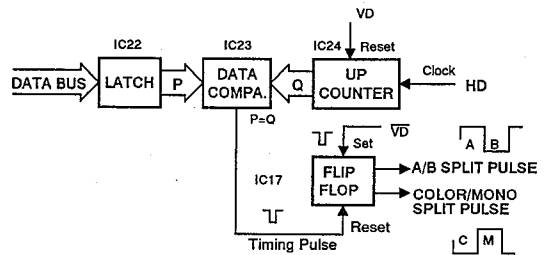
① Data I/O circuit

Three address buses of A0 ~ A2 are divided in to eight addresses (\$5000~\$5007) by the IC9 address decoder IC. Eight address lines latch ICs 10, 11, 12, 13, 22, 27, 28 and 29 respectively and they function to receive and transmit data via the CPU (IC101) and data bus in the MPU BOARD.

- **IC10** ... Latches the data from the CPU and controls various selections.
- **IC11** ... When any decoder board or digital board has been set, the input to this IC is considered as "L" and the data is transferred to the CPU.
- **IC12** ... The output from the burst detection circuit in the decoder board is inputted to this IC and then, transferred to the CPU. The CPU reads which is the "H" among the four control lines, and identifies the system among NTSC, PAL-B, PAL-M and SECAM and then, select a decoder board suitable for the system.
- **IC13** ... Latches the data from the CPU and controls various selections.
- **IC22** ... Outputs the data on the screen split position for the COLOR/MONO and A/B SPLIT.
- **IC27** ... Latches the data from the CPU and controls various selections.
- **IC28** ... When the PARALLEL REMOTE mode is selected, the data from the remote connector on the rear are inputted to this IC and transferred to the CPU.
- **IC29** ... Latches the data from the CPU and controls selection of input channels.

② COLOR/MONO and A/B SPLIT circuits

Splitting is accomplished by selecting the COLOR/MONO or ch. A/B control lines at the intended position on the scanning line. The timing pulse for selection is generated by the IC23 data comparator and outputted from the No.19 pin. The data from the CPU and the IC24 UP counter data which uses HD as a clock are compared with each other in the IC23 and when the data agree with each other, an active "L" is outputted from the No.19 pin.



(b) Pulse system

① Synchronous separation circuit

The Y signal, the HD signal with the D1 inputted, and the EXT SYNC signal are inputted to the IC1 (analog switch). One of them is selected and outputted from IC1.

The selected signal is amplified in the IC2 and is applied to the inverting input terminal of the IC3 comparator. On the other hand, a DC signal which has been attained by rectifying the negative portion of the selected synchronizing signal is applied to the non-inverting input terminal of the IC3 comparator. As a result, a positive synchronizing signal is outputted from the No.1 pin of IC3.

② Synchronizing signal detection circuit

The synchronizing signal outputted from the IC3 No. 1 pin undergoes peak holding by the D4 and C12 and then, inputted to the No.5 pin of the IC3 comparator. As a result, if a synchronizing signal has been applied to the the IC3 No.7 output, "H" will be detected and if not, "L" will be detected. When "L" is detected, the IC5 (1/3) analog switch setting is changed to the position corresponding to the free-run HD pulse which is generated in the MPU BOARD.

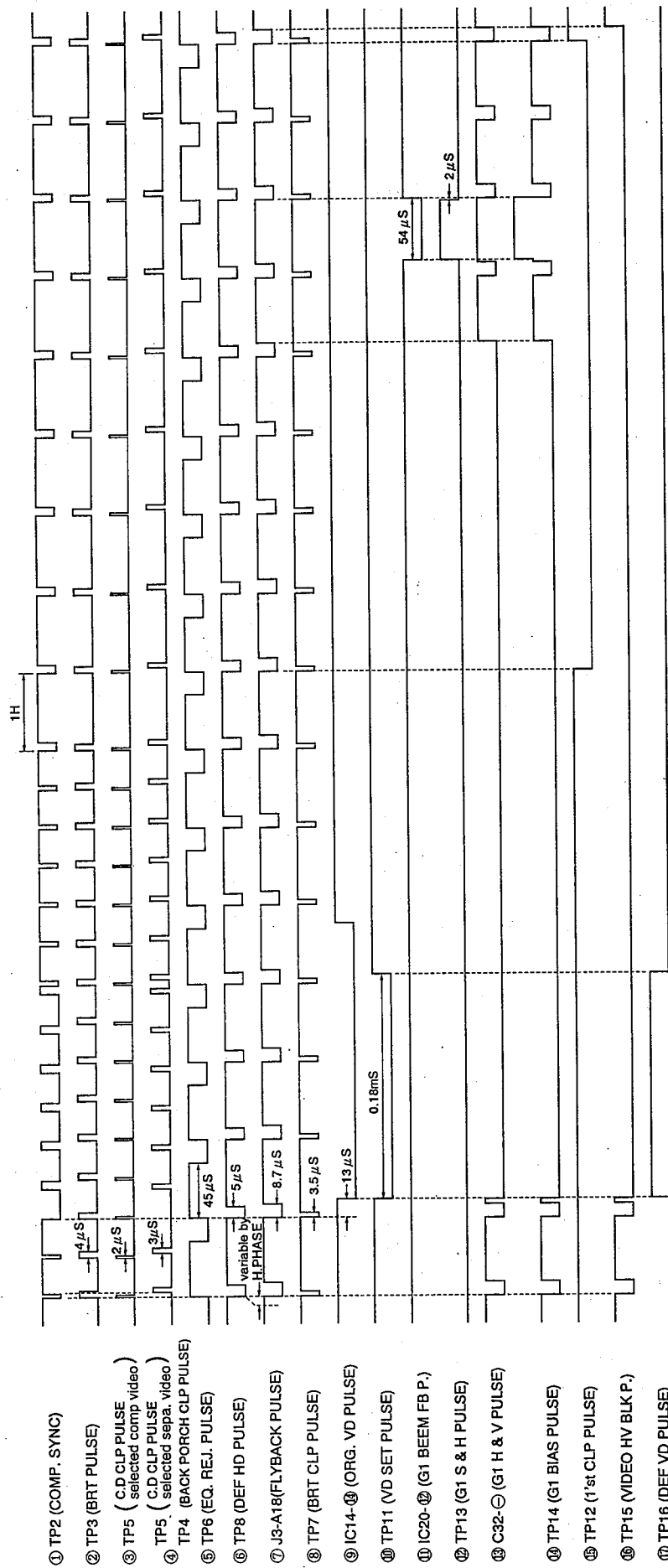


Fig 2.3 Timing chart

③ Horizontal pulse generating circuit

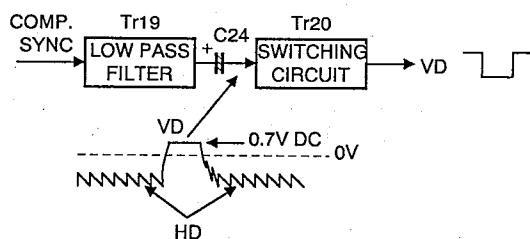
The negative composite synchronizing signal outputted from the IC5 No.14 pin is inputted to the IC6 ~ IC 8 monostable multivibrators. If it is inputted to "A" of the IC, it will be triggered at the rising edge and if inputted to "B", it will be triggered at the falling edge and, in both cases, it will be outputted from "Q" (positive polarity) or "Q̄" (negative polarity) at the pulse width determined by the time constant of the CR which is attached to the outside of the IC. For the phase of the pulse, refer to the timing chart shown in Fig. 2-3.

④ Equalizing pulse eliminating circuit

To eliminate the equalizing pulse which has been added to the composite synchronizing signal, the "Q" output width of the IC7 (2/2) monostable multivibrator is set to the width equivalent to two-third of 1H (Fig. 2-3 ⑤) and the output pulse is fed back to the "A" input.

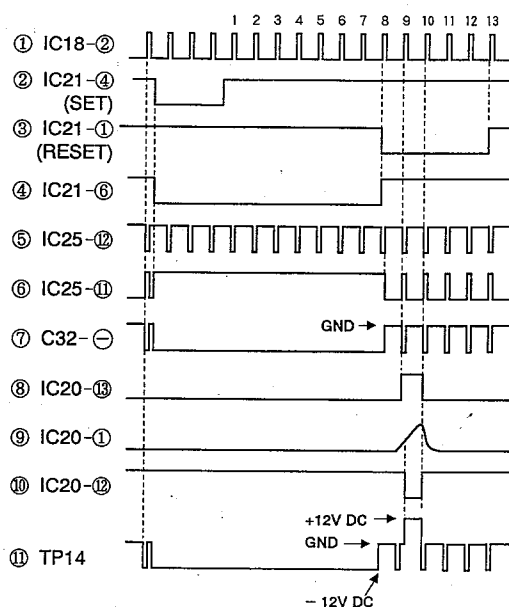
⑤ Vertical pulse generating circuit

The positive composite synchronizing signal which is outputted from the IC3 No.1 pin passes the low-pass filter consisting of R65, R66, R67, R68, C21, C22 and C23 to reduce the horizontal synchronizing signal component level and then, it becomes a complete vertical synchronizing signal (VD) after passing through the switching circuit which is clamping the VD peak and which consists of C24, R70, R71 and Tr20.



The vertical synchronizing signal outputted from the Tr20 collector, the test signal VD and the VD for D1 input are inputted to the analog switch IC14 (1/3)(2/3), respectively and any one of them is outputted from the IC14 No.14 pin. Using this pulse as a trigger, the pulse width is changed by the IC15 and IC16 monostable multivibrators to become a vertical pulse.

⑥ G1 BIAS pulse generating circuit



The G1 BIAS pulse is produced on the basis of a counter and the action of the counter is as given below. The VD pulse (the waveform ② in the above Fig.) outputted from the IC16 No.9 pin is inputted to the clear terminal of the IC18 counter and resets the counter. Counting up is accomplished by using the flyback pulse (① in the above Fig.) which is transmitted from the DEF BOARD via the MPU BOARD as a clock.

The HV blanking pulse (⑦ in the above Fig.) which is a component of the G1 BIAS pulse is produced in the following procedure. When the pulse ② in the above Fig. is inputted to the set terminal of the IC21 (2/2) RS flip flop and the pulse ③ in the above Fig. produced in the IC18 counter is inputted to the reset terminal, the V blanking pulse (④ in the above Fig.) is produced. This pulse is then mixed with the flyback pulse (⑤ in the above Fig.) in the IC25 (4/4) NAND circuit to become the pulse ⑦.

The BIAS pulse (⑩ in the above Fig.) which is a component of the G1 BIAS pulse is produced in the following procedure. The pulse (③ in the above Fig.) produced in the IC18 counter and the pulse (⑨ in the above Fig.) produced by integrating this pulse by the VR10, R87 and C29 are mixed with each other in the IC20 (3/3) NAND circuit to become the pulse ⑩.

The HV blanking pulse (⑥ in the above Fig.) outputted from the IC25 No.11 pin is inverted in the Tr28 and is clamped to the GND by the D20 (⑦ in the above Fig.) The pulse (⑩ in the above Fig.) is mixed with this pulse by the Tr29

and D22 to produce the G1 BIAS pulse (① in the Fig. of previous page)

⑦ 50/60Hz detection circuit

If a 50Hz signal is detected in the IC26, "H" will be outputted from the No.6 pin and if a 60Hz signal is detected, "L" will be outputted. This output signal is transmitted to the CPU via the data bus. The CPU determines whether the deflection system is set at 50Hz or 60Hz according to the control line and the SYSTEM setting of the MENU.

(c) Signal system

① VIDEO select circuit

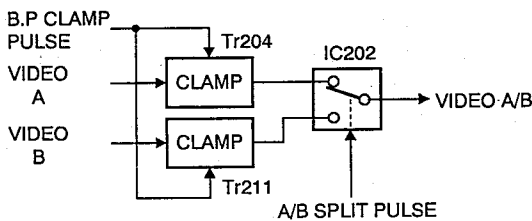
The data, which selects the channel of the video input, transmitted from the CPU is latched in the IC29 and then, inputted to the base of the Tr202, Tr209 and Tr216.

When the transistor has been turned on, a backward bias is actuated on the diodes (D201, 202 and 203) connected to the collector to stop the signal. When the transistor has been turned off, a forward bias is actuated on the diodes to pass the signal.

Therefore, any one of the three transistors is turned off to transmit one signal to the cathode of the diode. The selected signal is inputted to the IC201 No.4 pin via the Tr206 emitter follower.

② A/B SPLIT circuit

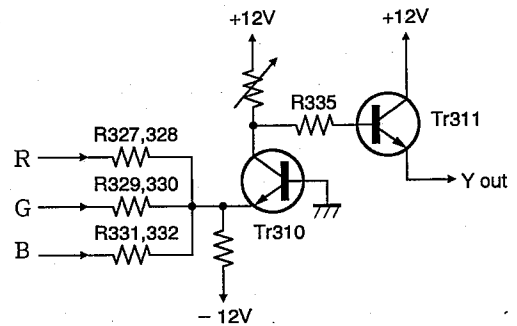
The signals inputted to the VIDEO A and VIDEO B are clamped by the Tr204 and Tr211, respectively and inputted to the IC202 No. 2 and 7 pins. These two signals are switched by the A/B SPLIT pulse inputted to the IC202 No.3 pin and the selected signal is inputted to the IC201 No.15 pin via the Tr213 emitter follower.



③ RGB input matrix circuit

The R, G and B signals are converted to Y, R-Y and B-Y signals, respectively when RGB signals are inputted and then, outputted from the INTERFACE BOARD. The converted Y signal is produced by passing the R, G and B signals through the resistance matrixes R327 through R332, respectively. It is adjusted by the Tr310 amplifier so that its level may be the same as that obtained when the YPBPR signals are inputted

and then, inputted to the IC301 analog switch via the Tr311 emitter follower.



On the other hand, the converted R-Y and B-Y signals are produced when the Y signal which is generated in the circuit shown above is subtracted from the R signal and B signal in the Tr302 and Tr317 differential amplifiers, respectively.

The Y, R-Y and B-Y signals so produced are inputted to the IC301. Also, the Y, R-Y and B-Y signals for YPBPR input are inputted to the IC301. They are selected by the IC301 analog switch, and inputted to the Tr305, Tr313 and Tr320 emitter followers.

When the AUX switch is set to the selected position ("ON"), the Tr322 transistor is turned on and, at the same time, Tr306, Tr314 and Tr321 are also turned on. A forward bias is actuated on the diodes D301, D303 and D304 and the Y, R-Y and B-Y signals are sent to the Tr307, Tr315 and Tr323, respectively. When AUX is not selected, the Tr 322 is turned off and a backward bias is actuated on the diodes D301, D303 and D304 to stop the AUX input signals.

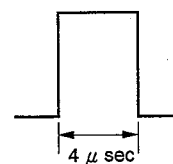
(3) Adjustment Procedure

Apply a 75% color bar signal to the composite video input terminal.

(a) VR1 (BST PHASE)

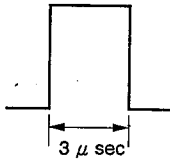
① Connect the probe to TP3.

② Adjust VR1 so that the pulse width is 4 μ sec.



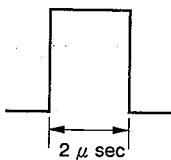
(b) **VR2(BST WIDTH)**

- ① Connect the probe to TP4.
- ② Adjust VR2 so that the pulse width is $3 \mu \text{ sec}$.



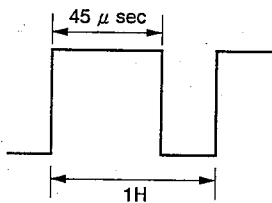
(c) **VR3(C.D CLP WIDTH)**

- ① Connect the probe to TP5.
- ② Adjust VR3 so that the pulse width is $2 \mu \text{ sec}$.



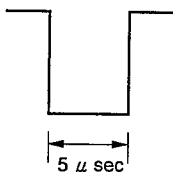
(d) **VR4(EQ.REJ)**

- ① Connect the probe to TP6.
- ② Adjust VR4 so that the pulse width is $45 \mu \text{ sec}$.



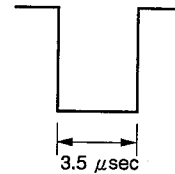
(e) **VR5(HD WIDTH)**

- ① Connect the probe to TP8.
- ② Adjust VR5 so that the pulse width is $5 \mu \text{ sec}$.



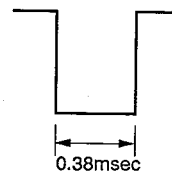
(f) **VR6(BRT P WIDTH)**

- ① Connect the probe to TP7.
- ② Adjust VR6 so that the pulse width is $3.5 \mu \text{ sec}$.



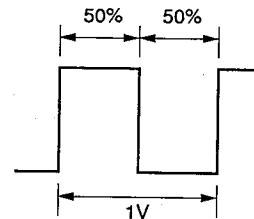
(g) **VR7(F.VD WIDTH)**

- ① Connect the probe to TP9.
- ② Adjust VR7 so that the pulse width is 0.38 m sec .



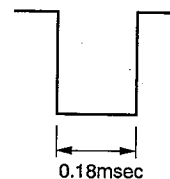
(h) **VR8(V.DL POS)**

- ① Connect the probe to TP10.
- ② Adjust VR8 so that the pulse width in the figure below is duty 50%.



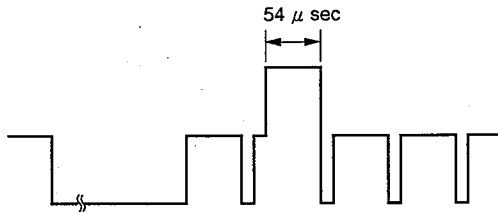
(i) **VR9(VD WIDTH)**

- ① Connect the probe to TP11.
- ② Adjust VR9 so that the pulse width is 0.18 m sec .



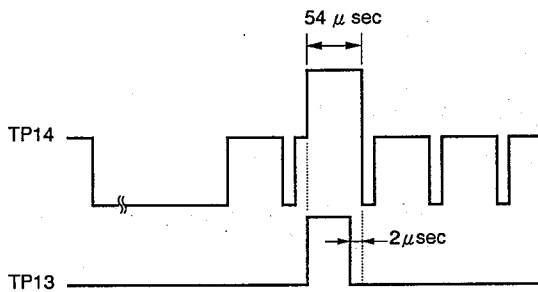
(j) **VR10(BIAS P. PHASE)**

- ① Connect the probe to TP14.
- ② Adjust VR10 so that the pulse width shown in the figure below is $54 \mu\text{sec}$.



(k) **VR11(S/H WIDTH)**

- ① Connect the probe to TP14.
- ② Connect the probe to TP13, too.
- ③ Adjust VR11 so that the waveform phase difference of TP14 and TP13 is $2 \mu\text{sec}$.



(l) **VR201(A/B DC)**

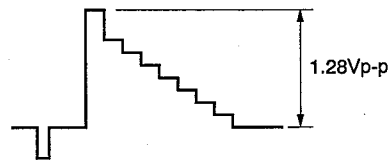
- ① Press the **INPUT SELECT** switch to select the "A/B".
- ② Connect the plus side of the voltmeter to TP203 and the minus side to TP201.
- ③ Adjust VR201 so that the DC voltage is +5V.

Be sure to set the following condition before the adjustment on and after here.

- ① Apply RGB signals(0.7Vp-p) to the AUX input terminals.
- ② Set the "RGB/YPbPr" selection to the "RGB" mode on the MENU screen. (Refer to 5-6. in the operation manual for details on the MENU setting method.)
- ③ Set the **AUX** switch on the front panel to "ON" position.
- ④ Set the CHROMA to 50%(preset) with the **CHROMA** switch on the pull-out panel.

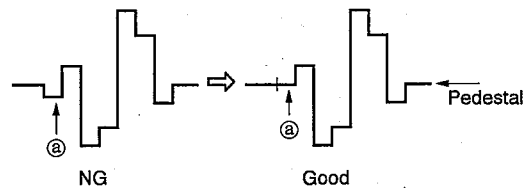
(m) **VR304(Y.LVL)**

- ① Connect the probe to TP305.
- ② Adjust VR304 so that the Y signal level is 1.28Vp-p .



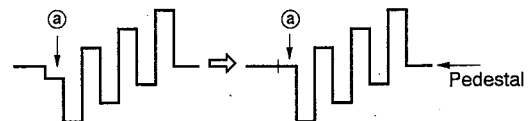
(n) **VR303(R.MIX LVL)**

- ① Connect the probe to TP302.
- ② Adjust VR303 so that the white component (part ①) of a color bar signal is the same level as the pedestal as shown in the figure below.



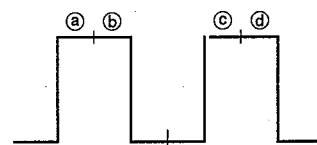
(o) **VR307(B MIX LVL)**

- ① Connect the probe to TP306.
- ② Adjust VR307 so that the white component (part ①) of a color bar signal is the same level as the pedestal as shown in the figure below.



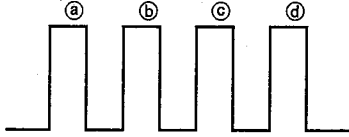
(p) **VR301(R-Y LVL)**

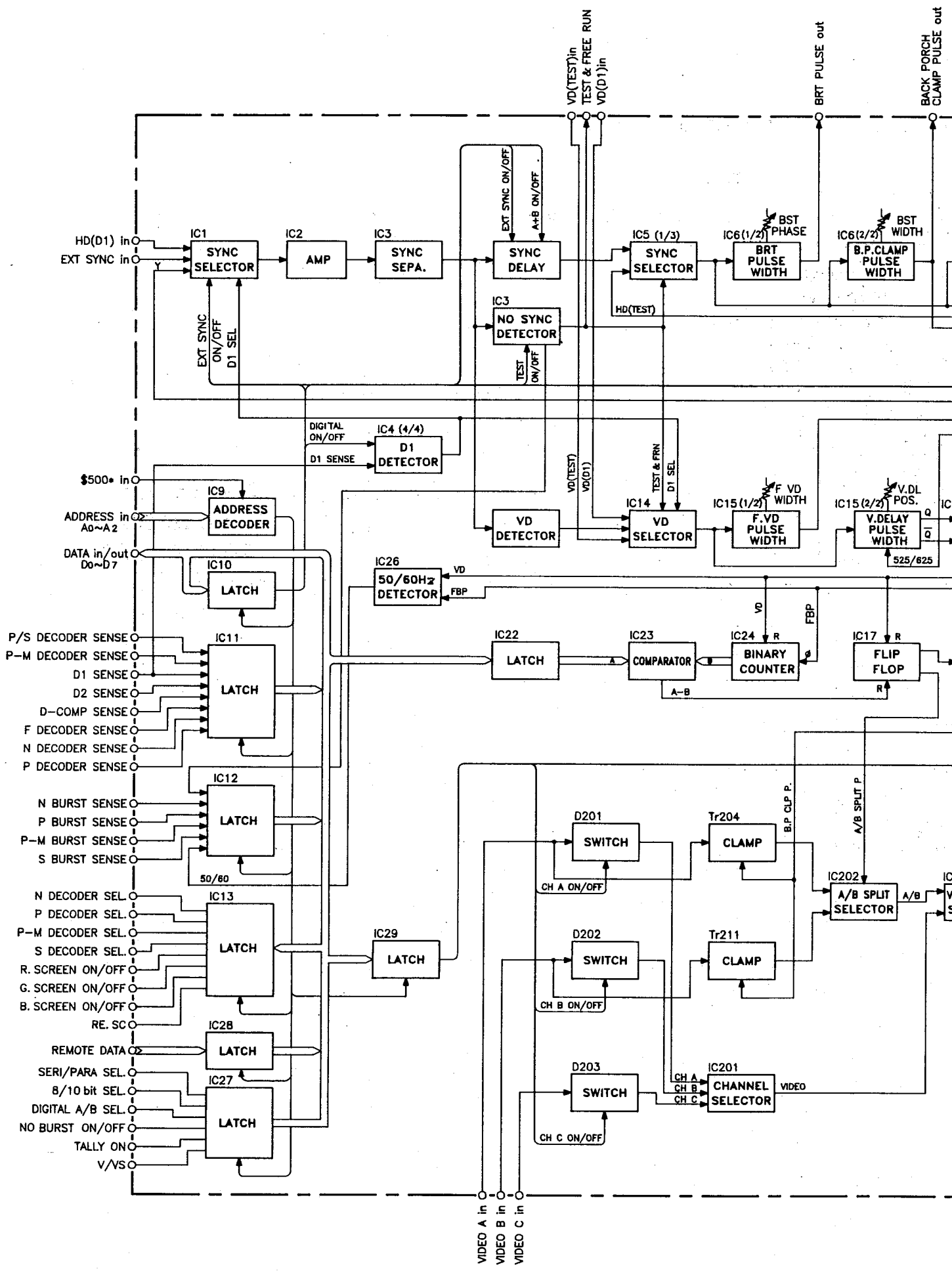
- ① Connect the probe to TP401 on the VIDEO OUT BOARD.
- ② Adjust VR301 so that the level of ① to ④ is the same as shown in the figure below.

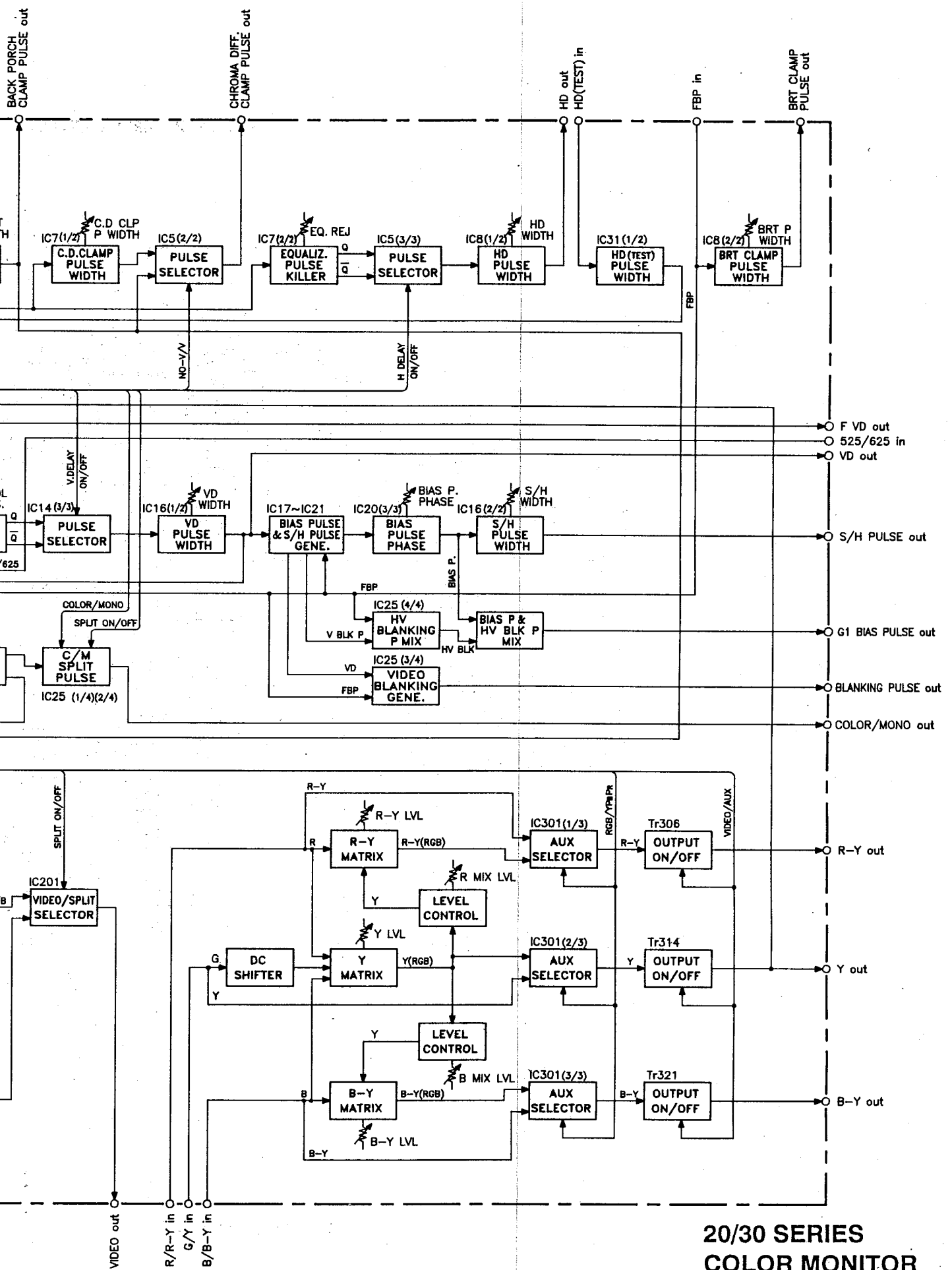


(g) VR305(B-Y LVL)

- ① Connect the probe to TP601 on the VIDEO OUT BOARD.
- ② Adjust VR305 so that the level of ① to ④ is the same as shown in the figure below.







20/30 SERIES
 COLOR MONITOR
 INTERFACE BOARD
 Block Diagram
 C2-904360

TP305

TP3

VR1(BST PHASE

VR304(Y.LVL)

VR307(B.MIX LVL)

VR305(B-Y LVL)

TP302

TP306

VR301(R-Y LVL)

VR303(R.MIX LVL)

TP201

VR201(A/B DC,

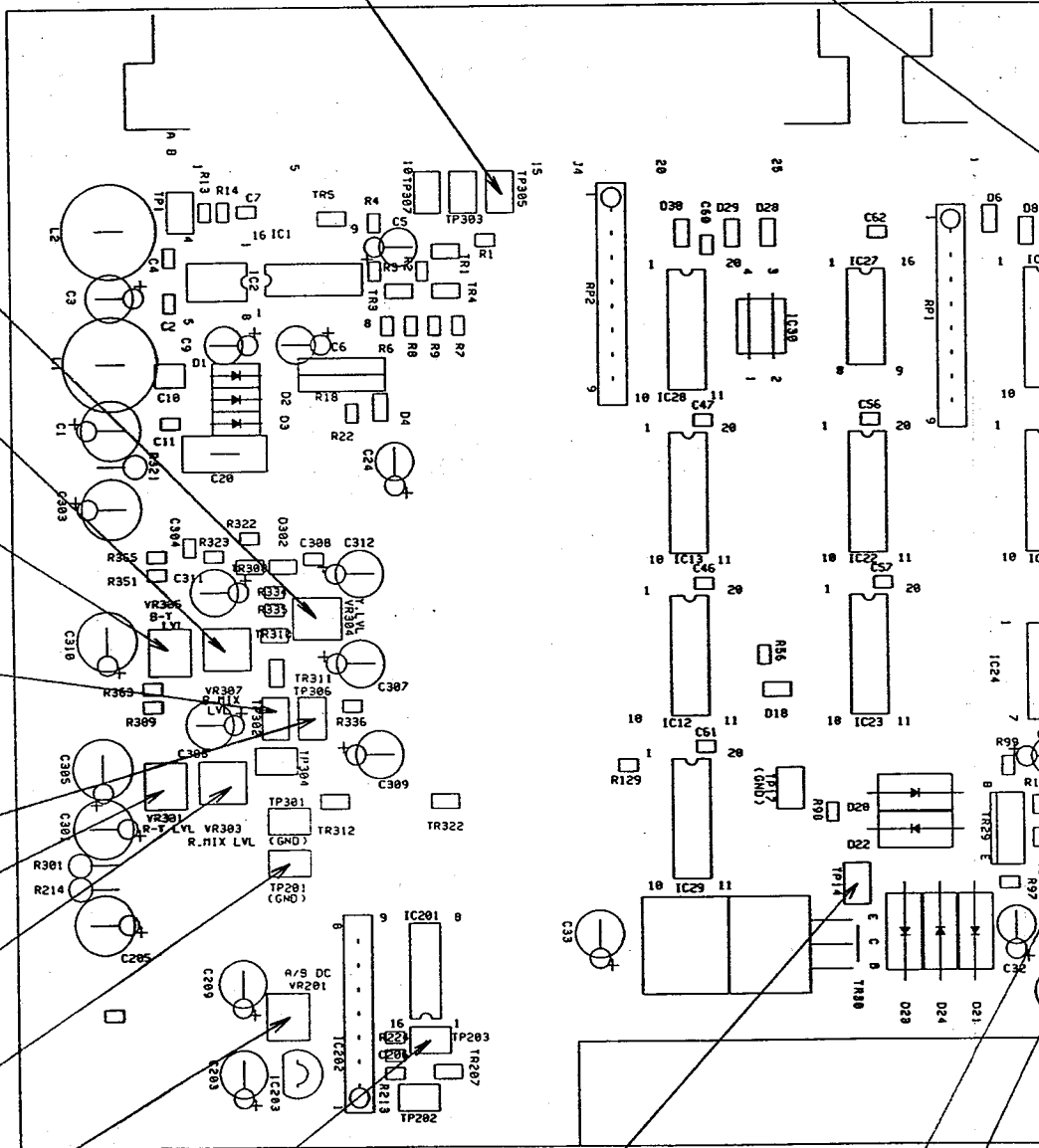
TP203

TP14

VR8(V.DL POS)

TP10

VR7(F.VD WIDTH)



VR2(BST WIDTH)

PHASE)

TP8

VR3(C.D CLP WIDTH)

VR4(EQ.REJ)

TP6

TP4

VR5(HD WIDTH)

VR6(BRT P WIDTH)

TP7

TP5

VR9(VD WIDTH)

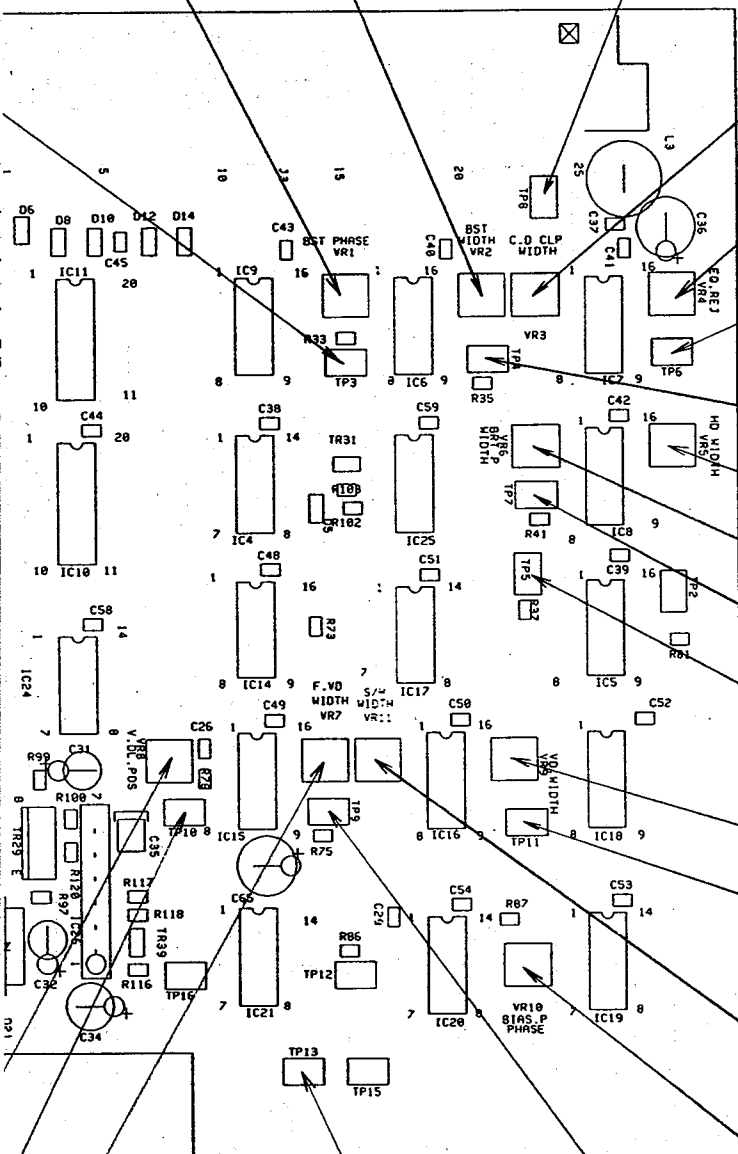
TP11

VR11(S/H WIDTH)

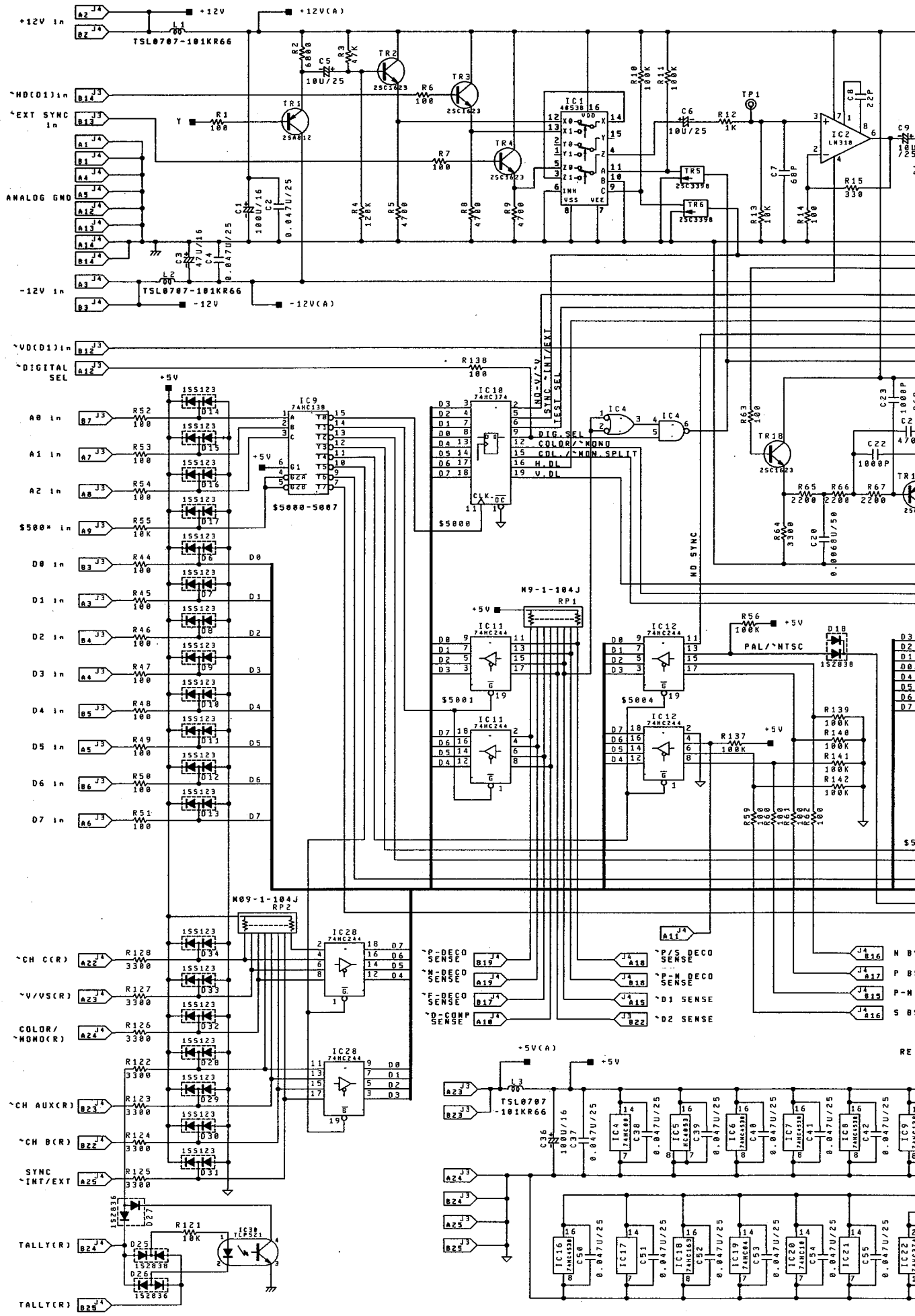
VR10(BIAS P.PHASE)

TP13

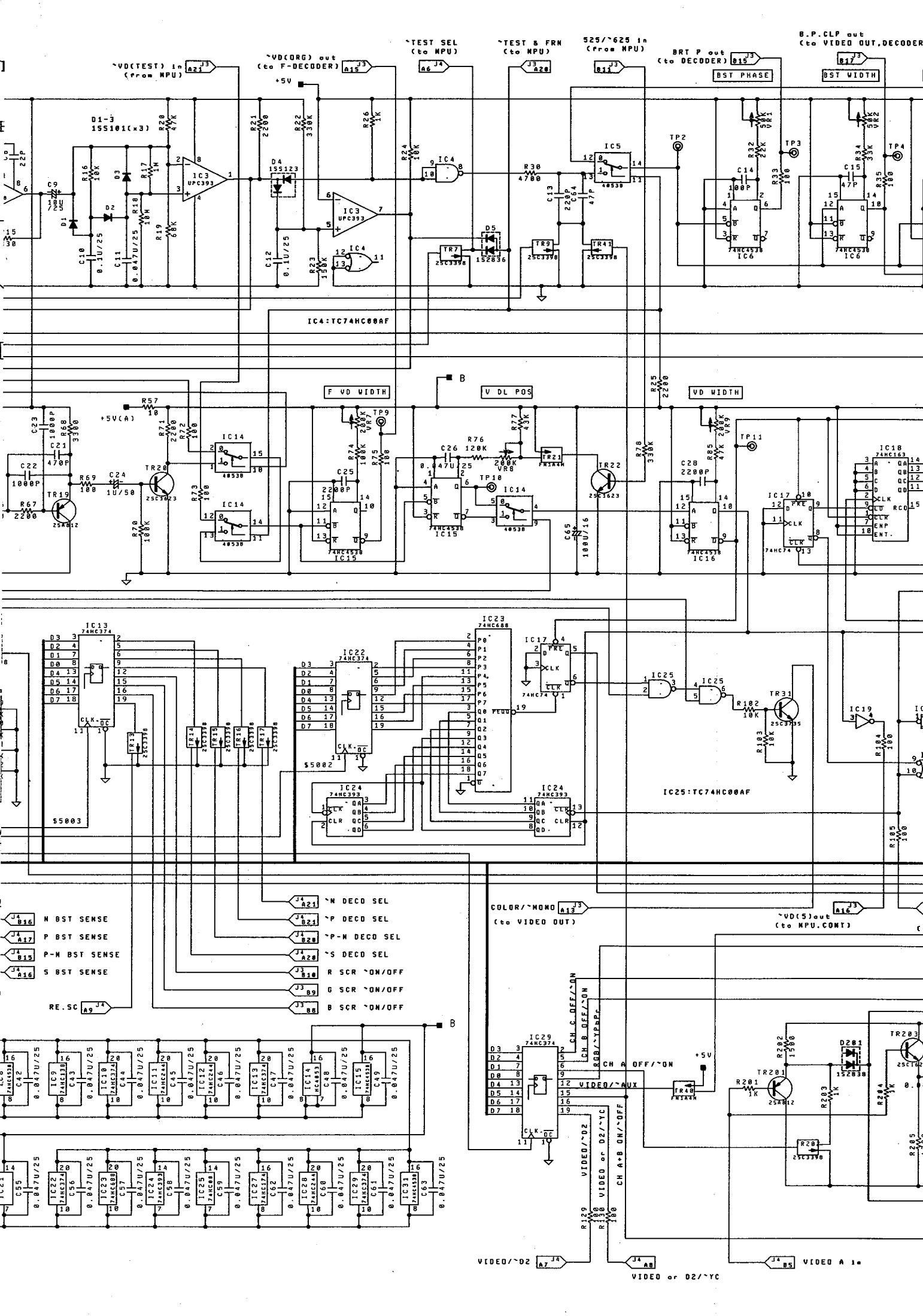
TP9



**20/30 SERIES
INTERFACE BOARD
PARTS LOCATION
PC1Y61**



- ~P-DECO SENSE J4 A18
- ~N-DECO SENSE J4 A18
- ~E-DECO SENSE J4 A19
- ~O-COMP SENSE J4 A18
- ~P/S DECO SENSE J4 A16
- ~P-M DECO SENSE J4 A17
- ~D1 SENSE J4 A15
- ~D2 SENSE J4 A16



VD(TEST) In
(From MPU)

VD(ORG) out
(to F-DECODER)

TEST SEL
(to MPU)

TEST & FRM
(to MPU)

525/625 In
(From MPU)

BRT P out
(to DECODER)

B.P. CLP out
(to VIDEO OUT, DECODER)

IC4:TC74HC00AF

F VD WIDTH

V DL POS

VD WIDTH

IC13:74HC374

IC23:74HC688

IC25:TC74HC00AF

- J1 B16 N BST SENSE
- J2 B16 P BST SENSE
- J3 B16 P-M BST SENSE
- J4 B16 S BST SENSE
- J5 A9 RE. SC
- J6 A21 N DECD SEL
- J7 A21 P DECD SEL
- J8 A21 P-M DECD SEL
- J9 A21 S DECD SEL
- J10 B10 R SCR ON/OFF
- J11 B9 G SCR ON/OFF
- J12 B8 B SCR ON/OFF

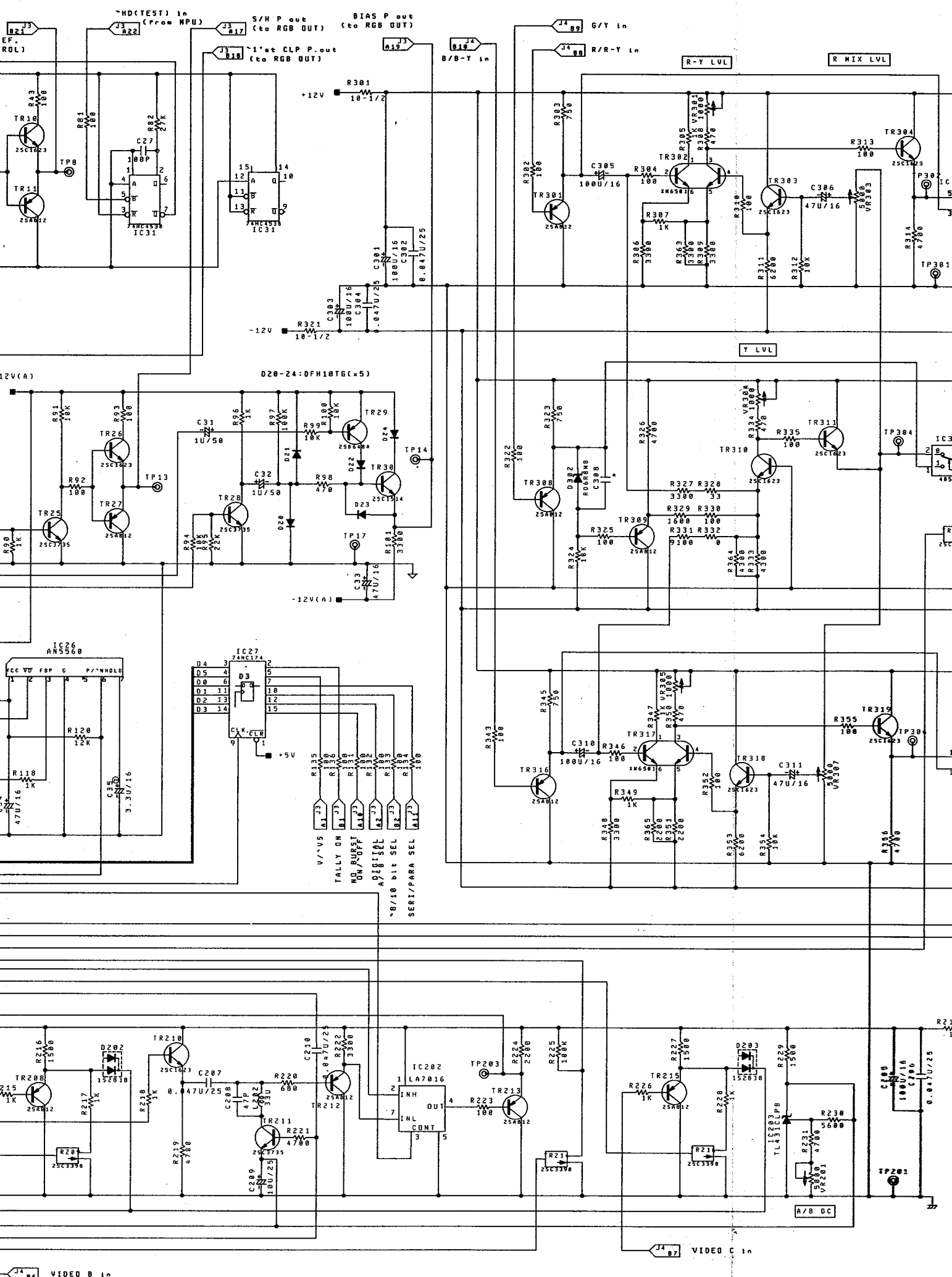
COLOR/MONO
(to VIDEO OUT)

VD(5)out
(to MPU.CONT)

VIDEO/D2
A7

VIDEO or D2/YC
A8

VIDEO A1
A5



~HD(TEST) In (From NPU) J3 A22
S/H P out (to RGB OUT) J3 A17
BIAS P out (to RGB OUT) J4 B18
~1st CLP P.out (to RGB OUT) J3 B18
B/B-Y In J4 B18
G/Y In J4 B9
R/R-Y In J4 B8

R-Y LVL
R MIX LVL

D20-24:DFH10TG(x5)

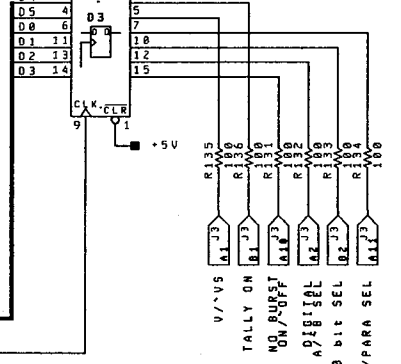
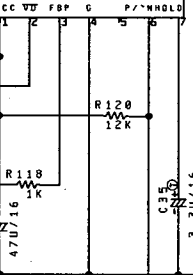
Y LVL

12V(A)

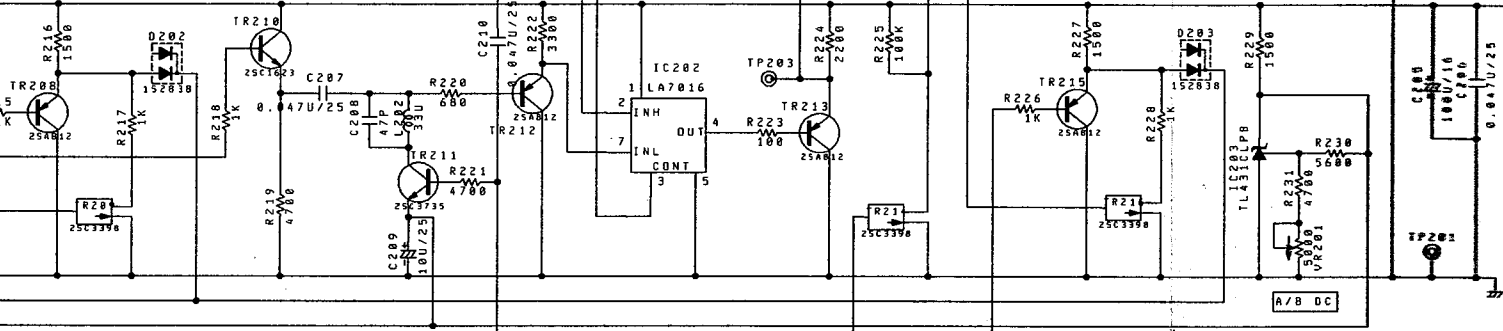
-12V(A)

IC26 AN5568

IC27 74NC174



V+V5 J1 J2
TALLY ON J3 J4
NO BURST ON/OFF J5 J6
DIGITAL SEL J7 J8
A/B bit SEL J9 J10
SERI/PARA SEL J11 J12



VIDEO B In J4 B6

VIDEO C In J4 B7

A/B DC

2-4. NTSC DECODER

2-4-1. PRE DECO(N)BOARD

(1) Outline

This board separates the composite signal of the NTSC system into the luminance signal and the color signal by using of the COMB filter or TRAP circuit to supply them to the DECODER(N)BOARD.

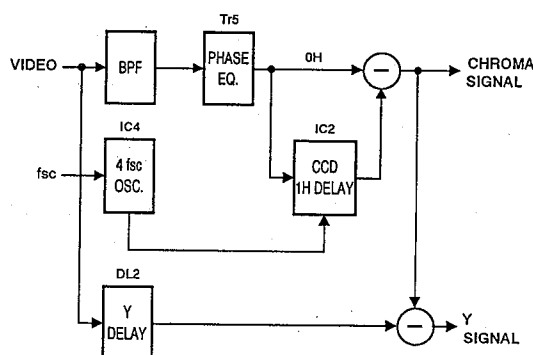
(2) Circuit Description

(a) CCD COMB filter circuit

The video signal supplied from the INTERFACE BOARD is selected with D2 signal by the analog switch of IC1 and outputted to IC1-④ pin. It is separated into the luminance signal (Y) and chroma signal (C) by the CCD COMB filter circuit.

The chroma (C) signal separation circuit is firstly described here. It picks up only the frequency centering around 3.58MHz from the video signal by the band pass filter consisting of L1, L2 and L10 to perform phase compensation with the phase equalizer consisting of Tr5, VR10, C8 and L3. The Tr38 serves to subtract the signal delayed for 1H by the 1H CCD delay line (IC2) from this signal and makes a chroma signal alone. The CCD delay line of IC2 is provided for 1H delay using the quadruple subcarrier (4fsc) as a clock.

This 4fsc clock is made at IC 4 on the basis of the subcarrier sent from the DECODER (N) BOARD. Next, the luminance signal separation circuit description is given below. It eliminates the delay error between Y and C by equalizing the amount of the signal delay to that of the signal delay caused by the chroma circuit at the Y delay line of DL2. The Tr36 serves to subtract the chroma signal from this signal, thereby separating the luminance signal alone.



(b) Trap circuit

The chroma signal at trapping is made by passing through the delay line DL3 to eliminate the delay error caused by the Y signal trap filter without passing through the CCD COMB filter circuit. The luminance signal is made by eliminating the 3.58MHz component through the trap filter circuit consisting of C44, C45 and L8.

(3) Adjustment Procedure

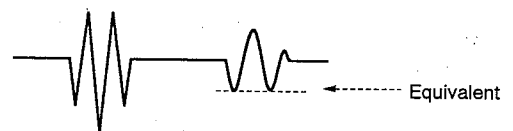
Set the preset level of the HUE and CHROMA respectively to 50%.

(a) L1, L2, L10

- ① Apply a sweep signal to the composite video input terminal.
- ② Connect the probe to TP1.
- ③ Adjust L2 and L10 so that the frequency characteristics of 3.58MHz \pm 0.5MHz is flat.
- ④ Adjust L1 so that the frequency characteristics of 7.16MHz is minimum.

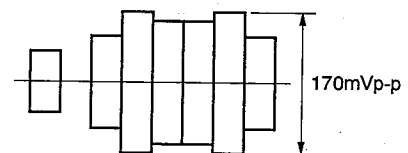
(b) VR10(PHASE EQ)

- ① Apply a 2T pulse signal to the composite video input terminal.
- ② Connect the probe to TP1.
- ③ Adjust VR10 so that the right pulse is the same level as the left pulse.



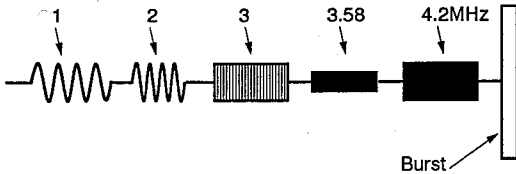
(c) VR2(ϕ H LEVEL)

- ① Apply a color bar signal.
- ② Connect the probe to TP1.
- ③ Adjust VR2 to that the chroma level is 170m Vp-p.

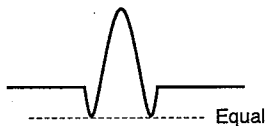


(d) **VR3(1H LEVEL)**
VR4(1H PHASE)
VR14(1H PHASE EQ)

- ① Apply a multi burst signal.
- ② Connect the probe to TP8.
- ③ Adjust VR3, VR4 and VR14 so that the level of the burst portion is as maximum as possible and the level of the 3MHz portion is as minimum as possible.

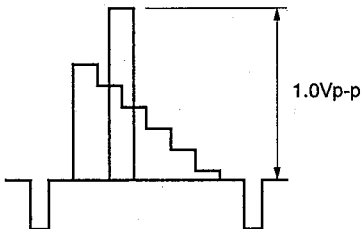


- ④ Apply a color bar signal.
- ⑤ Connect the probe to TP3.
- ⑥ Adjust VR7 and VR8 to eliminate the subcarrier roughly.
- ⑦ Apply a 2T pulse signal.
- ⑧ Adjust VR3, VR4 and VR14 so that the right pulse is the same level as the left pulse.



(e) **VR5(COMB Y LEVEL)**

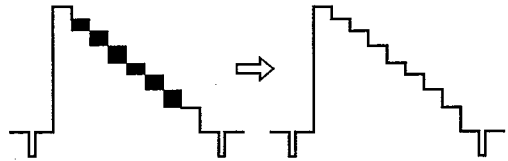
- ① Apply a color bar signal.
- ② Connect the probe to TP7.
- ③ Adjust VR5 so that the level is 1.0Vp-p.



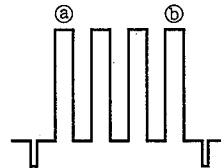
(f) **VR7(Y COMB PHASE)**
VR8(COMB LEVEL)
VR11(CHROMA LEVEL)

- ① Apply a color bar signal.
- ② Connect the probe to the emitter of Tr32.

- ③ Adjust VR11 so that the burst level is 0.28Vp-p.
- ④ Set the **MONO** and **COMB** switches respectively to "ON" position.
- ⑤ Connect the probe to TP602 on the VIDEO OUT BOARD inserted into the SLOT No.4.
- ⑥ Adjust VR7 and VR8 to eliminate the sub carrier.



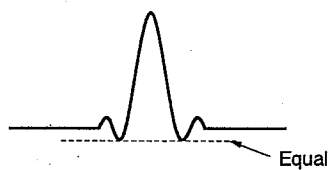
- ⑦ Set the **MONO** switch to "OFF" position.
- ⑧ At this time, confirm that the portion (a) is the same level as the portion (b). Otherwise, readjust VR11. When readjusting VR11, repeat steps ④ to ⑧ again.



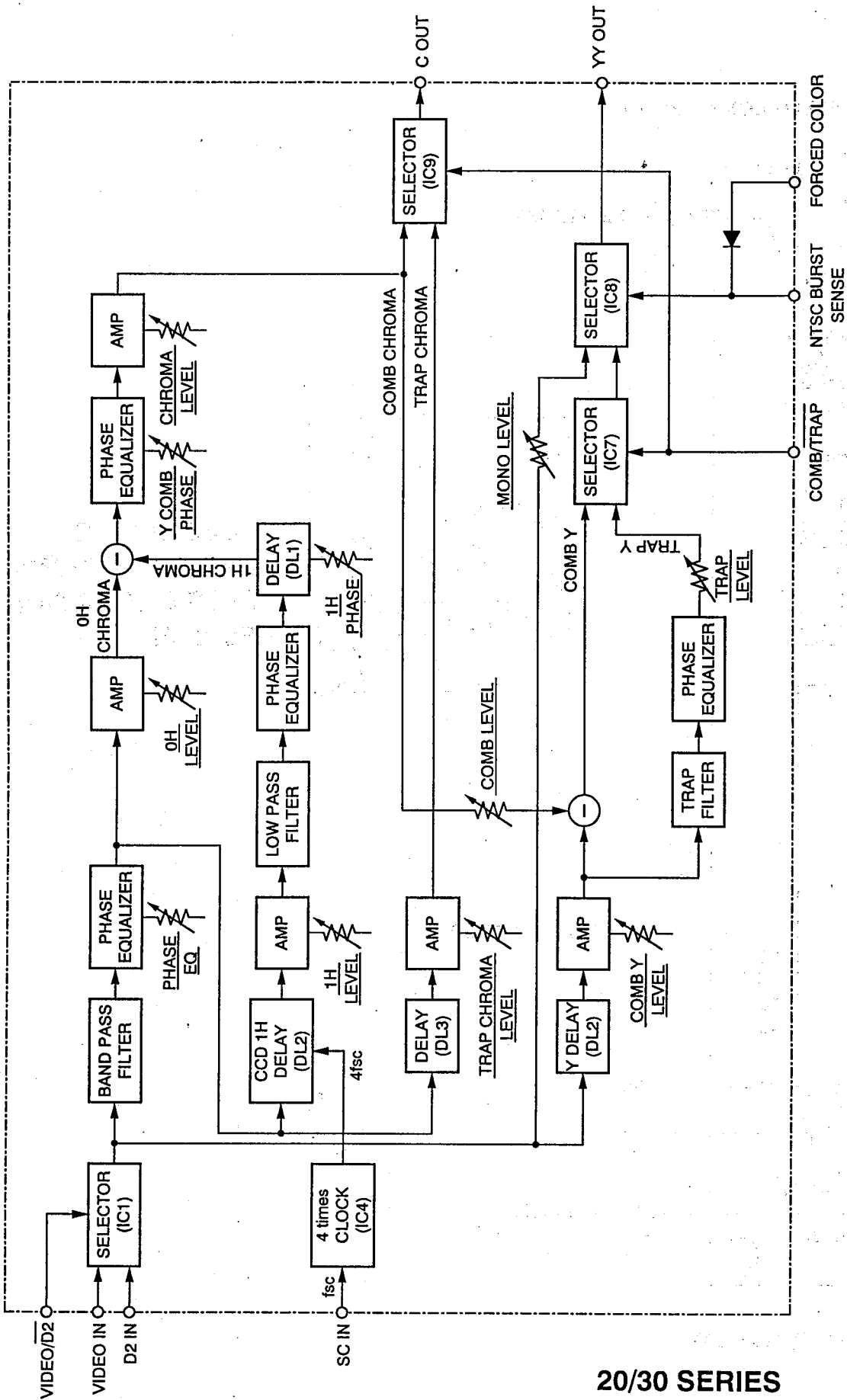
(g) **VR1(MONO LEVEL)**
VR9(TRAP LEVEL)
VR12(TRAP CHROMA LEVEL)
VR13
L8

- ① Set the **MONO** switch to "ON" position and set the **COMB** switch to "OFF" position.
- ② Connect the probe to TP602 on the VIDEO OUT BOARD.
- ③ Adjust L8 to eliminate the sub carrier.
- ④ Adjust VR9 to attain the same level with the **COMB** switch "ON".
- ⑤ Set the **MONO** switch to "OFF" position.
- ⑥ Adjust VR12 so that the portion (a) is the same level as the portion (b) in the figure of section (f) ⑧.
- ⑦ Apply a 2T pulse signal.
- ⑧ Set the **MONO** switch to "ON" position.

- ⑨ Adjust VR13 so that the right pulse is the same level as the left pulse.



- ⑩ Apply the signal with the burst and the signal without the burst, which are the same level.
- ⑪ When changing the input signal, adjust VR1 so that each level is the same.



**20/30 SERIES
COLOR MONITOR
PRE DECO(N) BOARD
Block Diagram**

C4-904384

VR12 (TRAP CHROMA LEVEL)

VR9 (TRAP LEVEL)

L8

TP3

VR13

VR8 (COMB LEVEL)

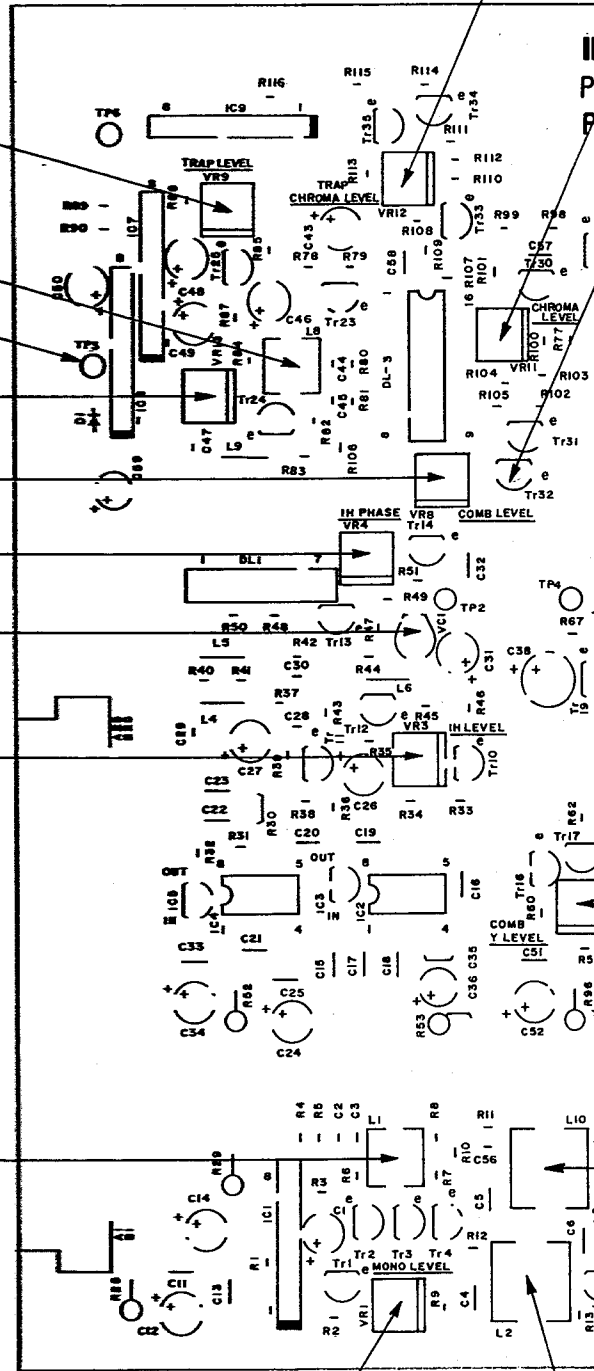
VR4 (1H PHASE)

VC 1

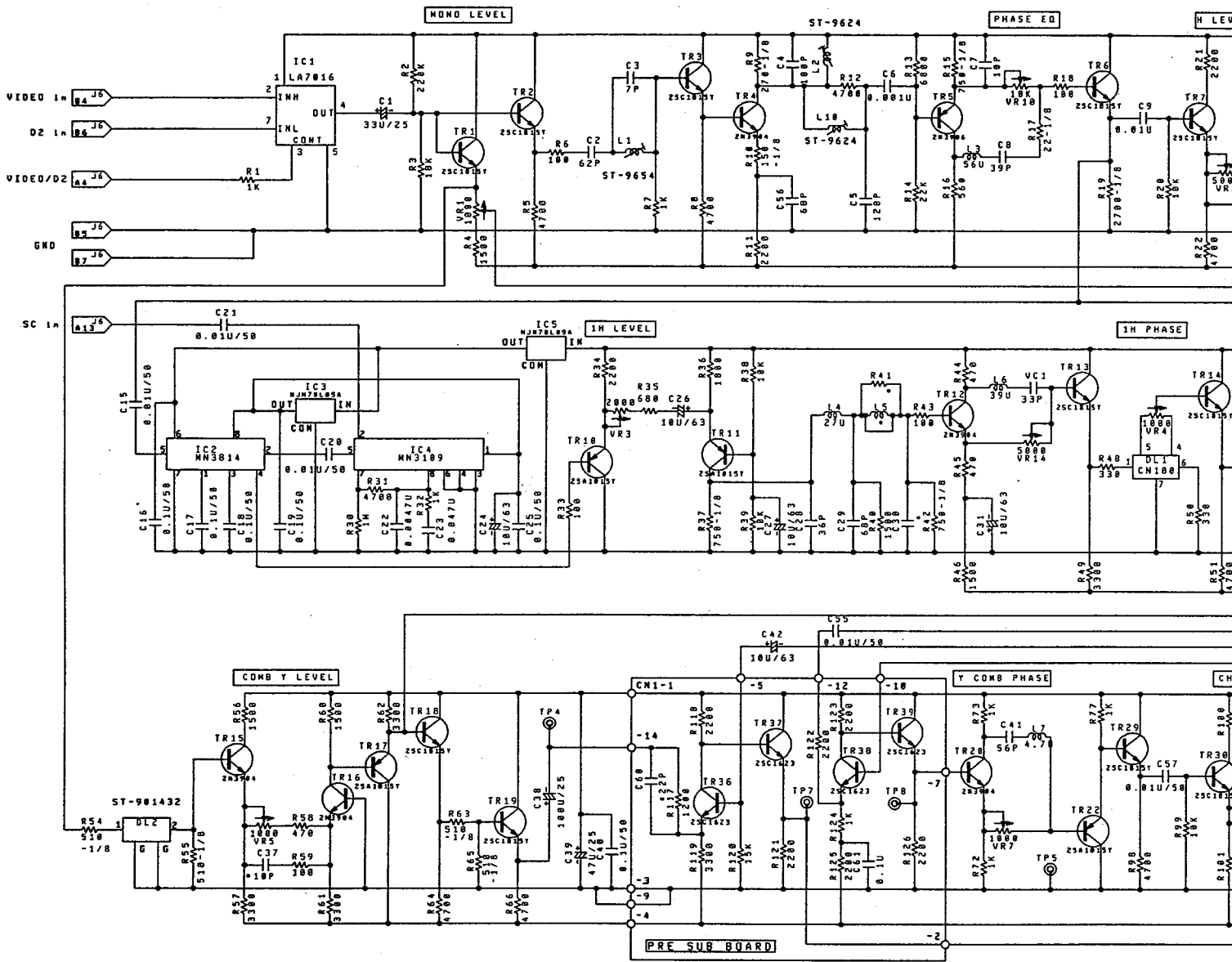
VR3 (1H LEVEL)

L1

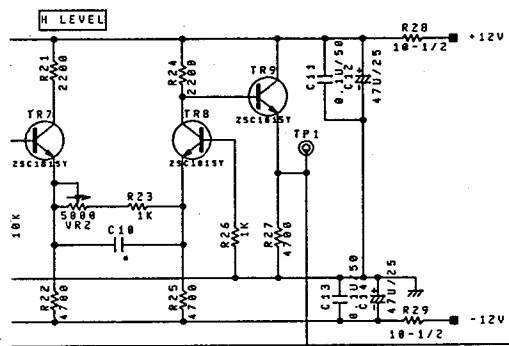
VR1 (MONO LEVEL)



20/30 SERIES
PRE DECO(N) BOARD
PARTS LOCATION
P-70403B

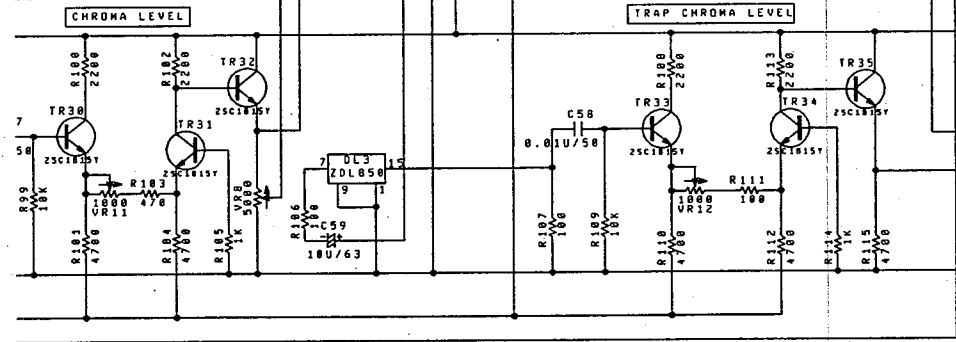
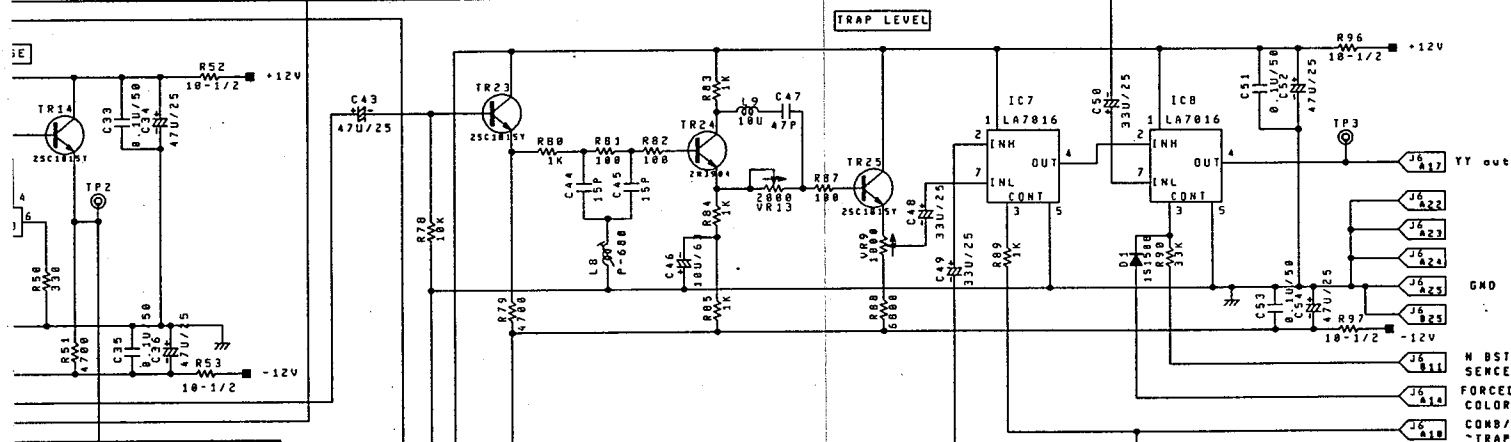
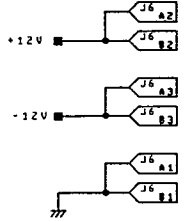


NOTE: 1. All resistors are in ohms 5% (parts marked F: 1%), 1/4 watt unless otherwise specified. 2. All capacitors are in farads, 300V unless otherwise specified. 3. All inductors are in henry unless otherwise specified. 4. Waveforms are taken with a color bar signal input. 5. Parts marked 6. Parts marked for X-radiat



LAST NO. LOST NO.

IC9 IC6
DL3 Tr-21, 26-28
Tr-39 R 47.57-71.74-76
R 126 C 61
C 61 VR6
L 10
VR14
TP8
VC1



marked * are factory selected value.
marked * are critical components
radiation.

**20/30 SERIES
COLOR MONITOR
PRE DECO(N) BOARD
Schematic Diagram
C21-904198B**

2-4-2. DECODER (N)BOARD

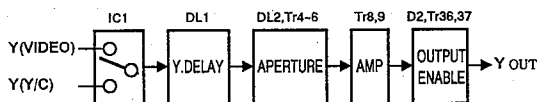
(1) Outline

This board produces the Y, R-Y and B-Y signals from the luminance signal and the color signal which are input, and supplies them to the VIDEO OUT BOARD.

(2) Circuit Description

(a) Y signal processing circuit

The Y signal separated apart from the chroma signal on the PRE DECO(N) BOARD is inputted to IC1. Also, the Y signal of Y/C input is inputted to IC1. One of them is selected and then inputted to 140nsec delay line DL1. The output from this delay line passes through the aperture circuit consisting of Tr4 ~ 6 and DL2, and the Y signal level is adjusted by means of the differential amplifier (Tr8, 9). Then, the back porch of this signal is clamped with Tr11 and the Y signal is supplied to the VIDEO OUT BOARD after passing through the output enable circuit consisting of D2, Tr36 and Tr37.



(b) Chroma signal demodulating circuit

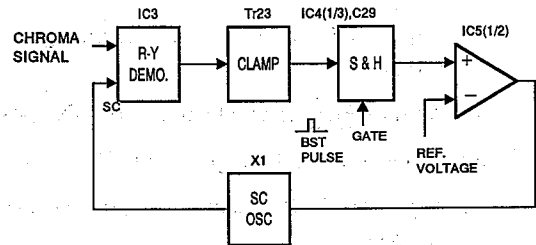
The chroma signal separated apart from the luminance signal on the PRE DECO(N) BOARD is inputted to IC2. Also, the C signal of Y/C input is inputted to IC2. One of them is selected and inputted to IC3 and IC7.

The IC3 and IC7 that are made to apply a sine wave of 3.58MHz from the subcarrier oscillator, perform the demodulation of R-Y and B-Y respectively. The R-Y signal is supplied to the VIDEO OUT BOARD after passing through the output enable circuit consisting of D3, Tr39 and Tr40, and the B-Y signal is supplied to the VIDEO OUT BOARD after passing through the output enable circuit consisting of D4, Tr42 and Tr43.

(c) Color hold circuit

The SYNC portion (including no chroma component) of the demodulated R-Y signal is clamped with Tr23. Then, the burst portion is subject to sampling by means of the sample & hold circuit consisting of IC4 and C29 and the DC voltage is compared with the reference voltage applied to IC5-⑥ pin. The sub-carrier oscillator is controlled according to the result of the comparison, thereby stabilizing the oscillation frequency and phase.

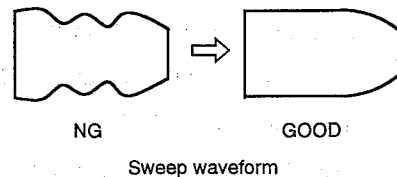
Since the phase of the burst signal crosses the R-Y axis, if the hue is not shifted, when the reference voltage is zero, the color hold is correctly adjusted. Conversely, hue adjustment is carried out by changing the reference voltage at IC5(2/2) and IC6 by the hue control voltage.



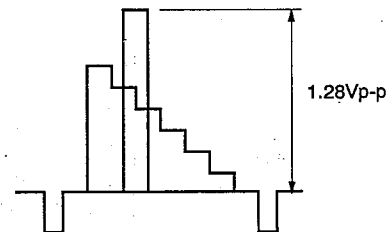
(3) Adjustment Procedure

(a) VR1(MATCH) VR2(Y LEVEL) VR12(APT OS)

- ① Apply a sweep signal to the composite video input terminal.
- ② Connect the probe to TP9.
- ③ Adjust VR1 so that the waveform is flat.



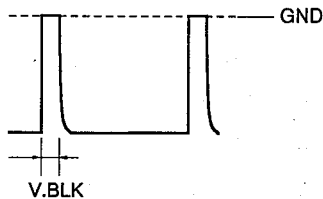
- ④ Apply a color bar signal.
- ⑤ Set the **FORCED COLOR** switch on the pull-out panel to "ON" position.
- ⑥ Adjust VR2 so that the level is 1.28Vp-p.



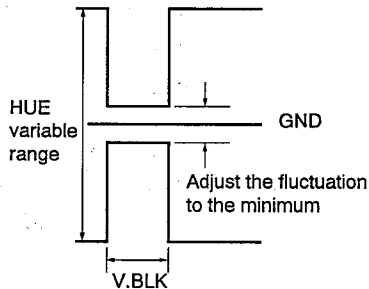
- ⑦ Apply a sweep signal.
- ⑧ Set the aperture level to 20% with the **APT** switch on the pull-out panel. (Refer to 5-5(1) in the OPERATION MANUAL.)
- ⑨ Adjust VR12 so that the aperture starts taking effect.

(b) **VR6(HUE HIGH)**
VR7(HUE LOW)
VR9(HUE OFFSET)
L5

- ① Apply a color bar signal.
- ② Connect the probe to TP6.
- ③ Adjust L5 so that the oscillating level is maximum.
- ④ Set the **HUE** manual control to "ON" position.
- ⑤ Connect the probe to pin ⑦ of IC8.
- ⑥ Adjust the HUE manual level so that the output is GND.
- ⑦ Connect the probe to TP8.
- ⑧ Adjust VR9 so that the period of the vertical blanking is GND. At this time, when the color hold is not obtained, adjust VR3 (COLOR HOLD).



- ⑨ Connect the probe to pin ⑫ of IC6.
- ⑩ Adjust VR7 so that the output is GND.
- ⑪ Connect the probe to TP5.
- ⑫ Adjust VR6 so that the period of the vertical blanking is GND.
- ⑬ When the HUE is changed from MIN to MAX, adjust VR7 so that the level fluctuation of the vertical blanking period against GND is minimum.



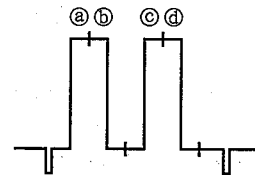
- ⑭ Set the **HUE** manual control to "OFF" position.

(c) **VR3 (COLOR HOLD)**

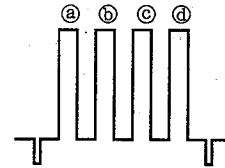
- ① Set the **FORCED COLOR** switch on the pull-out panel and **SYNC EXT** switch on the FRONT PANEL respectively to "ON" position.
- ② Connect the probe to TP602 on the VIDEO OUT BOARD inserted into the SLOT No.5.
- ③ When attenuating a color bar signal to - 50dB with the attenuator, adjust VR3 so that the color hold is obtained.

(d) **VR5(R-Y LEVEL)**
VR8(B-Y LEVEL)
VR10(COMB HUE)
VR11(SUB HUE)
L6

- ① Apply a color bar signal.
- ② Connect the probe to TP402 on the VIDEO OUT BOARD.
- ③ Set the HUE level to 50% with the **HUE** preset switch on the pull-out panel. (Refer to 5-3(2) in the OPERATION MANUAL).
- ④ Adjust VR5 and VR11 so that the level of portions ② to ④ in the figure below is the same.



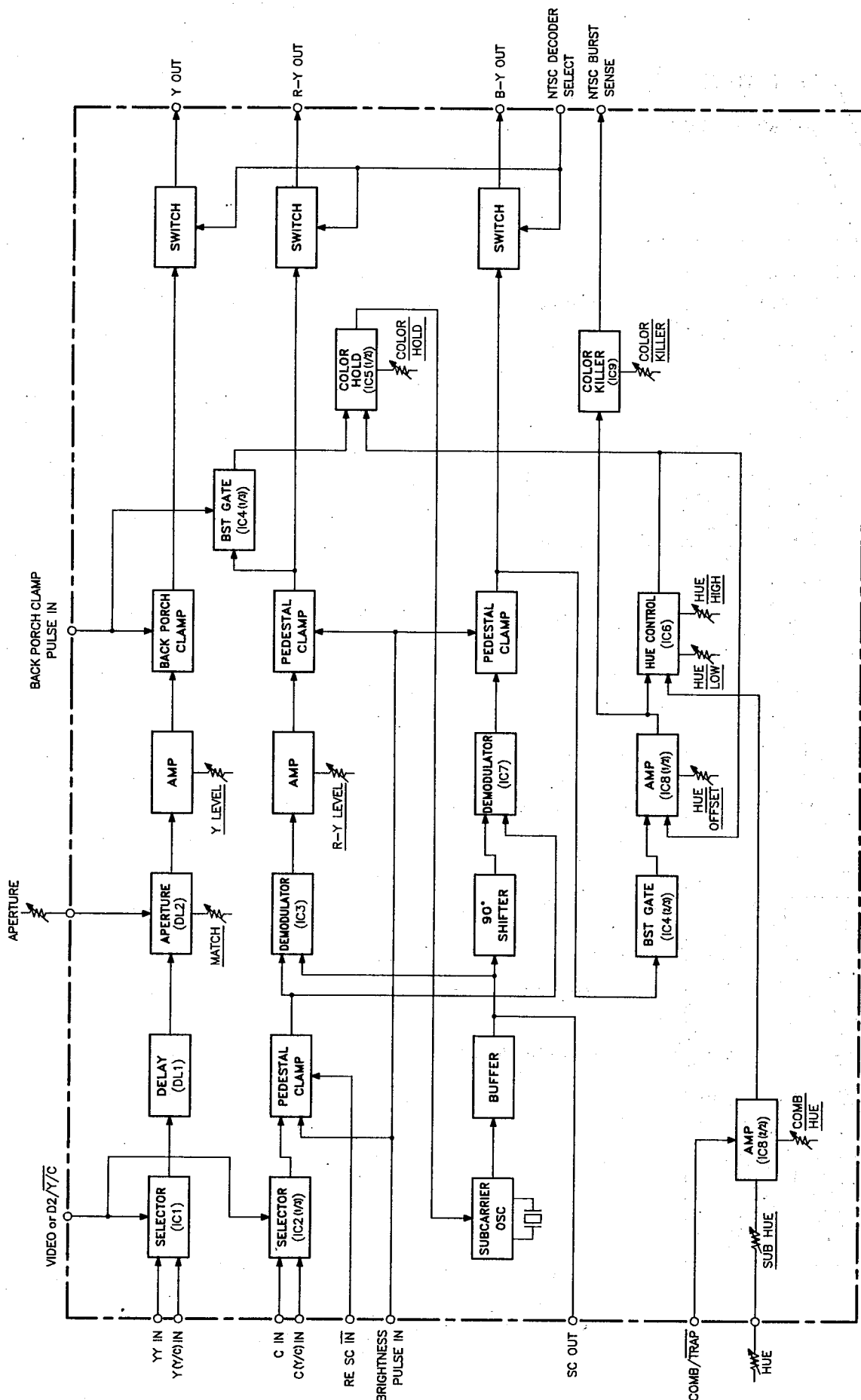
- ⑤ Connect the probe to TP602 on the VIDEO OUT BOARD.
- ⑥ Adjust VR8 and L6 so that the level of portions ② to ④ in the figure below is the same.



- ⑦ When the **COMB** switch is switched on/off, adjust the HUE shifting (level shifting of portions ② and ③ above) with VR10 with the **COMB** switch "ON".

(e) *VR4(COLOR KILLER)*

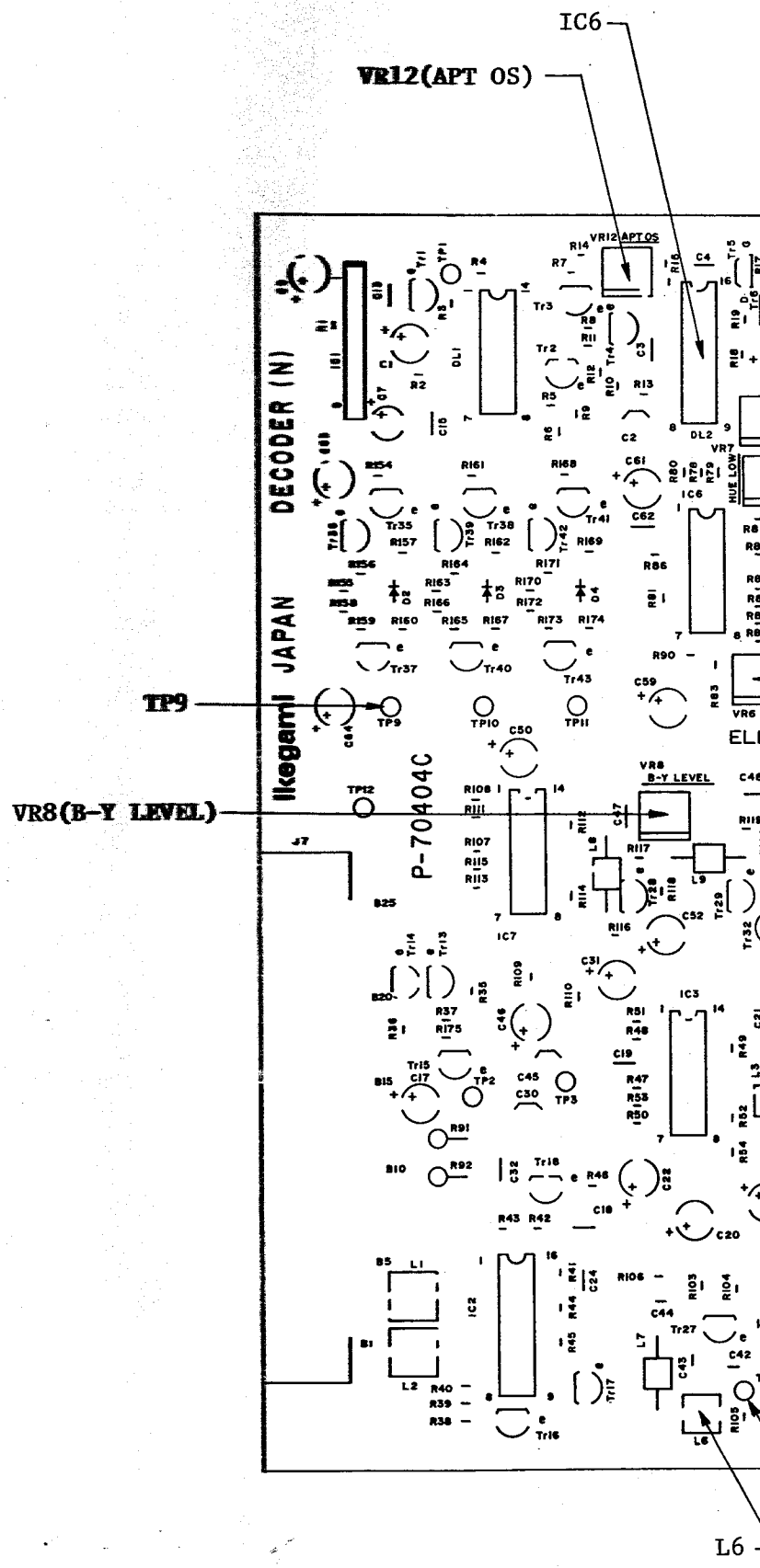
- ① Set the **FORCED COLOR** switch to "OFF" position.
- ② Connect the probe to TP602 on the VIDEO OUT BOARD.
- ③ When attenuating a color bar signal to - 18dB with the attenuator, adjust VR4 so that the color killer operates.

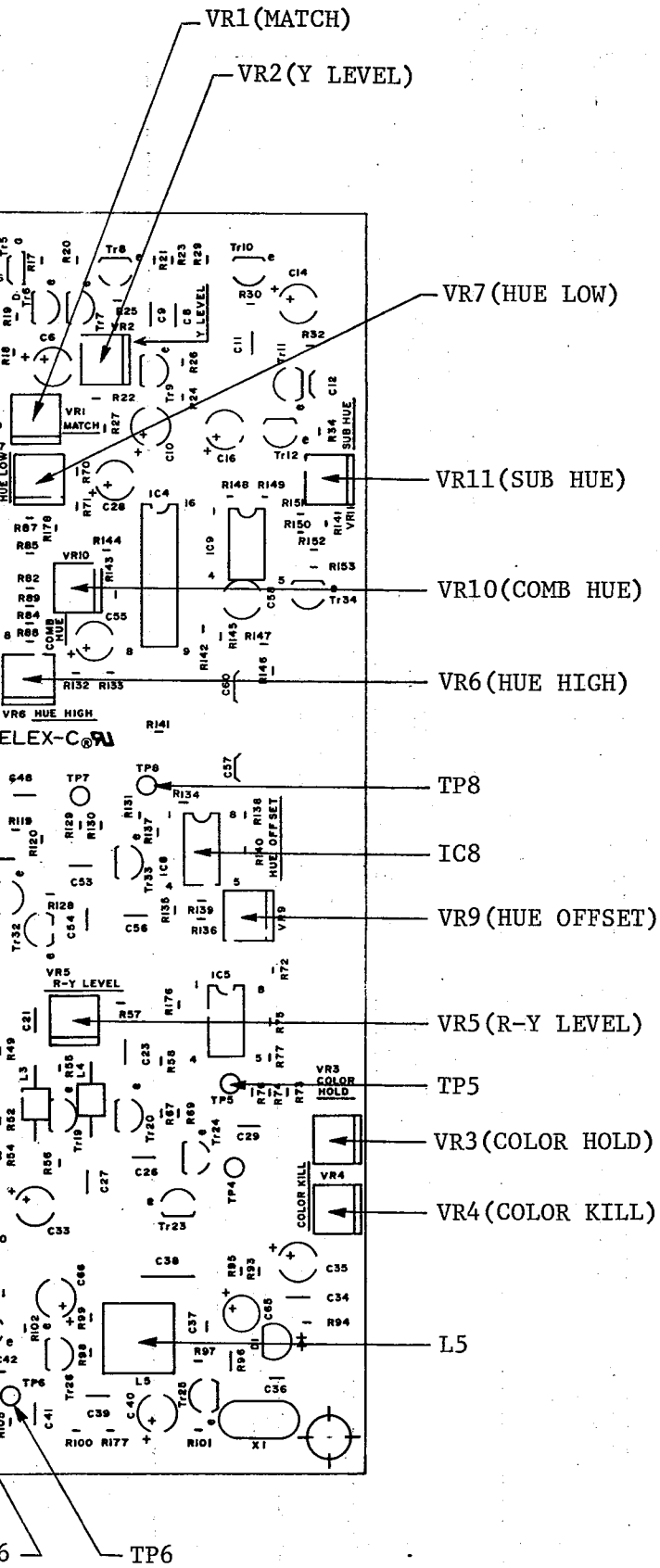


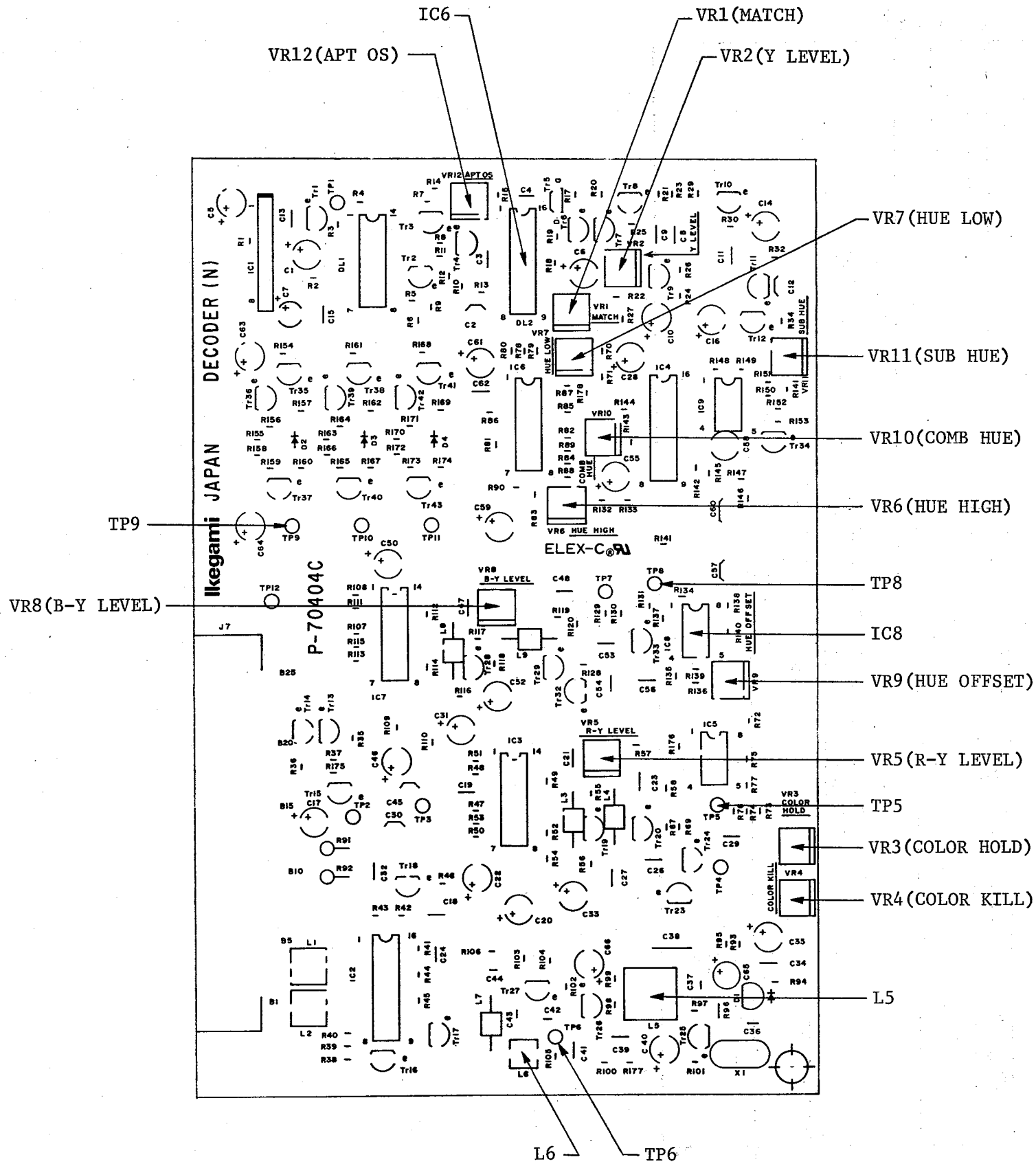
**20/30 SERIES
COLOR MONITOR
DECODER(N) BOARD
Block Diagram**

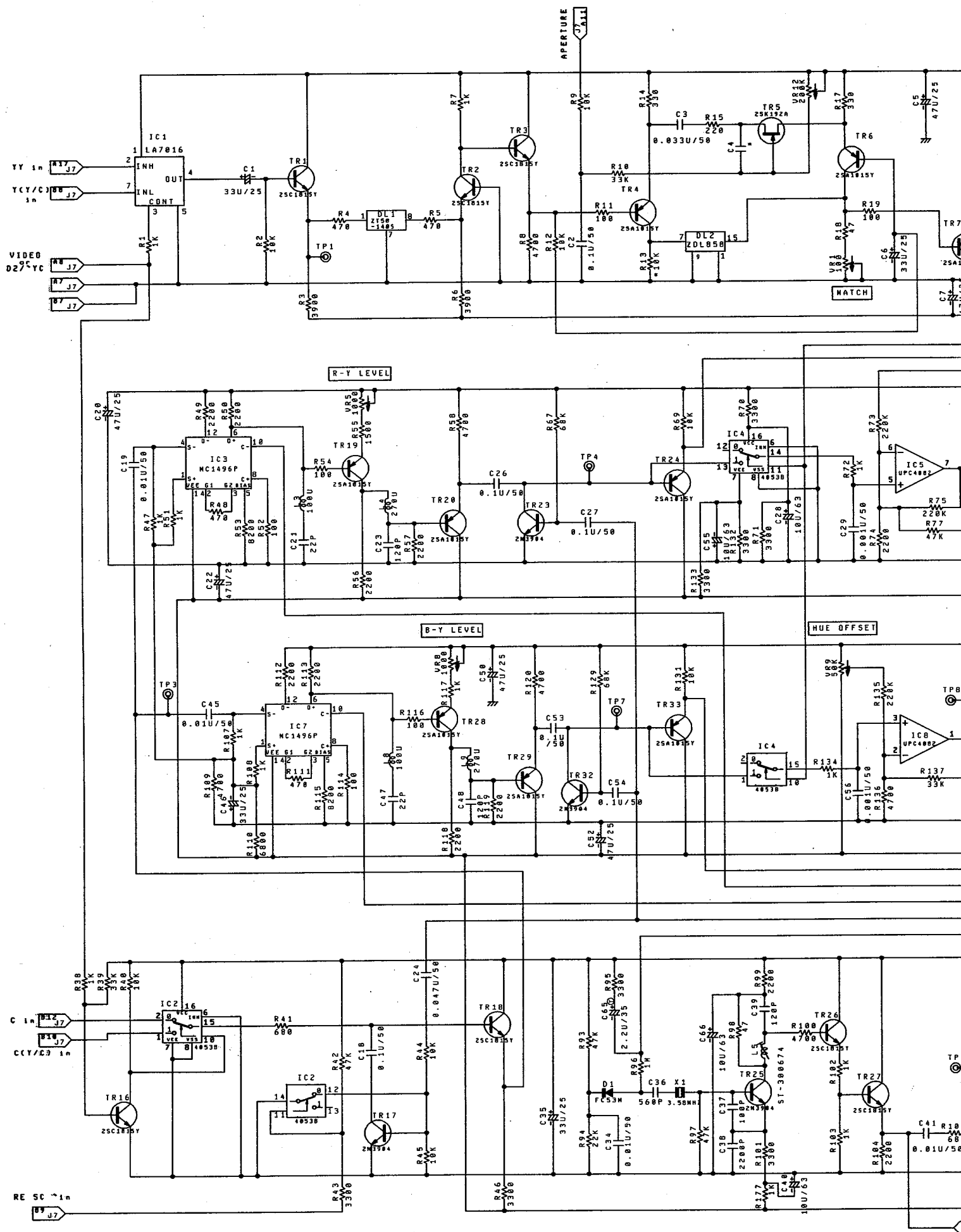
C3-904326

**20/30 SERIES
 DECODER(N) BOARD
 PARTS LOCATION
 P-70404C**

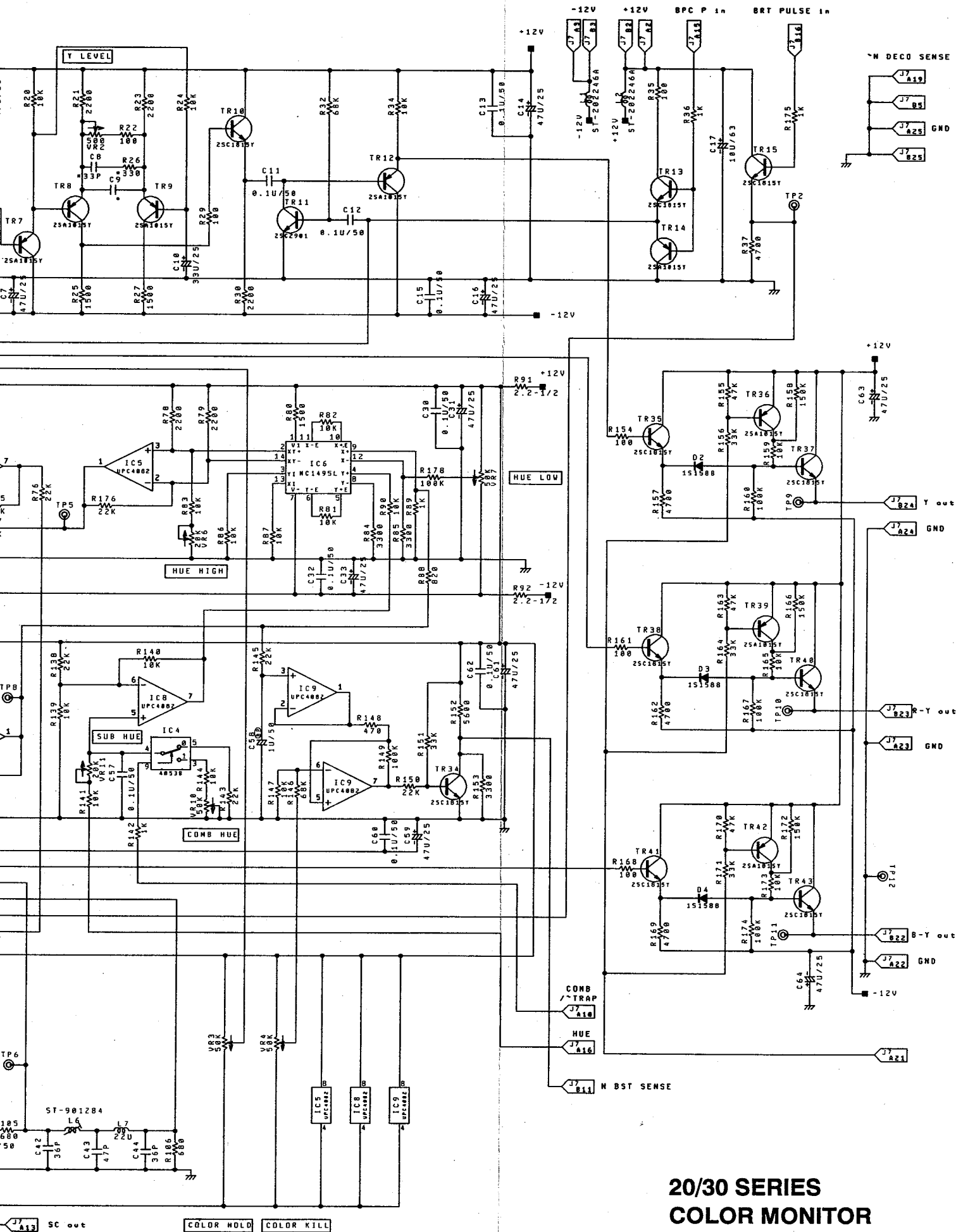








NOTE: 1. All resistors are in ohms 5% (parts marked F:1X), 1/4 watt unless otherwise specified. 2. All capacitors are in farads, 300V unless otherwise specified. 3. All inductors are in henry unless otherwise specified. 4. Waveforms are taken with a color bar signal input. 5. Parts marked * are factory selected value. 6. Parts marked * are critical components for X-radiation.



**20/30 SERIES
COLOR MONITOR
DECODER(N) BOARD
Schematic Diagram
C11-904197A**

LAST NO.	LOST NO.
IC9 D4	Tr 21,22,30,31
DL2 VR12	R 16,28,31,33,59-66
TR43 R178	68,121-128,130
L9 C66	C 25,49,51

2-5. PAL-B DECODER

2-5-1. DECODER(P)BOARD

(1) Outline

This board separates the composite signal into the luminance signal and color signal, and supplies the separated luminance signal (or the Y signal of the Y/C input) and the R-Y and B-Y signals produced from the separated color signal (or the C signal of the Y/C input) to the VIDEO OUT BOARD.

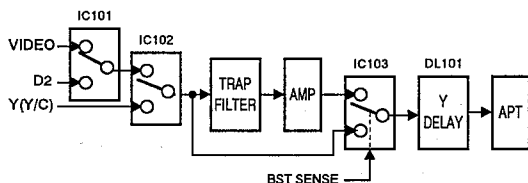
(2) Circuit Description

(a) Y signal process circuit

The VIDEO signal supplied from the INTERFACE BOARD is inputted to IC101. Also, the D2 signal is inputted to IC101. One of them is selected and outputted from IC101-④ pin. The subcarrier component of this signal is removed by the subcarrier trap filter circuit consisting of L101 and C102 and the phase compensation is performed by the phase equalizer consisting of L102, L103, C103 and C104.

The differential amplifier consisting of Tr104 and Tr105 serves to compensate the drop of the level by the filter circuit.

This signal or Y signal at Y/C input is selected with the analog switch of IC103 and inputted to the delay line DL101 to compensate the delay with the chroma circuit. The output enters the aperture circuit consisting of Tr109, Tr110, Tr111 and DL102.



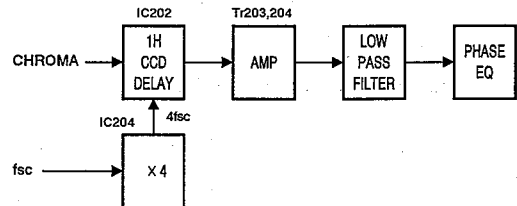
(b) 1H CCD delay line

The 4.43MHz subcarrier signal(fsc) sent from the subcarrier oscillating circuit is inputted to IC204-② pin, this frequency is quadrupled (4fsc) at IC204 and it is outputted to pin ⑤.

The 1H CCD delay line IC203 operates using the 4fsc signal as a clock signal. Thus the chroma signal that is delayed for 1H is outputted from IC202-④ pin.

This signal passes through the differential amplifier of Tr203 and Tr204, so that the clock component is removed by the low-pass filter consisting of C216, C218 and L204.

Then, the phase compensation is performed by the phase equalizer consisting of Tr205, L206, VC201 and R226.



(c) Chroma signal demodulation circuit

Only the chroma component is picked out from the signal outputted from IC201-④ pin by the band-pass filter consisting of L201, L202, L203, C201, C243 and C202, and it is inputted to ⑪ pin of the demodulator IC208.

The IC208 incorporates the band amplifier circuit, color hold circuit and demodulating circuit. The R-Y signal is outputted from ⑳ pin and the B-Y signal is outputted from ㉗ pin.

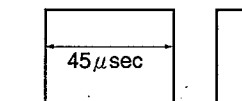
(3) Adjustment Procedure

Set VR206 (CHROMA LEVEL) to the center position.

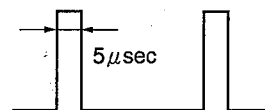
(a) VR303(HP WID)

VR304(EQ KILL)

- ① Apply a color bar signal.
- ② Connect the probe to TP306.
- ③ Adjust VR304 so that the pulse width is 45 μ sec.

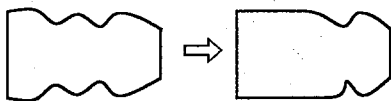


- ④ Connect the probe to TP307.
- ⑤ Adjust VR303 so that the pulse width is 5 μ sec.

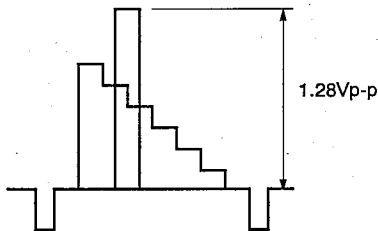


(b) **VR101(TRAP LEVEL)**
VR102(MATCH)
VR103(Y LEVEL)
VR104(APT OS)
L101 L102 L103

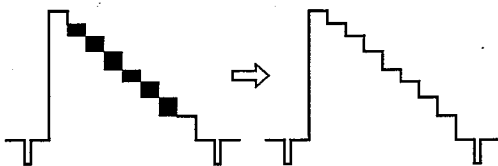
- ① Apply a sweep signal.
- ② Connect the probe to TP302.
- ③ Adjust VR102 so that the frequency characteristics of the low-to-middle frequency is flat.



- ④ Apply a color bar signal.
- ⑤ Adjust VR103 so that the level is 1.28Vp-p.

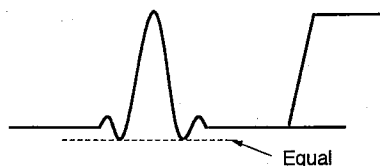


- ⑥ Set the **FORCED COLOR** switch on the pull-out panel to "ON" position.
- ⑦ Adjust VR101 so that the level is 1.28Vp-p.
- ⑧ Adjust L101 to eliminate the sub carrier.



- ⑨ Apply a sweep signal.
- ⑩ Set the **FORCED COLOR** switch to "OFF" position.
- ⑪ Set the aperture level to 20% with the **APT** preset switch on the pull-out panel. (Refer to 5-5(1) in the OPERATION MANUAL.)

- ⑫ Adjust VR104 so that the aperture starts taking effect.
- ⑬ Apply a 2T pulse signal.
- ⑭ Set the **FORCED COLOR** switch to "ON" position.
- ⑮ Adjust L102 and L103 so that the right pulse is the same level as the left pulse.



(c) **L201 L202**

- ① Apply a sweep signal.
- ② Connect the probe to TP202.
- ③ Adjust L201 and L202 so that the frequency characteristics of $4.43\text{MHz} \pm 0.5\text{MHz}$ is flat.

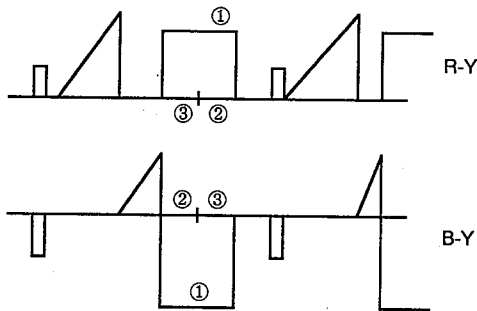
(d) **VR203(COLOR HOLD)**

- ① Apply a color bar signal.
- ② Set the **FORCED COLOR** and **SYNC EXT** switches respectively to "ON" position.
- ③ Connect the probe to TP304.
- ④ When attenuating a color bar signal to -50dB with the attenuator, adjust VR203 so that the color hold is obtained.

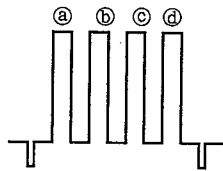
(e) **VR201(IH LEVEL) VR202**
VR204(S-LEVEL) VR205(R-Y MIX)
VR207(PHASE) VR208
VR301(R-Y LVL) VR302 (B-Y LVL)
VC201

- ① Apply a color bar signal.
- ② Set the oscilloscope mode to "ALT", and connect the probes to TP303 and TP304.
- ③ Adjust VC201 so that the line crawling of both the waveforms is minimum.
- ④ Apply a ANTI PAL signal.
- ⑤ Adjust VR201, VR202, VR207, VR208 and VC201 so that ① to ③ in the figure below are aligned.

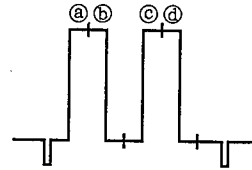
- ⑥ Repeat steps ① to ⑤.
If the ANTI PAL adjustment can not be done, adjust VR205.



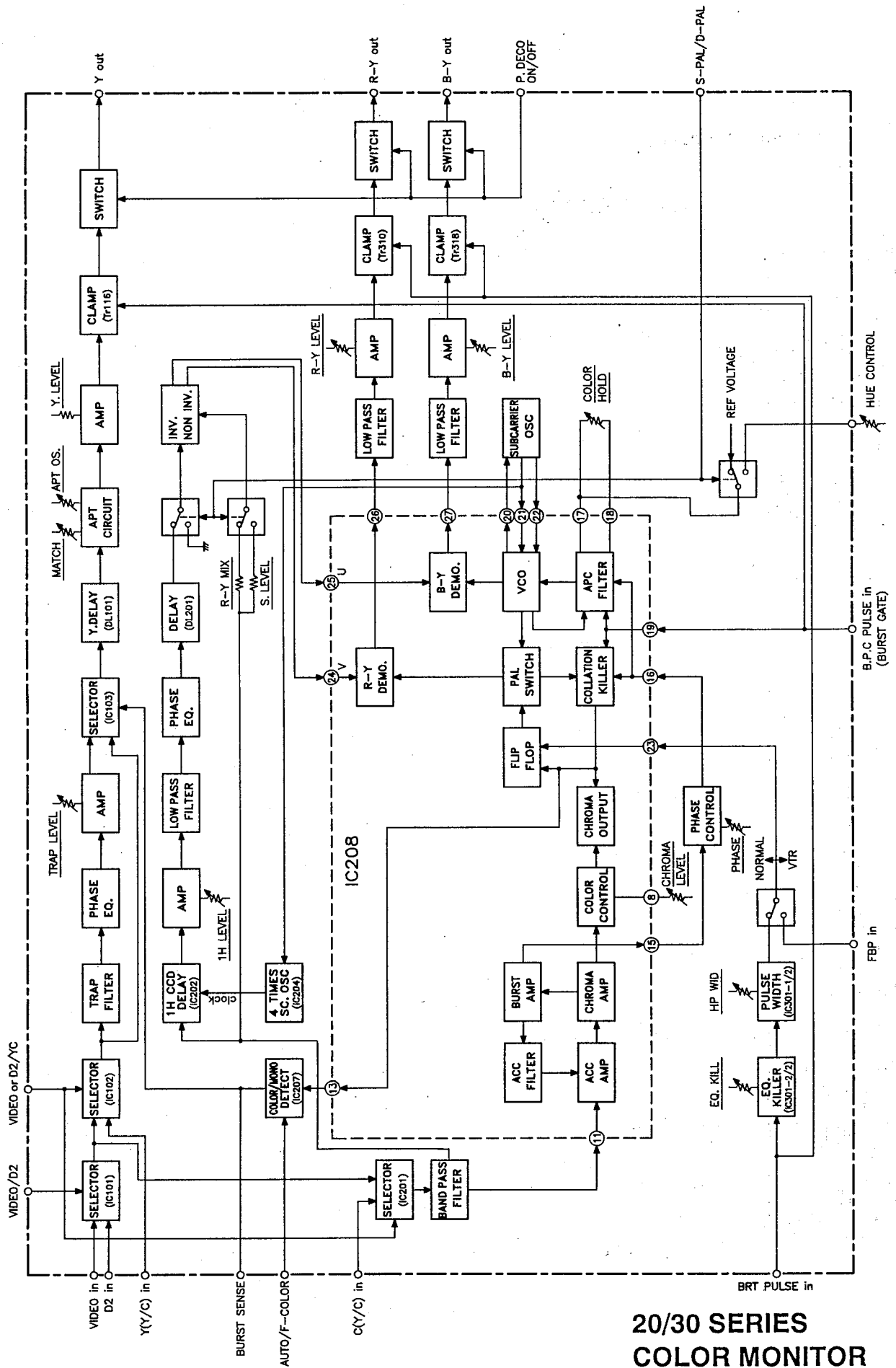
- ⑦ Apply a color bar signal.
⑧ Connect the probe to TP602 on the VIDEO OUT BOARD inserted into the SLOT No.4.
⑨ Adjust VR302 so that the level of portions ② to ④ in the figure below is the same.



- ⑩ Connect the probe to TP402 on the VIDEO OUT BOARD.
⑪ Adjust VR301 so that the level of portions ② to ④ in the figure below is the same.

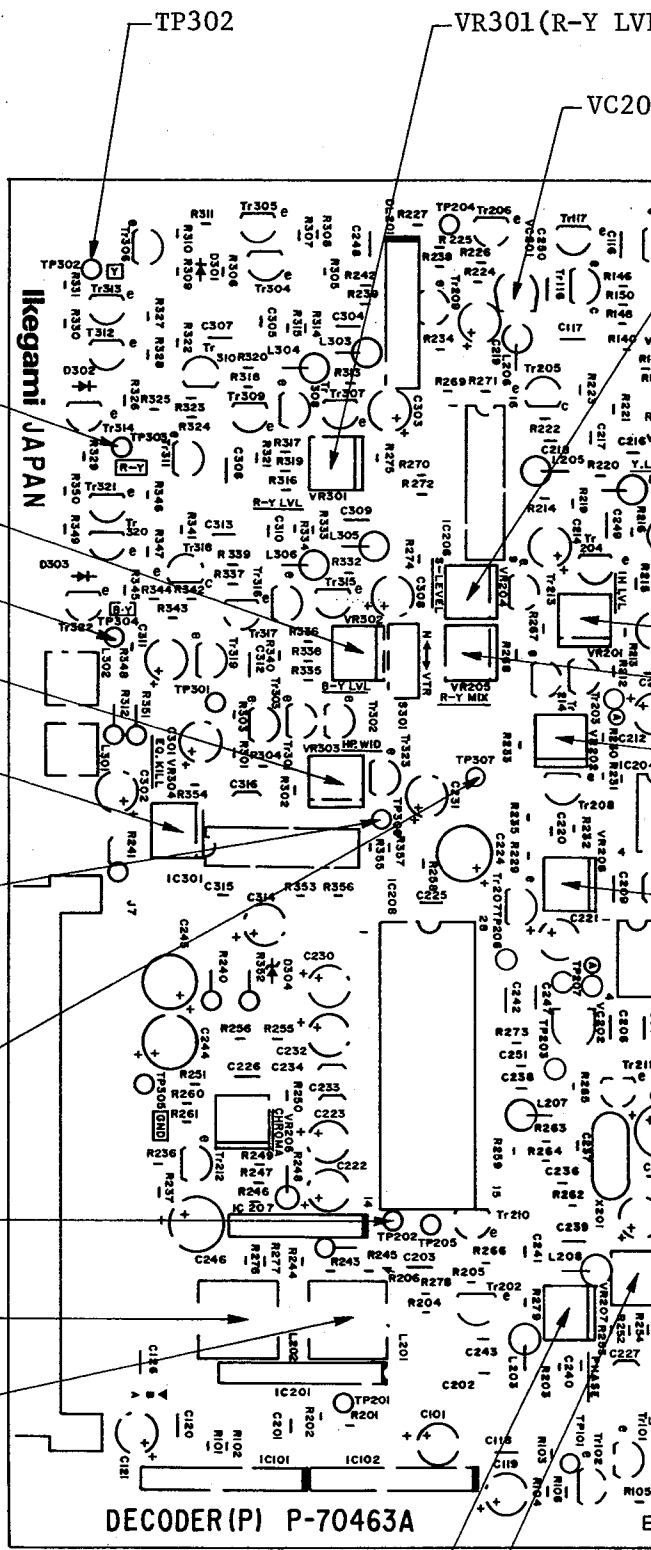


- ⑫ When the **S-PAL** switch is switched on/off, adjust VR204 so that the level of both the waveforms is the same.



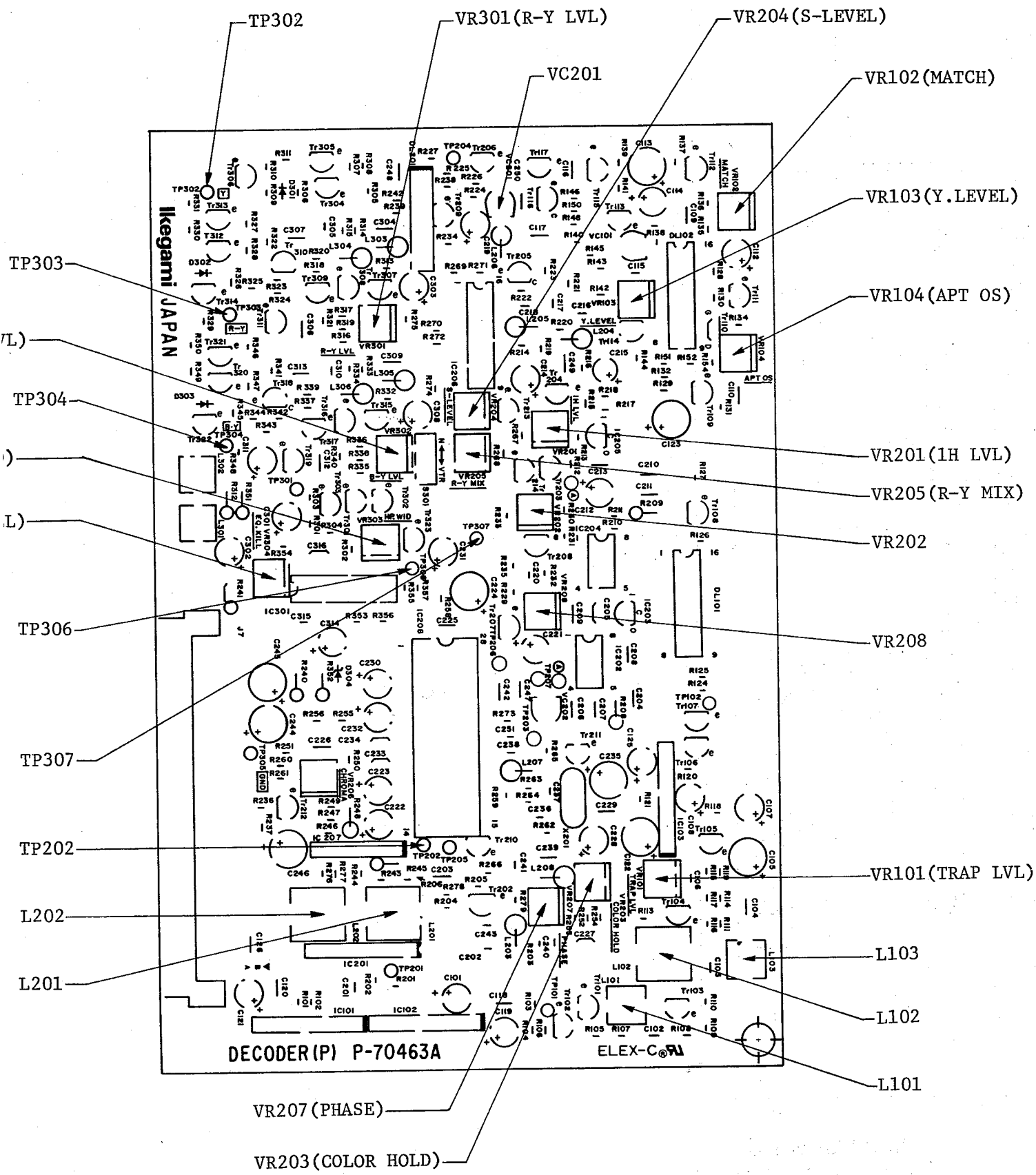
20/30 SERIES
 COLOR MONITOR
 DECODER(P) BOARD
 Block Diagram

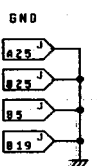
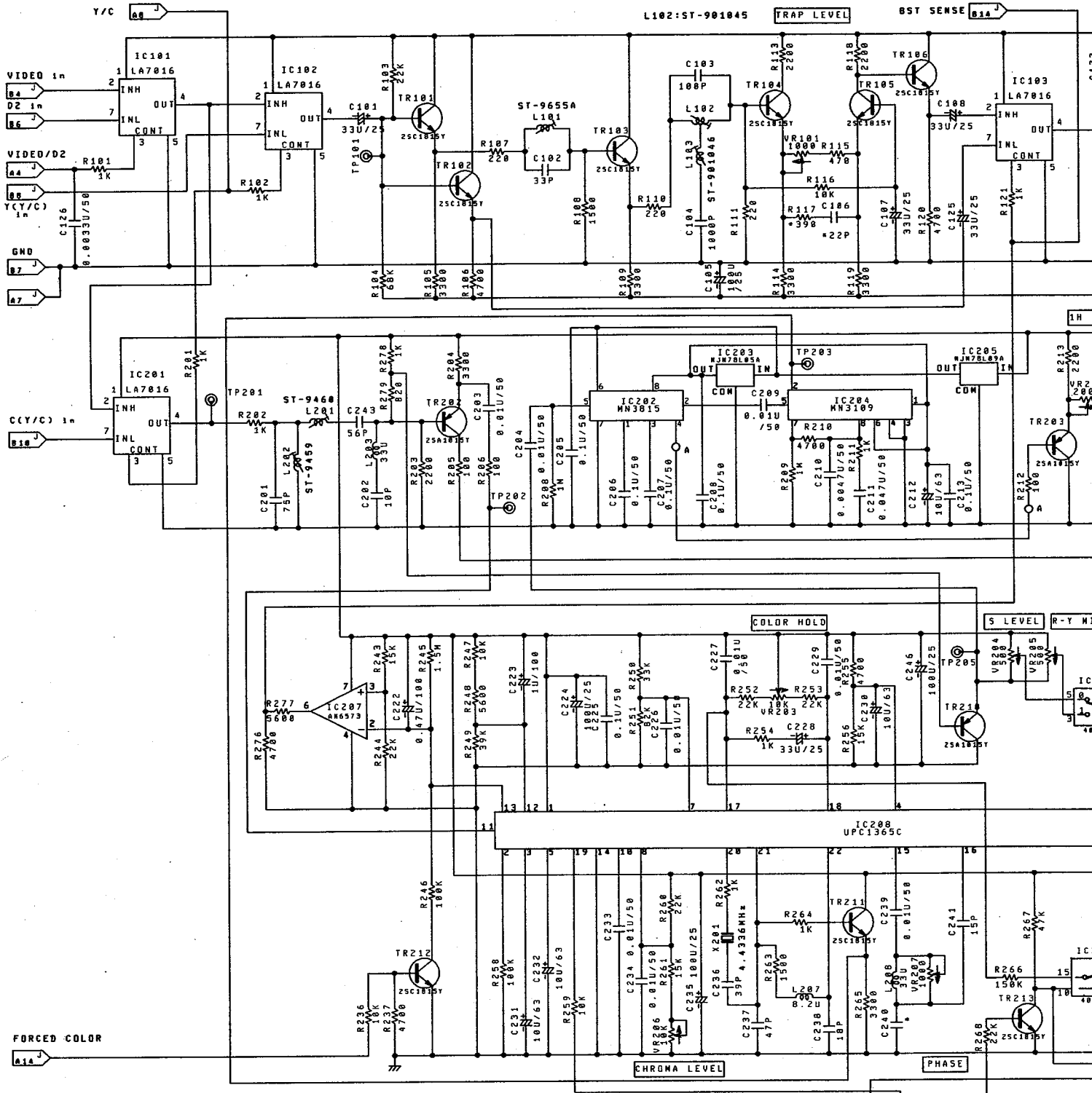
C4-904386



- TP302
- VR301 (R-Y LVL)
- VC20
- TP303
- VR302 (B-Y LVL)
- TP304
- VR303 (HP. WID)
- VR304 (EQ. KILL)
- TP306
- TP307
- TP202
- L202
- L201
- VR207 (PHASE)
- VR203 (COLOR HOLD)

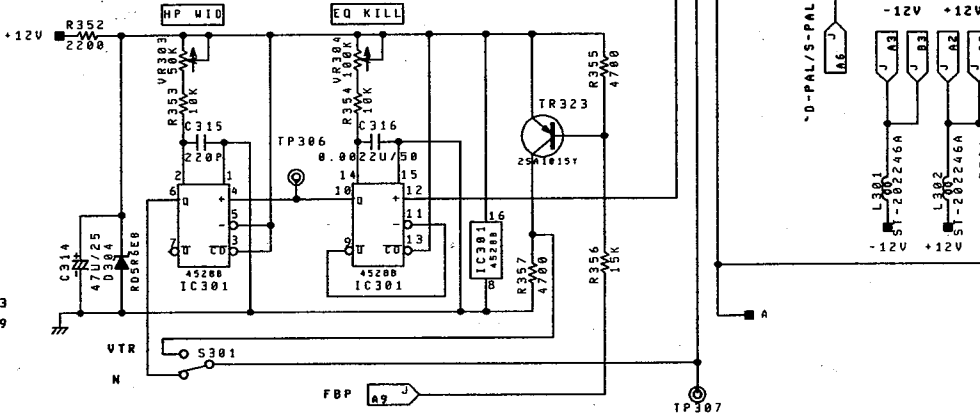
20/30 SERIES
 DECODER(P) BOARD
 PARTS LOCATION
 P-70463A



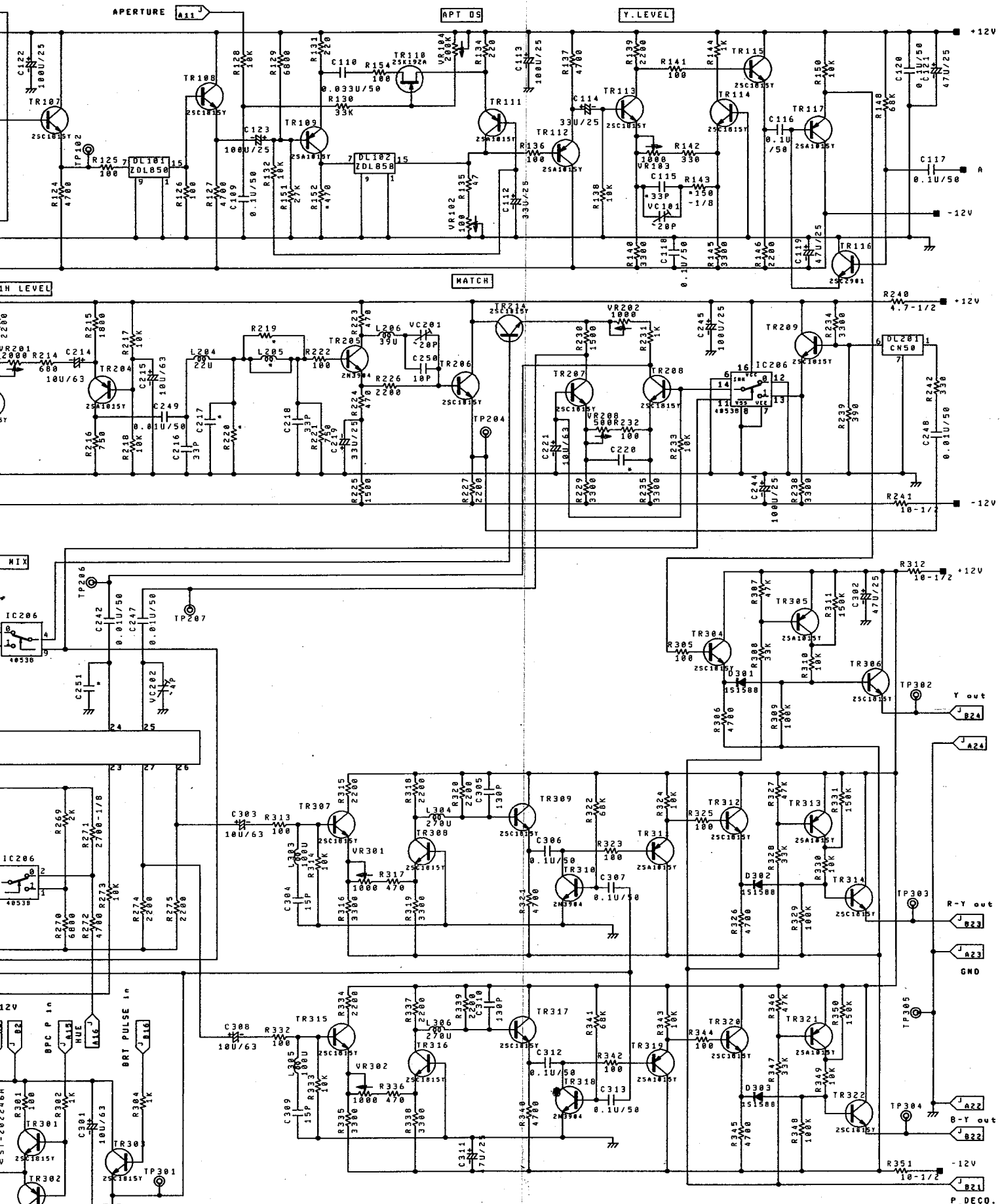


LAST NO.	IC 103	208	301
	DL 102	201	-
	Tr 117	214	323
	R 154	279	357
	C 126	251	316
	L 103	208	306
	VR 104	208	304
	D -	-	304
	TP 102	207	307

LOST NO.	R 112, 122, 123
	133, 147, 149
	153, 207
	228, 257
	C 111, 124
	Tr 201



NOTE: 1. All resistors are in ohms (Ω), 1K, 10K, 100K, 1M, 10M, 100M, 1G unless otherwise specified.
 2. All capacitors are in farads (F) unless otherwise specified.
 3. All inductors are in henries (H) unless otherwise specified.
 4. Waveforms are taken with a scope.



**20/30 SERIES
COLOR MONITOR
DECODER(P) BOARD
Schematic Diagram
C11-904264C**

5. Parts marked * are factory selected value.
6. Parts marked * are critical components for X-radiation.

5. Parts marked * are factory selected value.
6. Parts marked * are critical components for X-radiation.

as 5X (parts marked otherwise specified. Trade, 300V unless otherwise specified.)

as 5X (parts marked otherwise specified. Trade, 300V unless otherwise specified.)

2-6. VIDEO OUT BOARD (Fixed in SLOT No.4)

(1) Outline

The Y signal and the color difference signals are matrixed, the level of the R, G and B signals controlled, and these signals supplied to the RGB OUT BOARD.

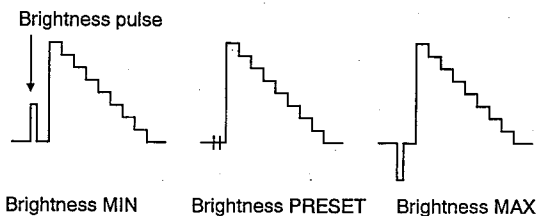
(2) Circuit Description

(a) Contrast, chroma control circuit

The Y signal that is outputted from each circuit is subject to gain control by IC201 according to the contrast level. The R-Y and B-Y signals are subject to gain control at IC101 and IC301 respectively according to the level attained by multiplying the chroma level by the contrast level at IC701.

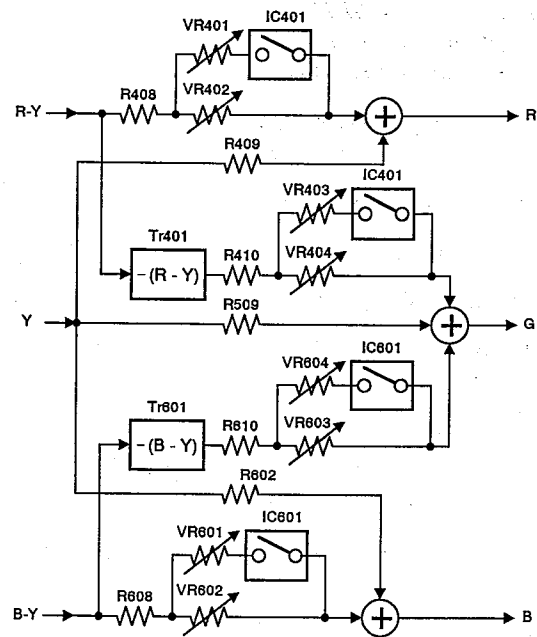
(b) Brightness addition circuit

The Y signal that is subject to contrast control is clamped to GND by Tr208 and a brightness pulse is added to the SYNC portion by means of the analog switch of IC102(2/3).



(c) RGB matrix circuit

The R signal and B signal are produced from the R-Y signal and B-Y signal by the resistance matrix with the Y signal. The G signal is produced by the respective resistance matrixes of the Y signal, -(R-Y) signal and -(B-Y) signal. When the matrix switch is turned on, the analog switch of IC401 and IC601 are turned on to change the matrix ratio, highlighting the red.



(d) Gain control circuit

The brightness pulse portion of each separated R, G and B signal is clamped by Tr407, 507 and 607 and mixed with a character signal and a safe title signal by means of the analog switch of IC402. Then, it is subject to gain control at IC403, 503 and 603 according to the gain level so that each signal is supplied to the RGB OUT BOARD.

(3) Adjustment Procedure

Be sure to set the following condition before the adjustment.

- ① Apply a 75% color bar signal of YPbPr (Y: 0.7Vp-p, Pb,Pr: 0.525Vp-p) to the AUX input terminals.
- ② Set the "RGB/YPbPr" selection to the "YPbPr" mode on the MENU screen. Then set the "YPbPr MODE" selection to the "MODE 1" mode. (Refer to 5-6 in the OPERATION MANUAL for details on the MENU setting method.)
- ③ Set the **AUX** switch on the front panel to "ON" position.
- ④ Set the CHROMA to 50% (preset) with the **CHROMA** switch on the pull-out panel.

(a) VR701(CHROMA LEVEL)

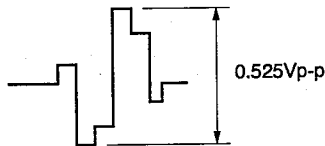
- ① Set VR701 to the center position as the first setting.

**(b) VR702(OFF SET)
VR703(CONT O.S)
VR704(CHROMA O.S)**

- ① Set the **CHROMA** and **CONT** switches on the front panel to the "MANUAL" state.
- ② Connect the probe to TP701.
- ③ Set the CHROMA and CONTRAST to MIN.
- ④ Adjust VR702 so that the DC voltage is 0V.
- ⑤ Set the CHROMA to MAX and set the CONTRAST to MIN.
- ⑥ Adjust VR703 so that the DC voltage is 0V.
- ⑦ Set the CHROMA to MIN and set the CONTRAST to MAX.
- ⑧ Adjust VR704 so that the DC voltage is 0V.
- ⑨ Repeat ③ to ⑧.

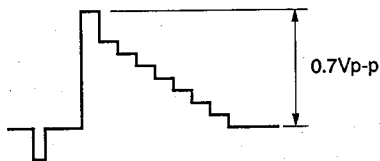
(c) VR101(R-Y OPT LVL)

- ① Connect the probe to TP105.
- ② Adjust VR101 so that the R-Y signal level is 0.525Vp-p.



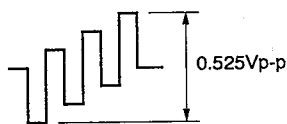
(d) VR201(Y OPT LVL)

- ① Connect the probe to TP205.
- ② Adjust VR201 so that the Y signal level is 0.7Vp-p.



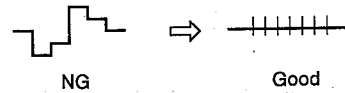
(e) VR301(B-Y OPT LVL)

- ① Connect the probe to TP305.
- ② Adjust VR301 so that the B-Y signal level is 0.525Vp-p.

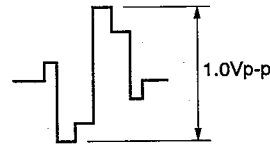


**(f) VR102(R-Y OFFSET)
VR103(R-Y GAIN)**

- ① Connect the probe to TP102.
- ② Set the **CHROMA** switch on the front panel to the "MANUAL" state and set the CHROMA to MIN.
- ③ Adjust VR102 to eliminate the signal component as shown in the figure below.

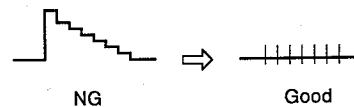


- ④ Set the **CHROMA** switch to the "PRESET" state. And set the **CONT** switch to the "MANUAL" state and set the CONTRAST to MAX.
- ⑤ Adjust VR103 so that the level is 1.0Vp-p.

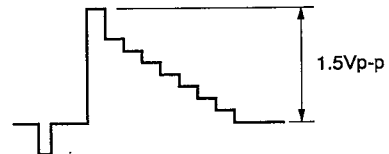


**(g) VR202(Y OFFSET)
VR203(Y GAIN)**

- ① Connect the probe to TP202.
- ② Set the **CONT** switch to the "MANUAL" state and set the CONTRAST to MIN.
- ③ Adjust VR202 to eliminate the signal component as shown in the figure below.



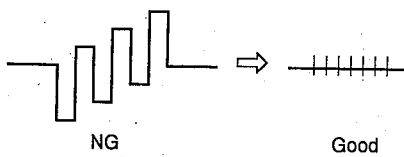
- ④ Set the CONTRAST to MAX.
- ⑤ Adjust VR203 so that the level is 1.5Vp-p.



**(h) VR302(B-Y OFFSET)
VR303(B-Y GAIN)**

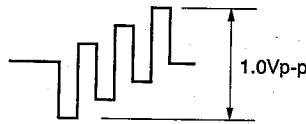
- ① Connect the probe to TP302.
- ② Set the **CHROMA** switch to the "MANUAL" state and set the CHROMA to MIN.

- ③ Adjust VR302 to eliminate the signal component as shown in the figure below.



- ④ Set the **CHROMA** switch to the "PRESET" state. And set the **CONT** switch to the "MANUAL" state and set the CONTRAST to MAX.

- ⑤ Adjust VR303 so that the level is 1.0Vp-p.

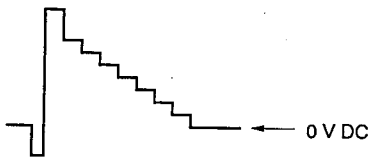


- (i) **VR105(R-Y DC)**
VR205(Y DC)
VR305(B-Y DC)

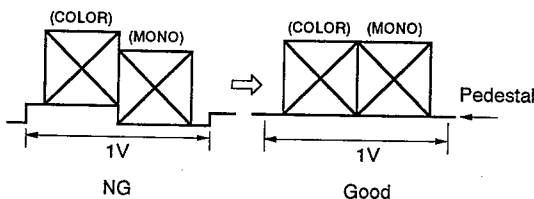
- ① Set the **CONT** and **CHROMA** switches to the "PRESET" state.

- ② Connect the probe to TP203.

- ③ Adjust VR205 so that the pedestal DC voltage is 0V.



- ④ Connect the probe to TP401.
⑤ Press the **MONO** switch for 2 seconds to get the "SPLIT" state.
⑥ Adjust VR105 so that the pedestal level of COLOR part is the same as that of MONO part.

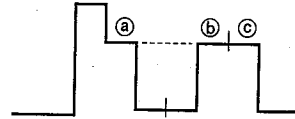


- ⑦ Connect the probe to TP601.
⑧ Adjust VR305 in the same manner as ⑥.

- (j) **VR402(R.BAL)**

- ① Connect the probe to TP401.

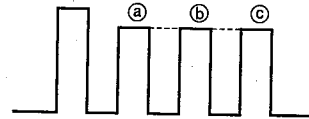
- ② Adjust VR402 so that the level of (a), (b), and (c) is the same in the figure below.



- (k) **VR602(B. BAL)**

- ① Connect the probe to TP601.

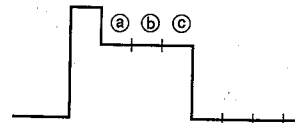
- ② Adjust VR602 so that the level of (a), (b), and (c) is the same in the figure below.



- (l) **VR404(GR. BAL)**
VR603(GB. BAL)

- ① Connect the probe to TP501.

- ② Adjust VR404 and VR603 so that the level of (a), (b), and (c) is the same in the figure below.



- (m) **VR406(RC SET)**
VR506(GC SET)
VR606(BC SET)

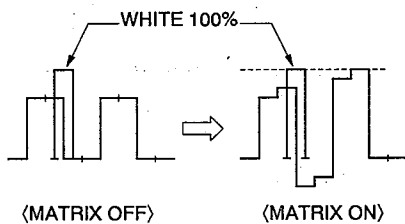
- ① Connect the probe to TP402.

- ② Adjust VR406 so that the DC voltage of the pedestal is 1.7V.

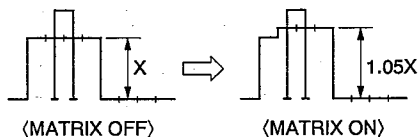
- ③ In the same manner, adjust VR506 at TP502 and adjust VR606 at TP602.

- (n) **VR401(MTX R BAL)**
VR403(MTX GR BAL)
VR601(MTX B BAL)
VR604(MTX GB BAL)

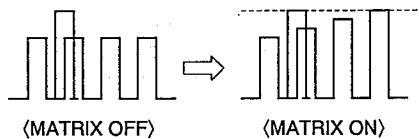
- ① Connect the probe to TP402.
- ② Adjust VR401 to attain the figure below with the **MATRIX IN** switch "ON"



- ③ Connect the probe to TP502.
- ④ Adjust VR403 and VR604 to attain the figure below with the **MATRIX IN** switch "ON".



- ⑤ Connect the probe to TP602.
- ⑥ Adjust VR601 to attain the figure below with the **MATRIX IN** switch "ON".

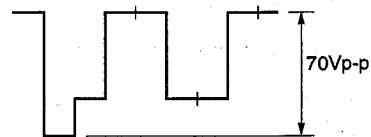


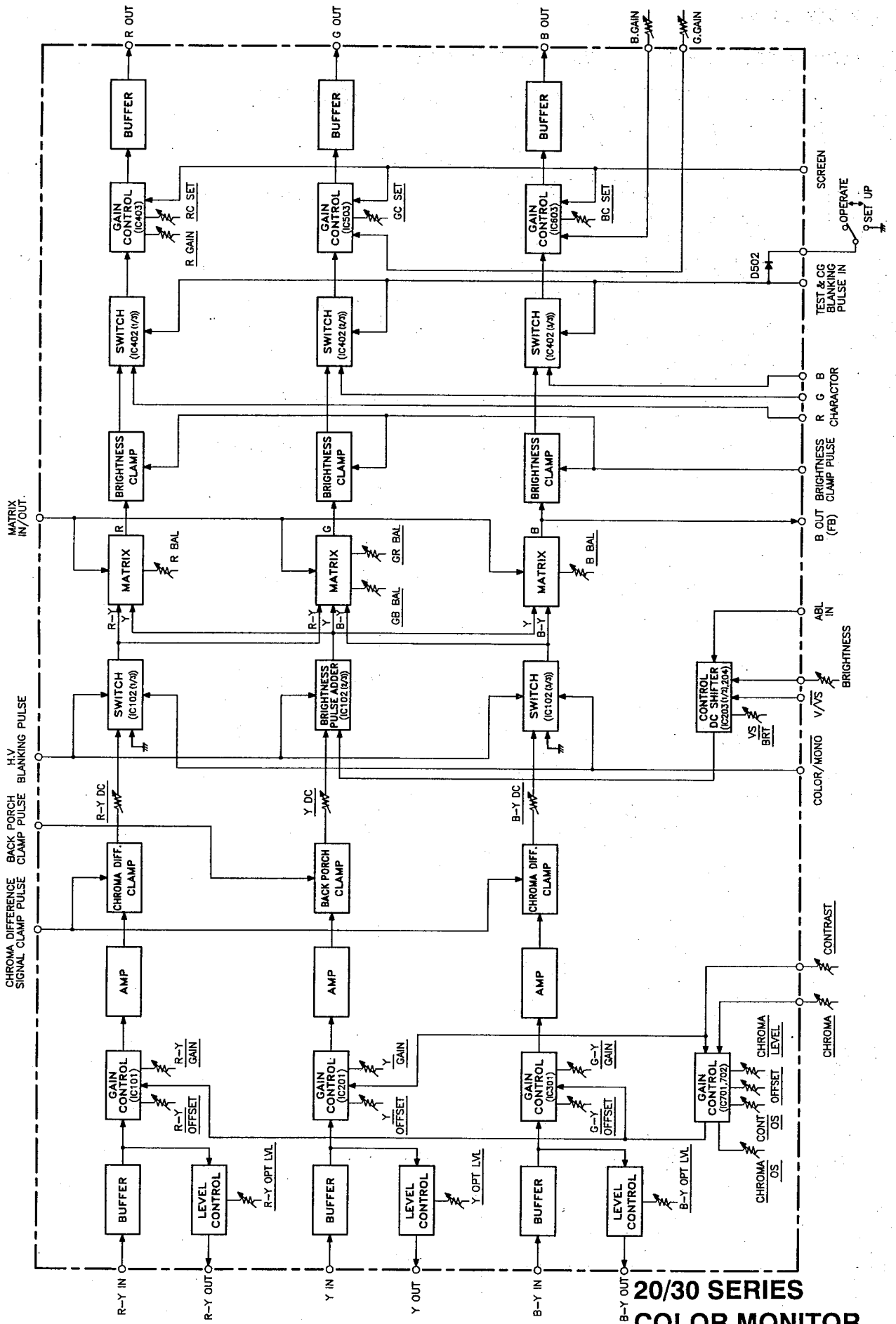
(o) **VR204(VS BRT)**

- ① Connect the probe to TP203.
- ② Set the "V/VS" selection to the "V" mode on the MENU screen. (Refer to 5-6 in the OPERATION MANUAL for details on the MENU setting method.)
- ③ Set the brightness pulse to the pedestal position with the **BRIGHT** control on the front panel.
- ④ Set the "V/VS" selection to the "VS" mode.
- ⑤ Adjust VR204 to set the brightness pulse to the set-up position.

(p) **VR405(R. GAIN)**

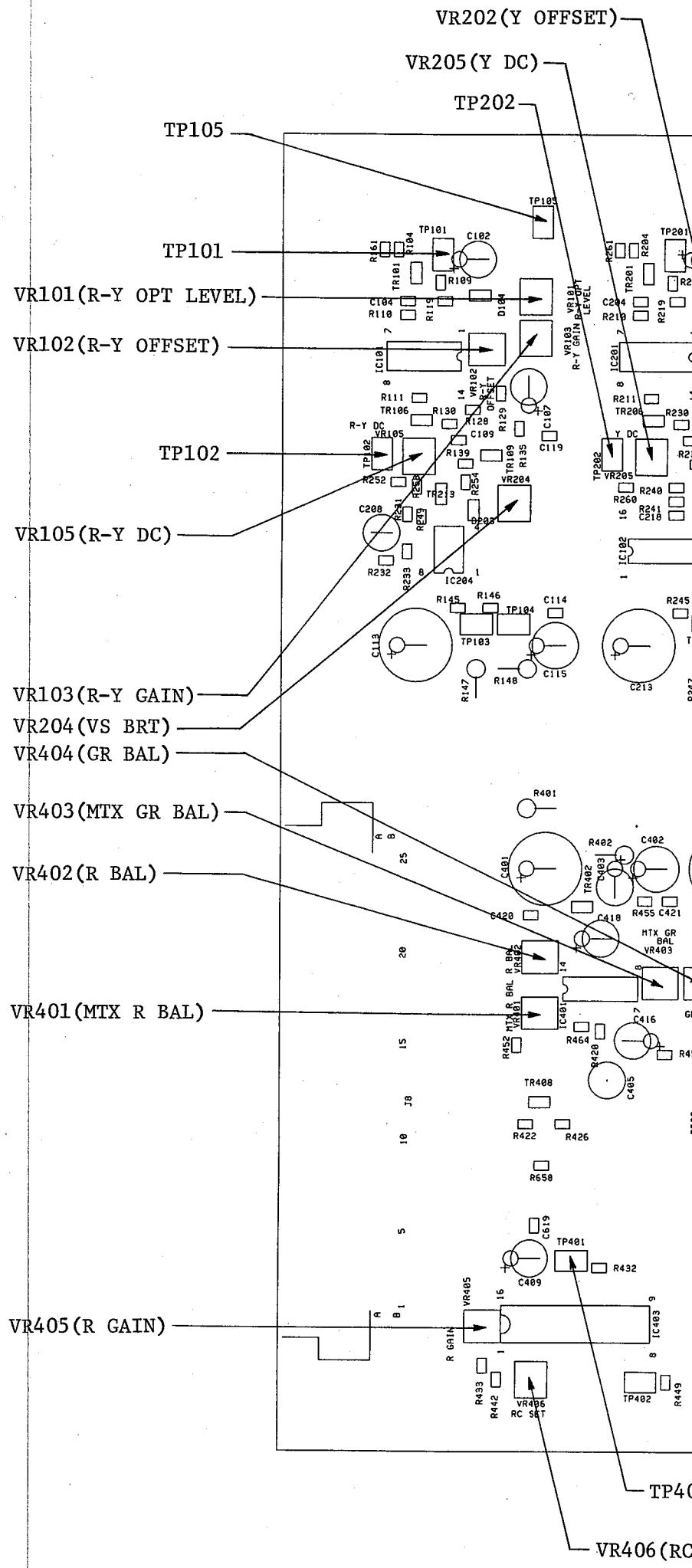
- ① Connect the probe to TP101 on the RGB OUT BOARD.
- ② Set the **CHROMA** switch to the "PRESET" state. And set the **CONT** switch to the "MANUAL" state and set the CONTRAST to MAX.
- ③ Adjust VR405 so that the level is 70Vp-p.

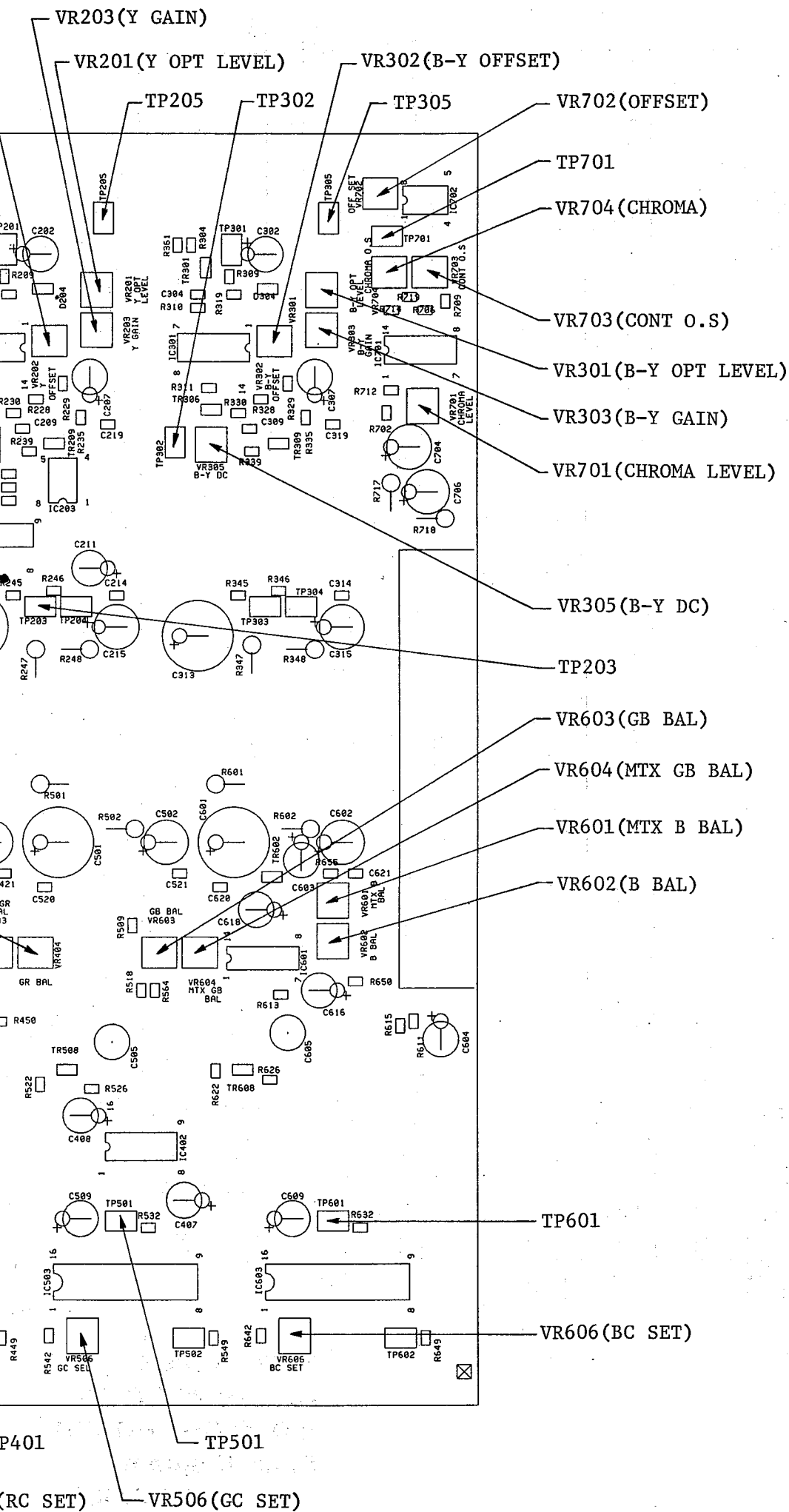


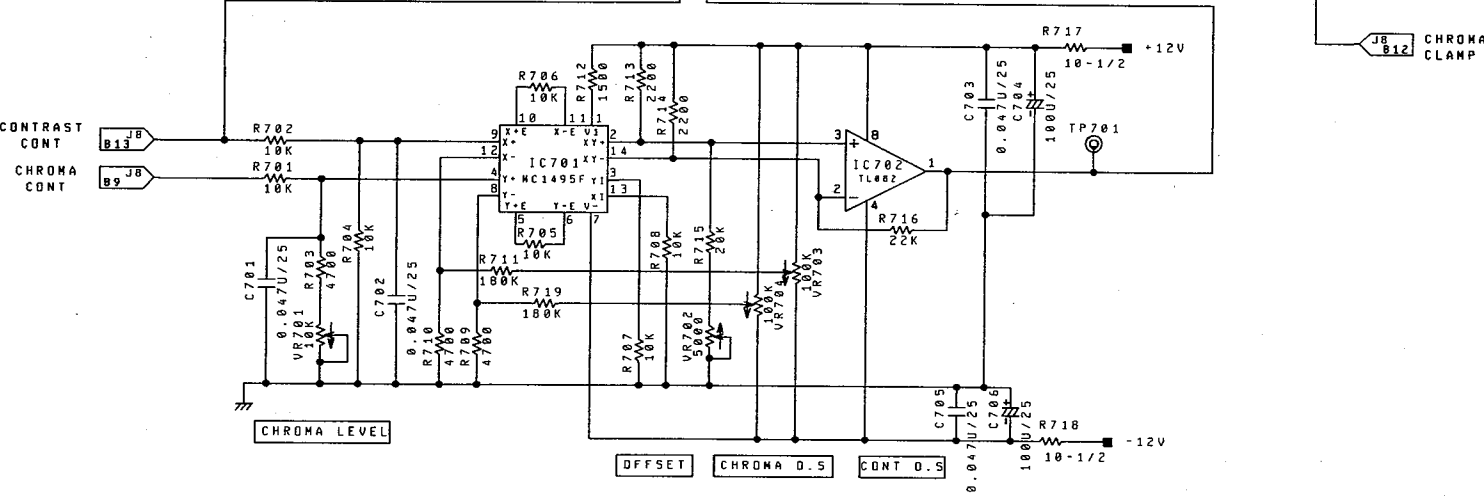
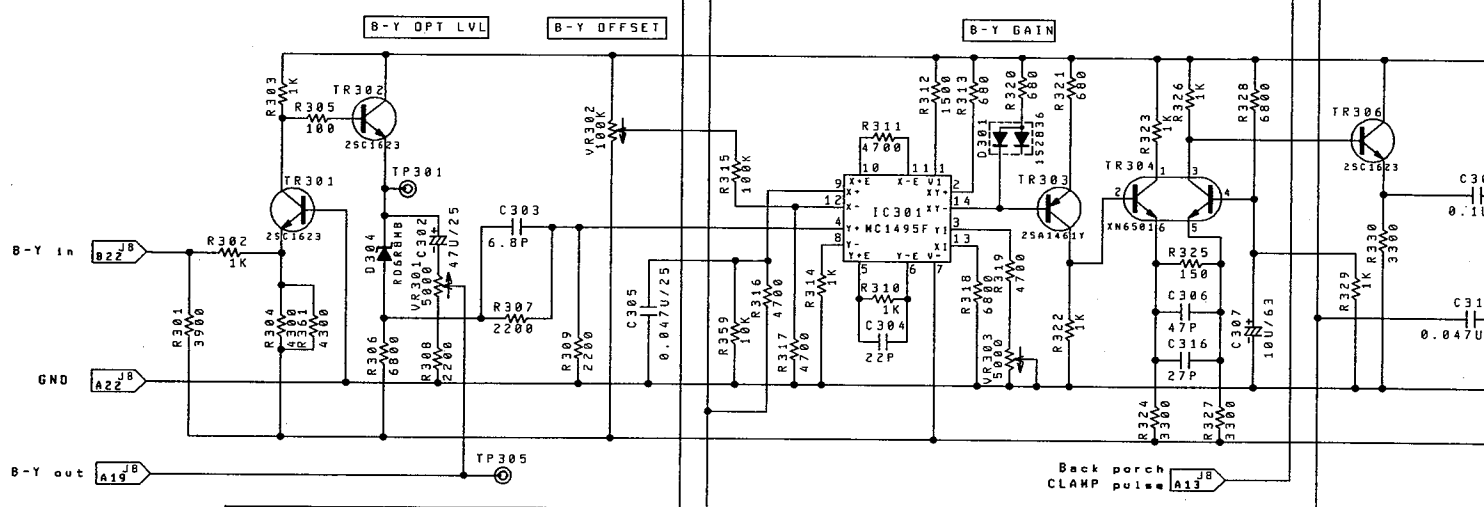
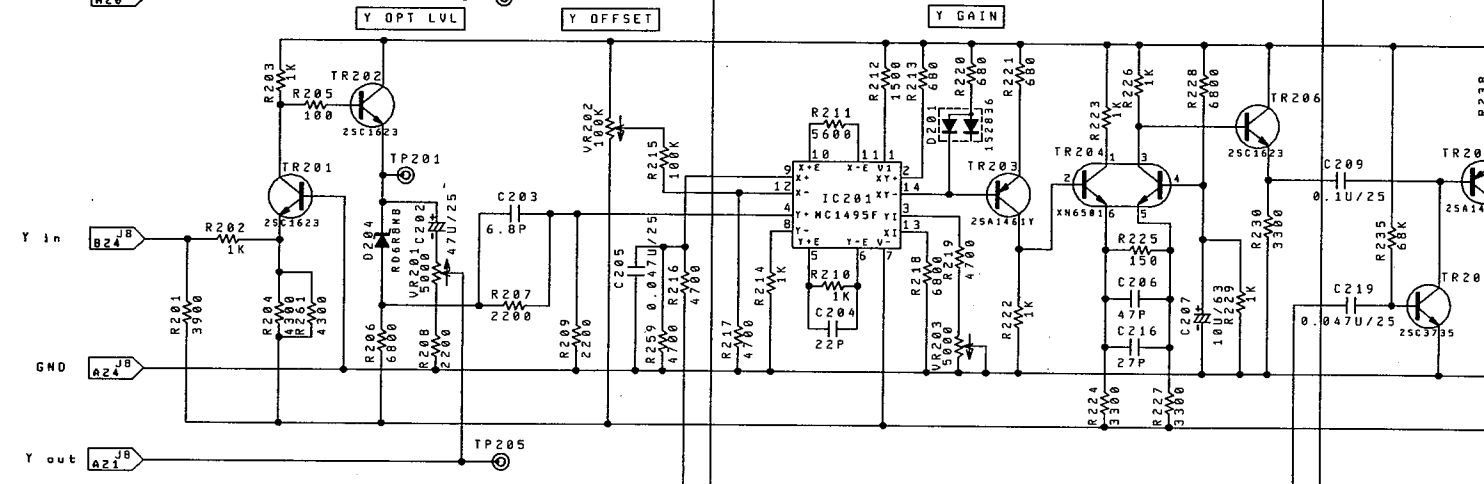
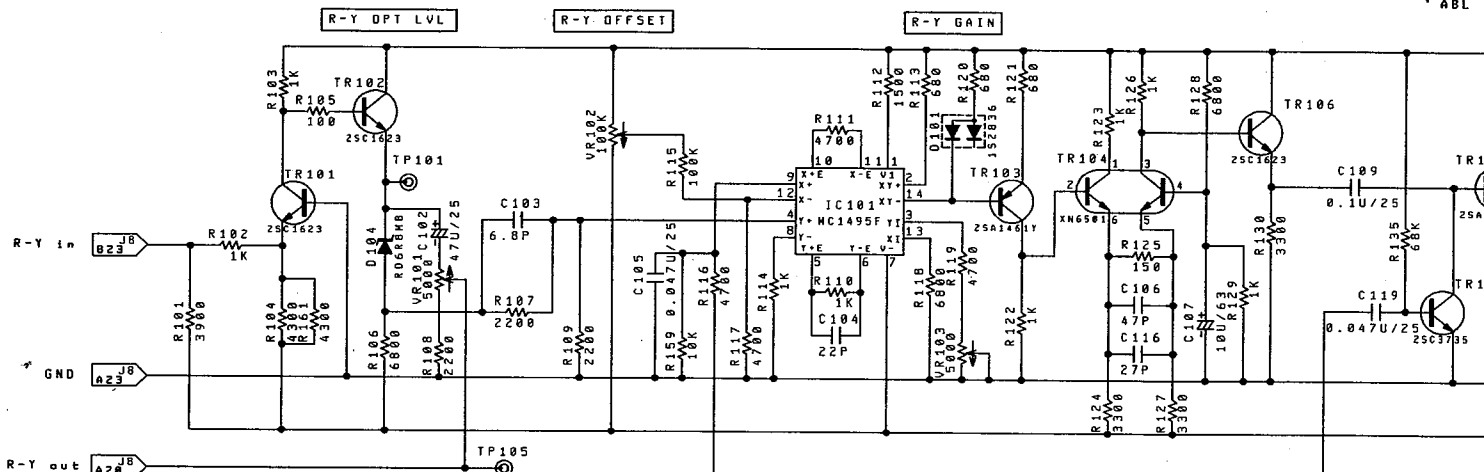


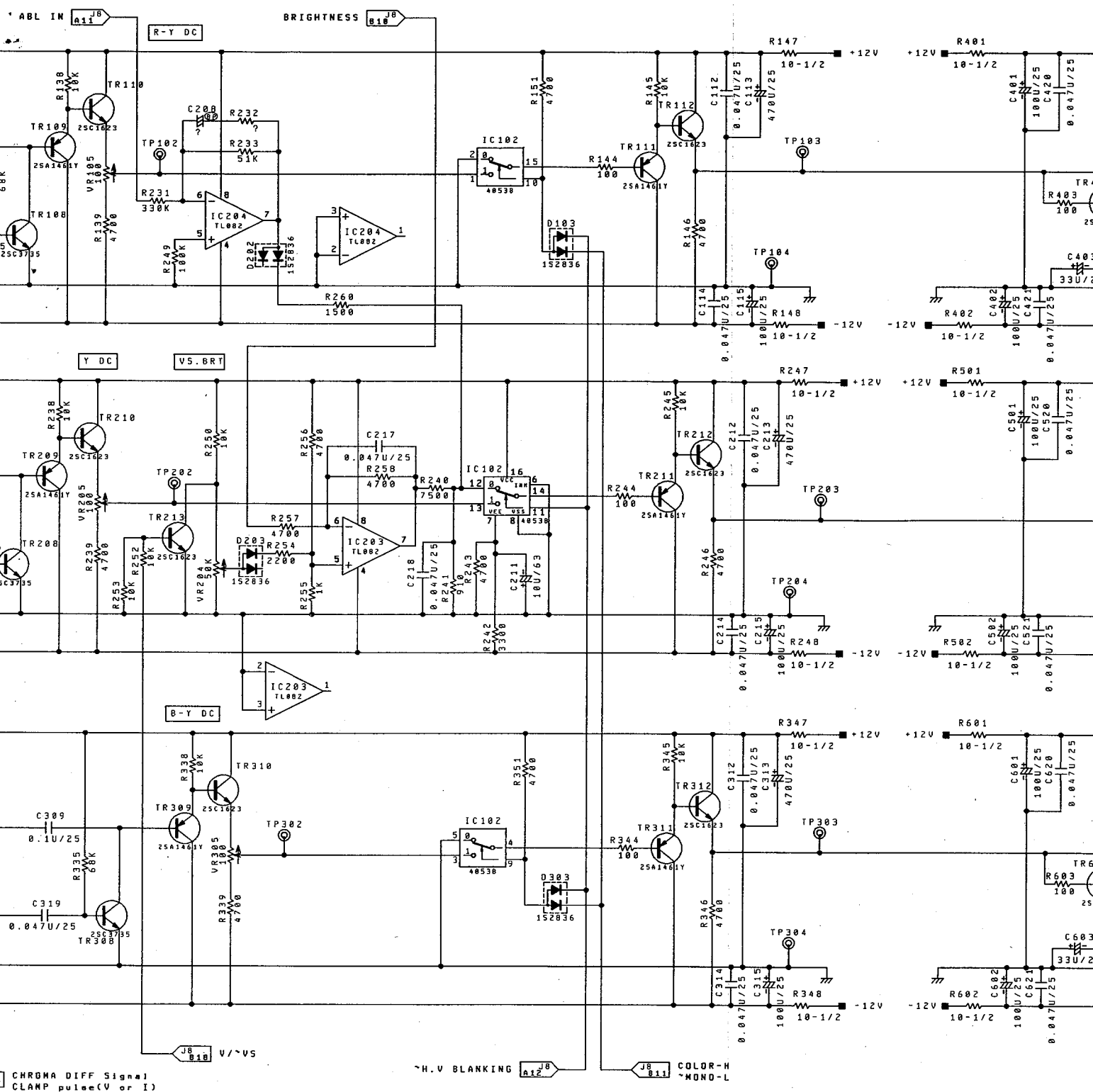
**20/30 SERIES
COLOR MONITOR
VIDEO OUT BOARD
Block Diagram
C2-904328**

**20/30 SERIES
VIDEO OUT BOARD
PARTS LOCATION
PC-2228**



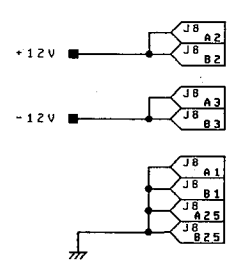






CHROMA DIFF Signal
CLAMP pulse(V or I)

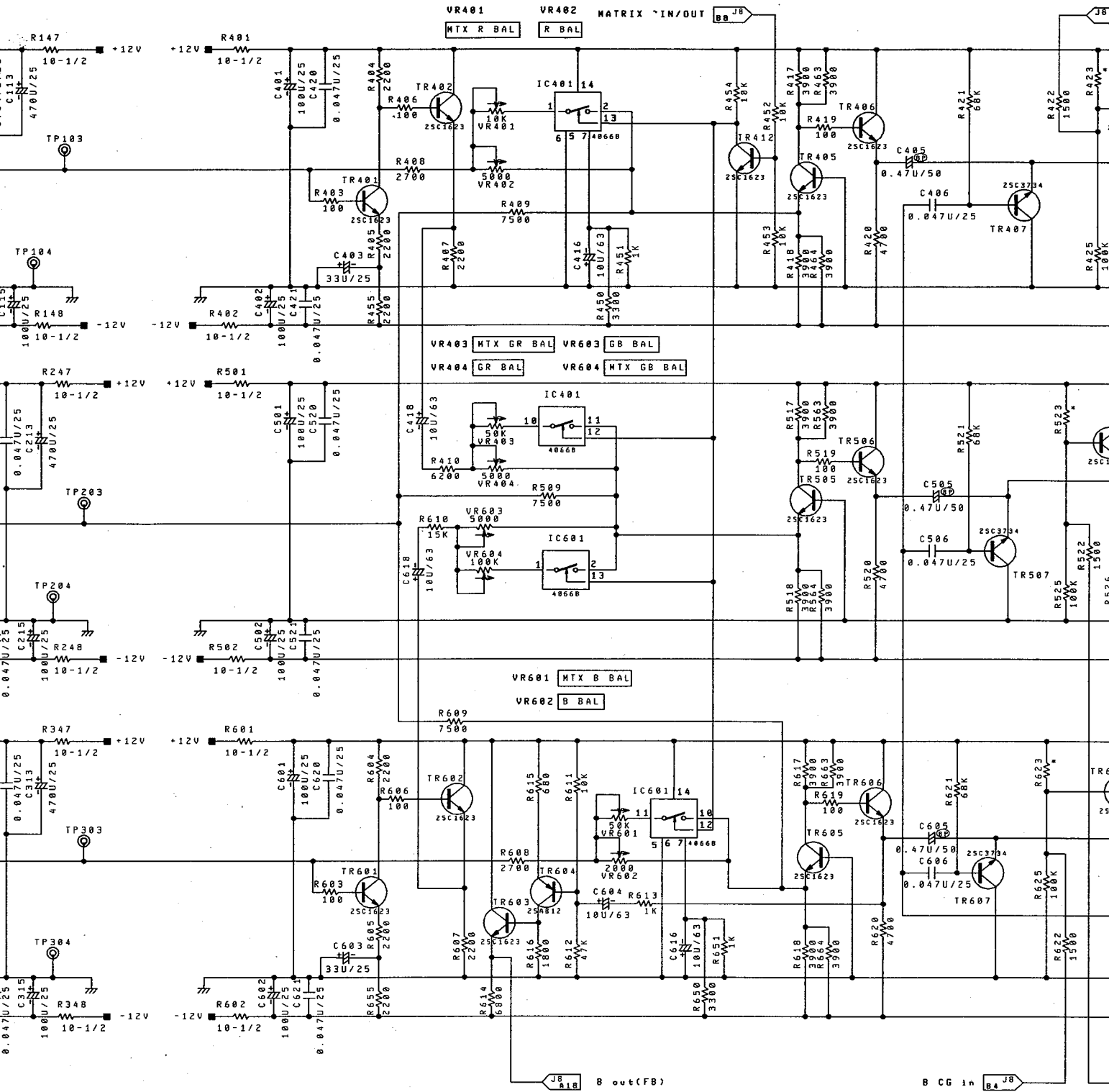
H.V. BLANKING A12 COLOR-H B11 MOND-L



?	20SERIES	30SERIES
R232	330K	22K
C208	1U/50	10U/25

LAST NO.						
IC	103	204	303	403	503	603 702
Tr	112	213	312	416	516	616
VR	105	205	305	406	506	606 704
D	104	204	304	401	502	601
R	161	261	361	464	564	664 719
C	119	219	319	421	521	621 706
TP	105	205	305	402	502	602 701

LOST NO.						
IC	103	202	302	303		
VR	104	304	501	505		
Tr	105	107	205	207		
	403	404	413	416		
	501	504	512	514		
	612	613				
R	131	134	136	137		
	150	152	158	166		
	234	236	237	251		
	331	334	336	337		
	350	352	358	366		
	411	416	424	444		
	503	508	510	516		
	533	544	550	555		
	624	627	630	633		
	656	657				



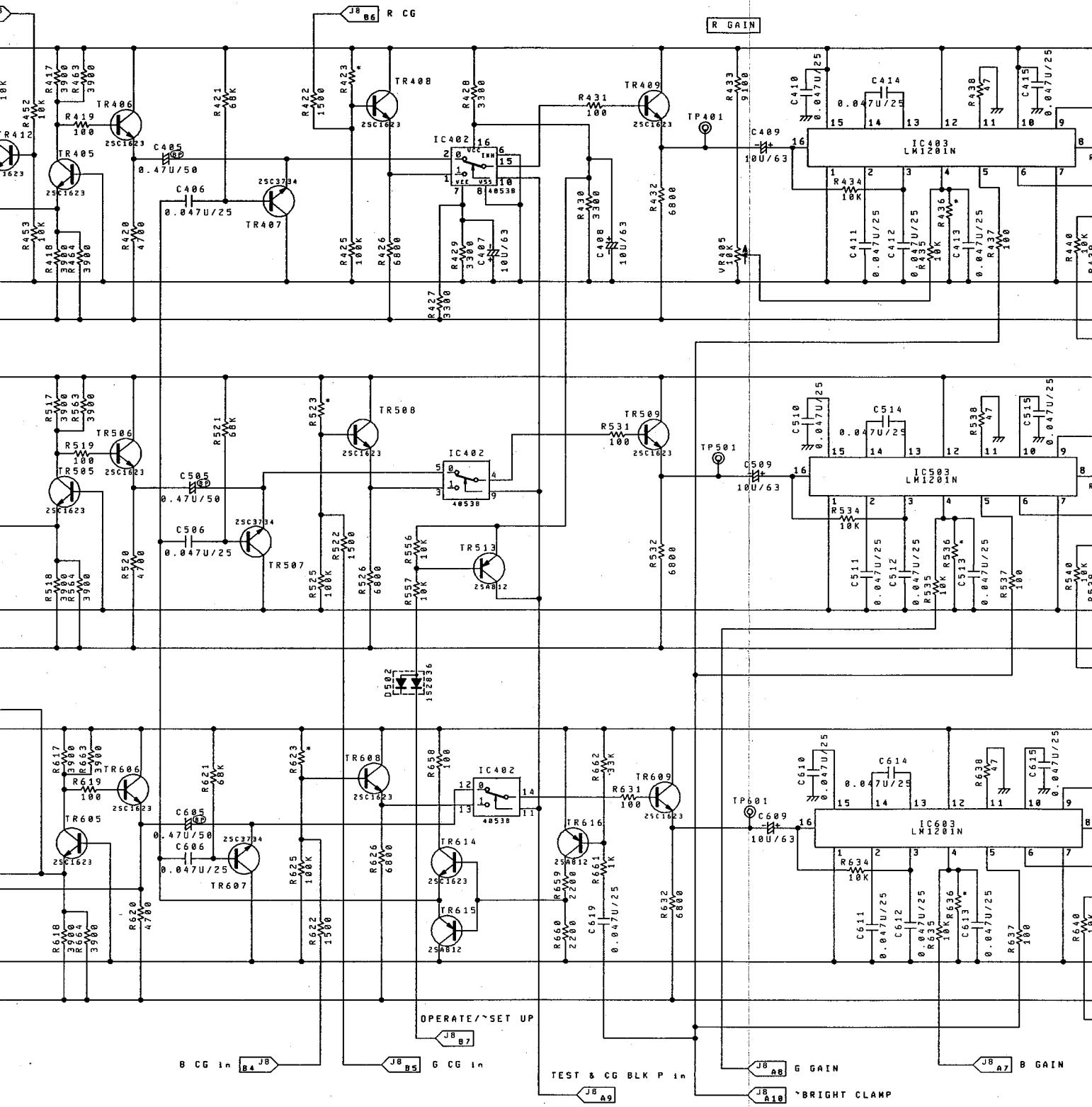
LOST NO.

403	503	603	702	IC	103, 202, 302, 303, 501, 502, 602
416	516	616		VR	184, 304, 501-505, 605
406	506	606	704	Tr	105, 107, 205, 207, 305, 307
401	502	601			403, 404, 413-416
464	564	664	719		501-504, 512, 514-516
421	521	621	706		612, 613
402	502	602	701	R	131-134, 136, 137, 140-143, 149
					150, 152-150, 160
					234, 236, 237, 251,
					331-334, 336, 337, 340-343, 349
					350, 352-358, 360
					411-416, 424, 444, 456-462
					503-508, 510-516, 524, 527-530
					533, 544, 550-555, 558-562
					624, 627-630, 633, 644, 652-654
					656, 657

C	101, 108, 110, 111, 117, 118, 201, 210
	301, 308, 310, 311, 317, 318
	404, 417, 419
	503, 504, 507, 508, 516-519, 607, 608, 617
D	102, 302

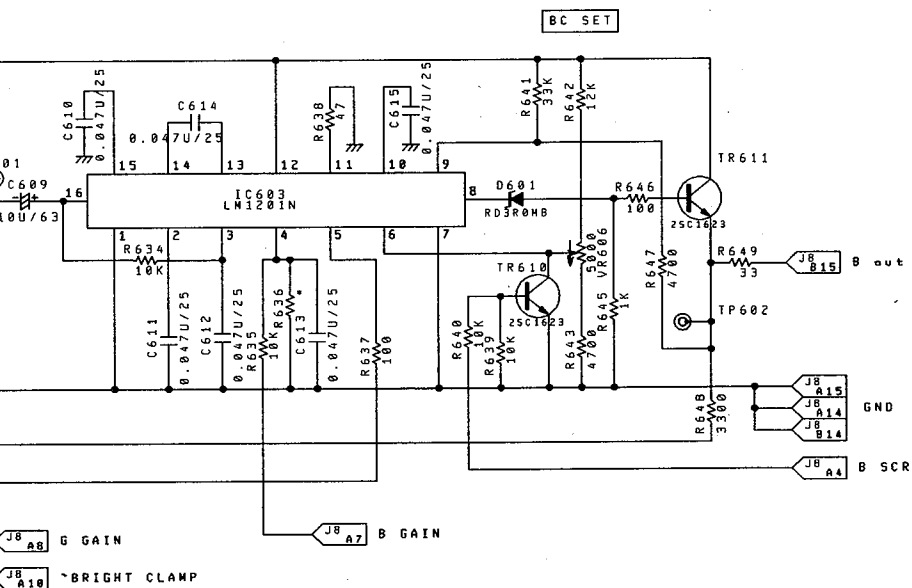
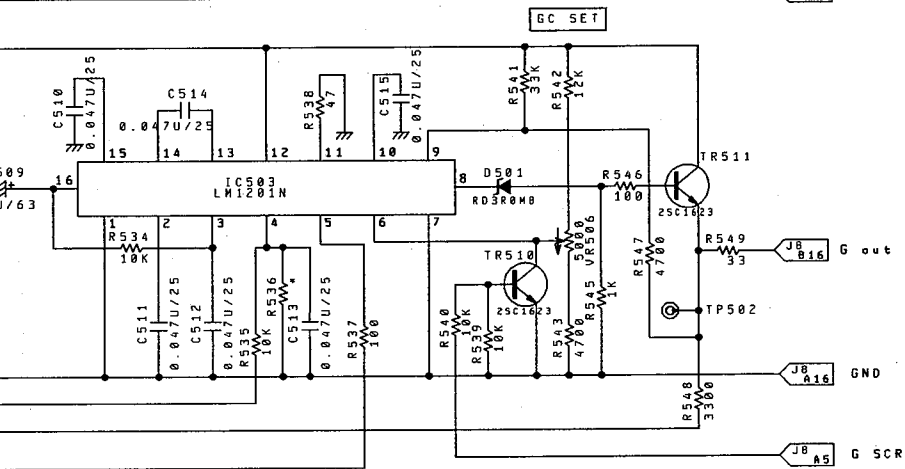
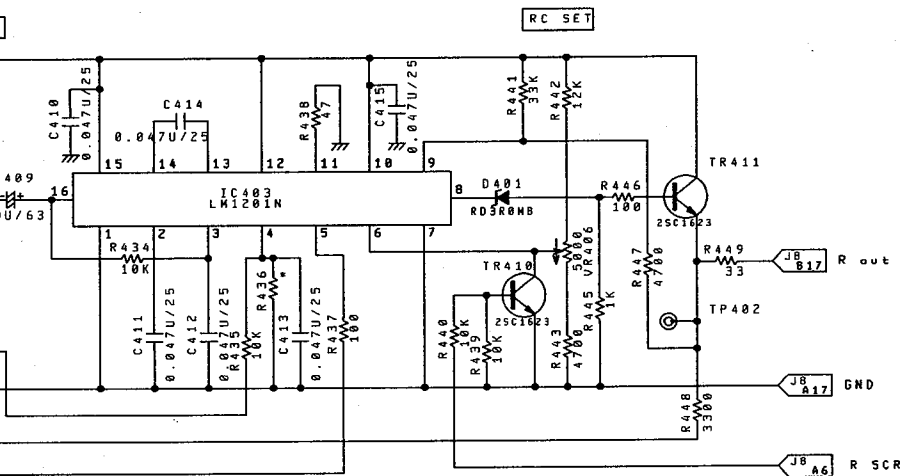
NOTE:

1. All resistors are in F:1%, 1/10 watt unless otherwise specified.
2. All capacitors are in otherwise specified.
3. All inductors are in specified.
4. Waveforms are taken w input.
5. Parts marked * are f
6. Parts marked * are c for X-radiation.



NOTE:

1. All resistors are in ohms 5%(parts marked F:1%), 1/10watt unless otherwise specified.
2. All capacitors are in farads, 300V unless otherwise specified.
3. All inductors are in henly unless otherwise specified.
4. Waveforms are taken with a color bar signal input.
5. Parts marked * are factory selected value.
6. Parts marked * are critical components for X-radiation.



**20/30 SERIES
COLOR MONITOR
VIDEO OUT BOARD
Schematic Diagram
C11-904379**

0-4. RGB OUT BOARD

(1) Outline

The R, G, and B signals supplied from the VIDEO OUT BOARD pass through the beam feedback clamp circuit, are amplified by the output amplifier circuit, and are applied to the cathode of the CRT.

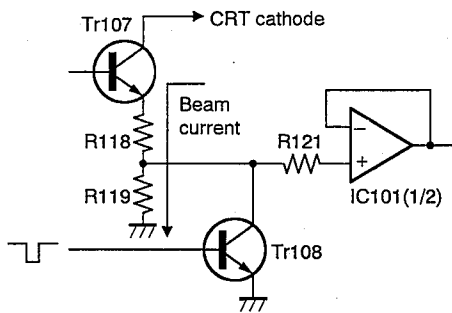
(2) Circuit Description

(a) Beam feedback circuit

Since each channel R, G and B has the same circuit, the circuit description given in this paragraph is of the R channel alone.

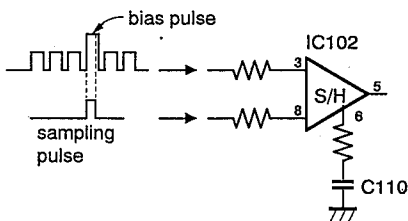
When the clamp pulse is "L", Tr108 is turned off. At this time, the bias pulse inputted to G1 of CRT flows from the cathode of CRT to R118 and R119 as a beam current.

This current is detected as a voltage at R119.



The voltage detected at R119 is inputted to the sample & hold IC (IC102) after passing through the voltage follower of IC101 (1/2).

Only the voltage of the bias pulse section is sampled at IC102, held at C110 and outputted to IC102-⑤ pin.



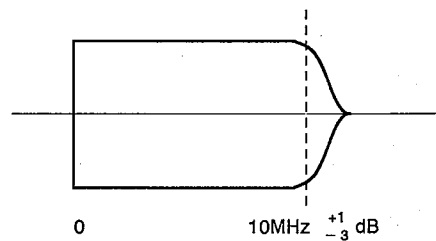
The DC voltage outputted from IC102-⑤ pin is controlled for the background DC voltage as a reference voltage at IC101 and outputted to IC101-⑦ pin. The DC voltage is fed back as a clamp voltage.

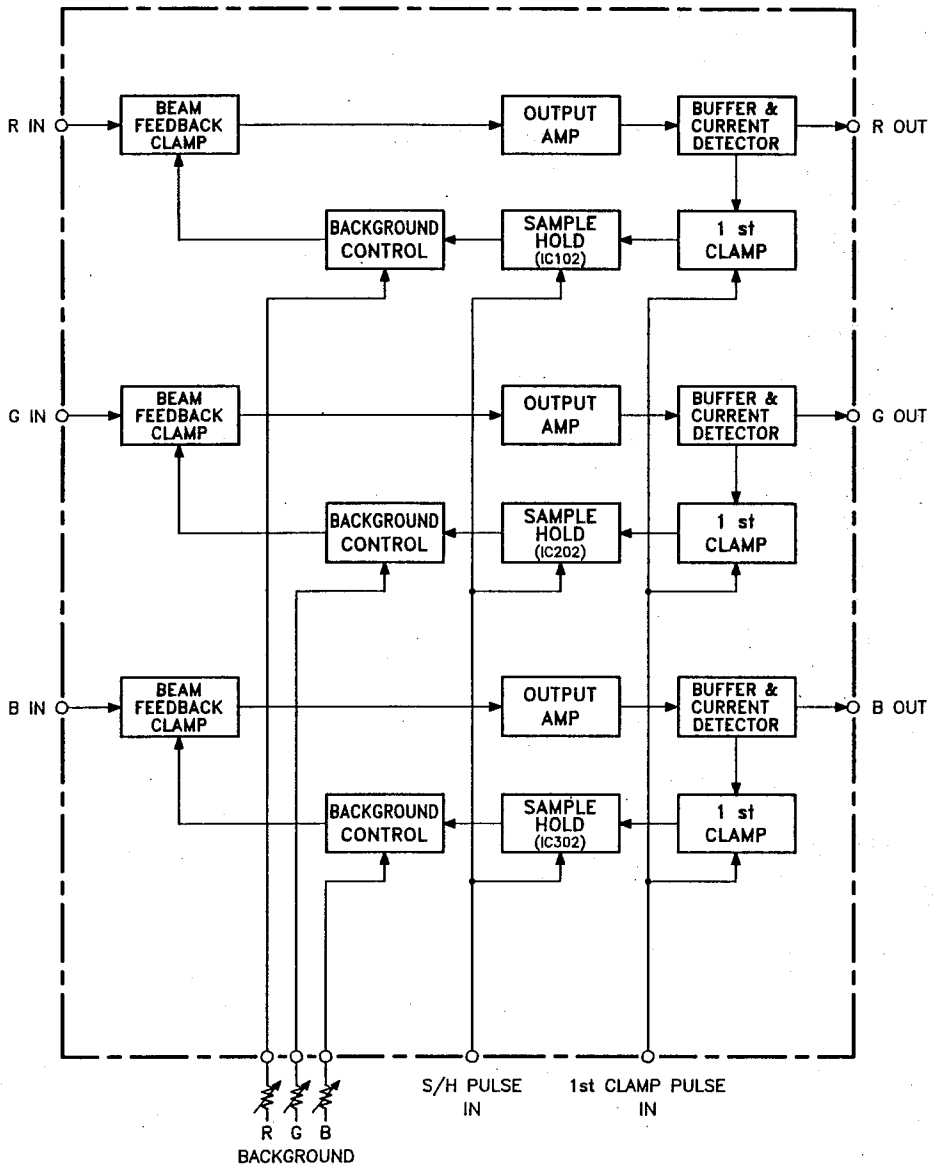
(3) Adjustment Procedure

Apply a sweep signal to the composite video input terminal.

(a) VC101(R.FRQ) VC201(G.FRQ) VC301(B.FRQ) VR101(R.FRQ) VR201(G.FRQ) VR301(B.FRQ)

- ① Connect a 100:1 probe to TP101.
- ② Adjust VC101 and VR101 so that the 10MHz level is within +1dB to -3dB of the 100kHz level.
- ③ Adjust VC201 and VR201 at TP201 in the same manner.
- ④ Adjust VC301 and VR301 at TP301 in the same manner.





20/30 SERIES
 COLOR MONITOR
 RGB OUT BOARD
 Block Diagram
 C4-904323

VR301(B.FRQ)

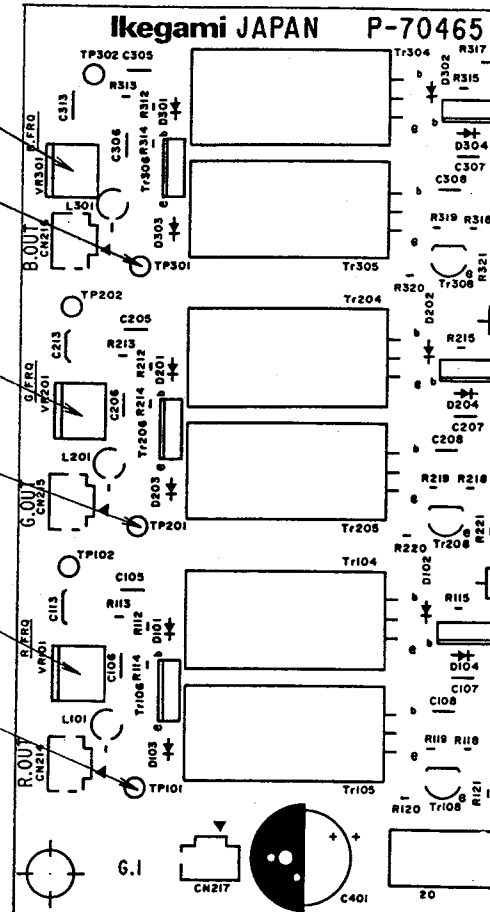
TP301

VR201(G.FRQ)

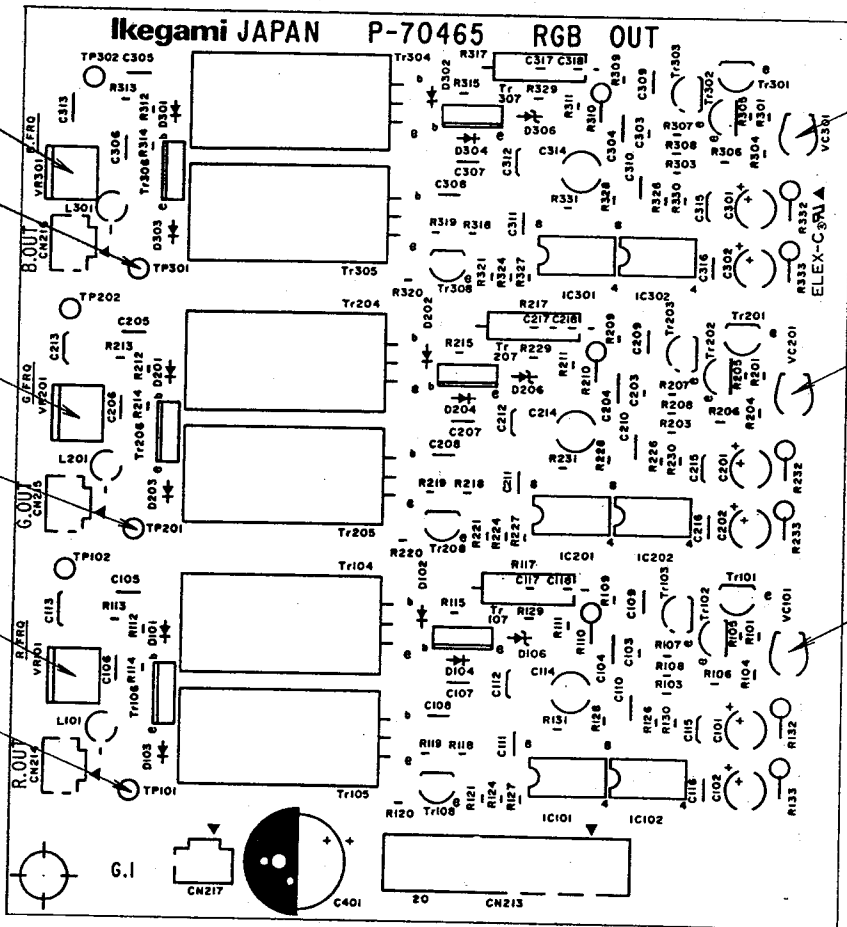
TP201

VR101(R.FRQ)

TP101



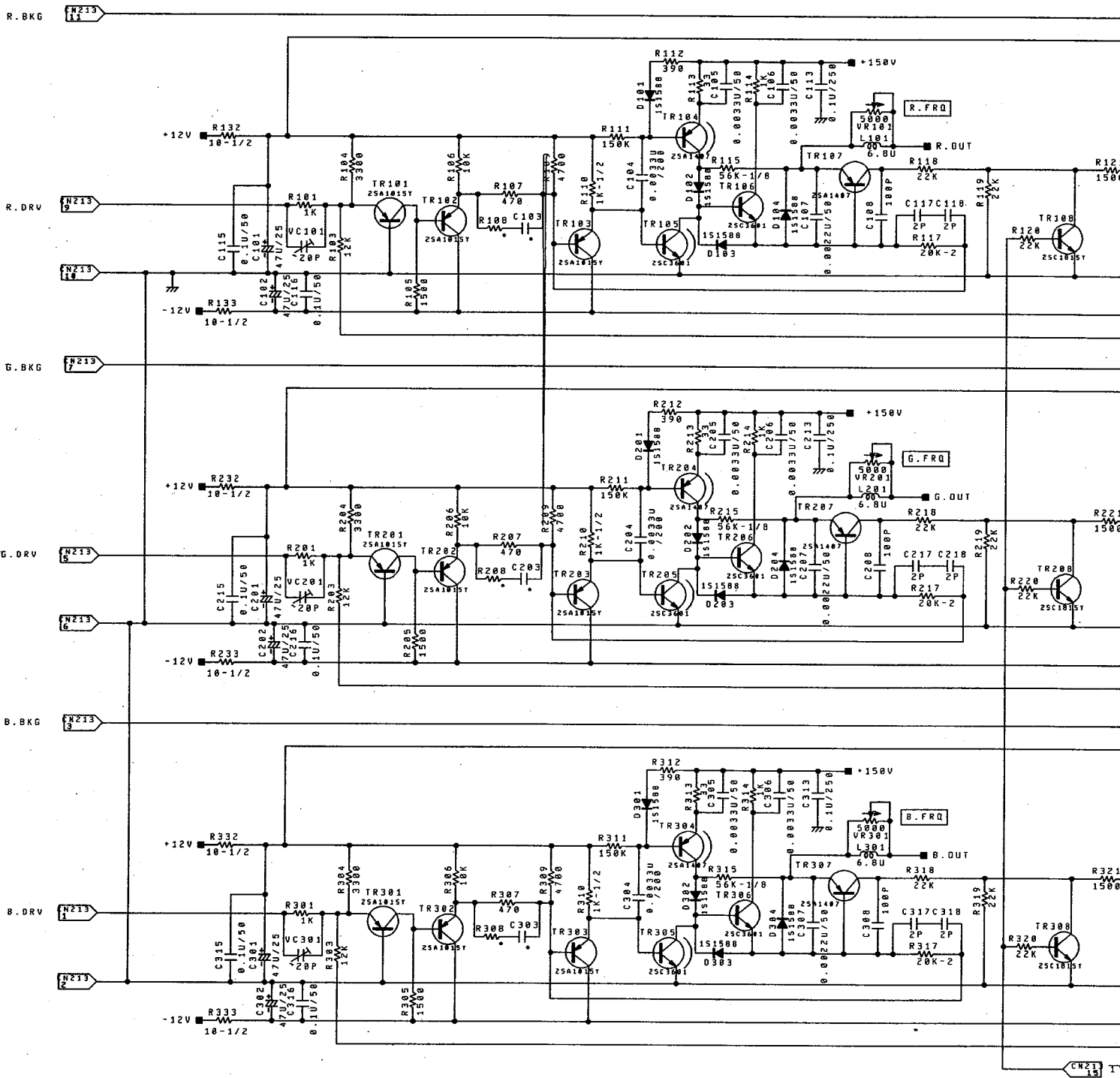
20/30 SERIES
 RGB OUT BOARD
 PARTS LOCATION
 P-70465



VC301(B.FRQ)

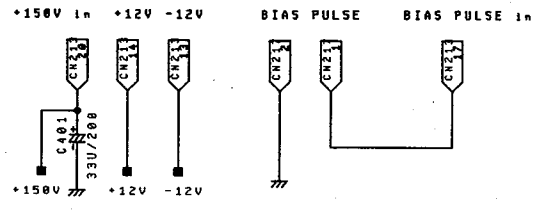
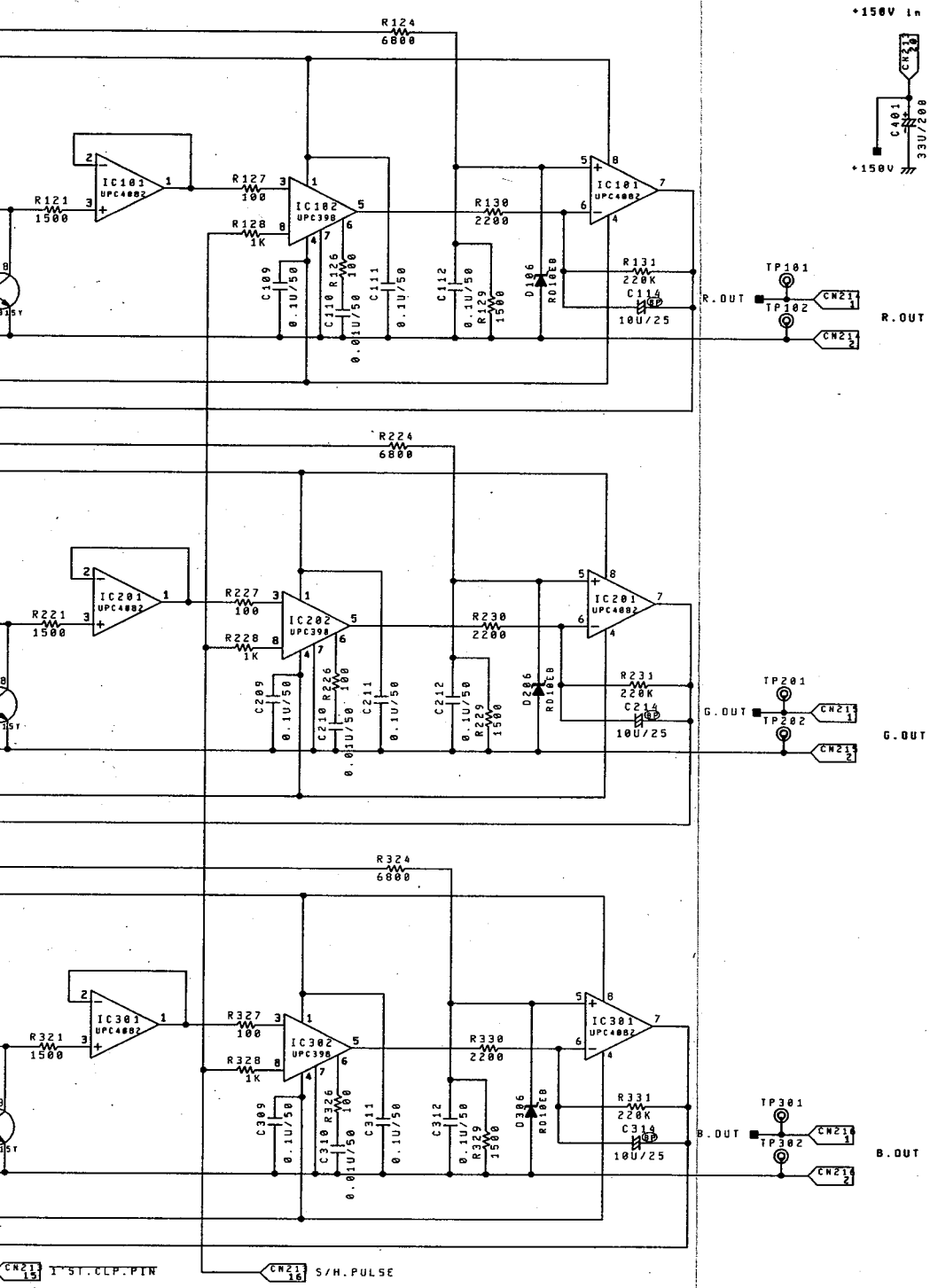
VC201(G.FRQ)

VC101(R.FRQ)



NOTE:

1. All resistors are in ohms 5% (parts marked F:1%), 1/10 watt unless otherwise specified.
2. All capacitors are in farads, 300V unless otherwise specified.
3. All inductors are in henry unless otherwise specified.
4. Waveforms are taken with a color bar signal input.
5. Parts marked * are factory selected value.
6. Parts marked ★ are critical components for X-radiation.



LAST NO.

IC	102	202	302
L	101	201	301
R	133	233	333
Tr	109	209	309
C	118	218	318
D	106	206	306
VC	101	201	301
TP	102	202	302

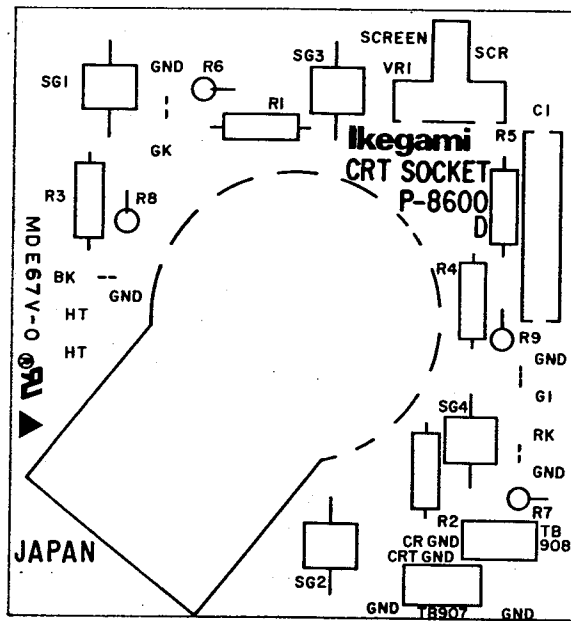
401

LOST NO.

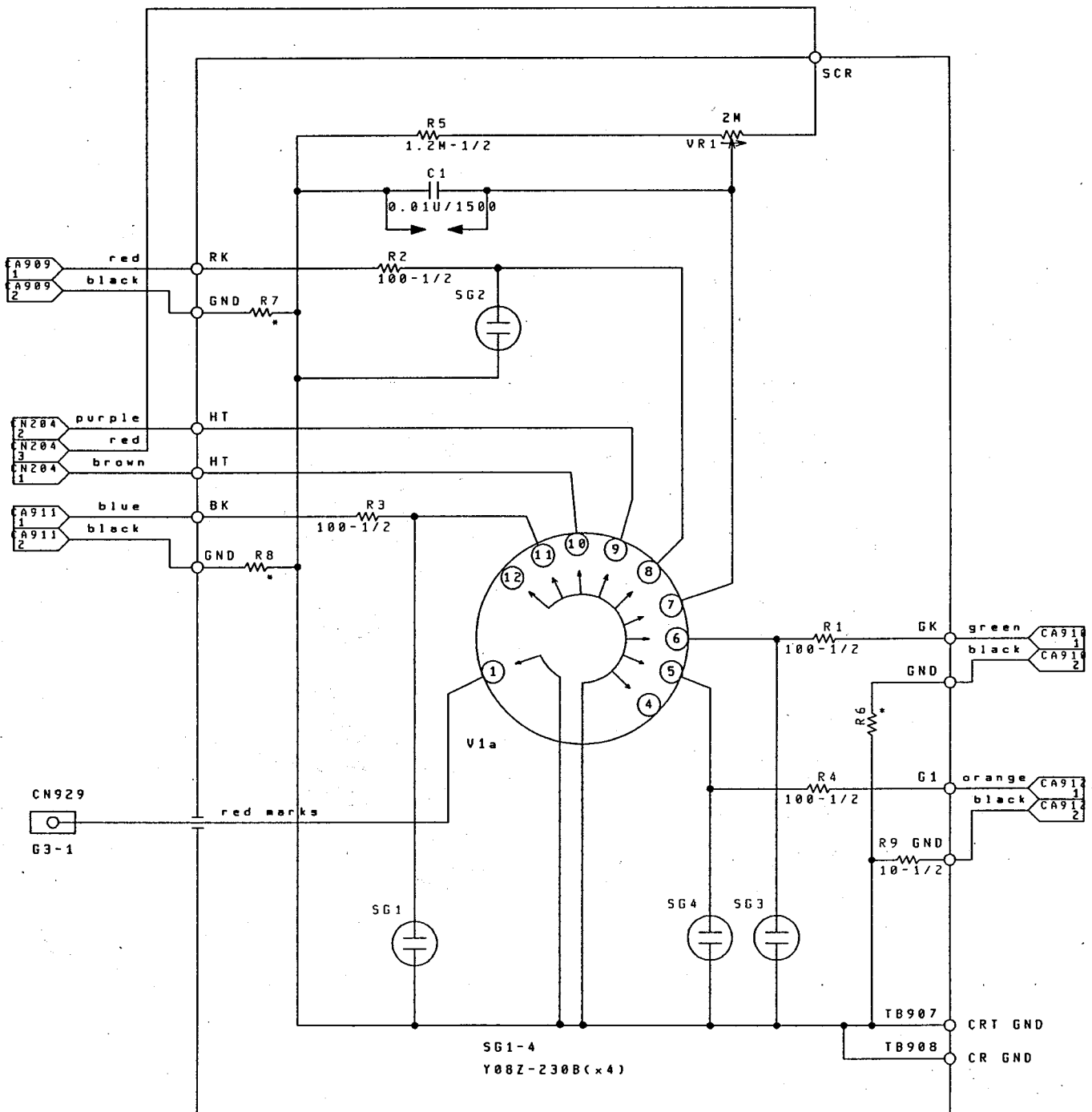
Tr	109, 209, 309
D	105, 205, 305
R	102, 202, 302
	116, 216, 316
	122, 222, 322
	123, 223, 323
	125, 225, 325

**20/30 SERIES
COLOR MONITOR
RGB OUT BOARD
Schematic Diagram
C21-904183C**

2-8. CRT SOCKET BOARD

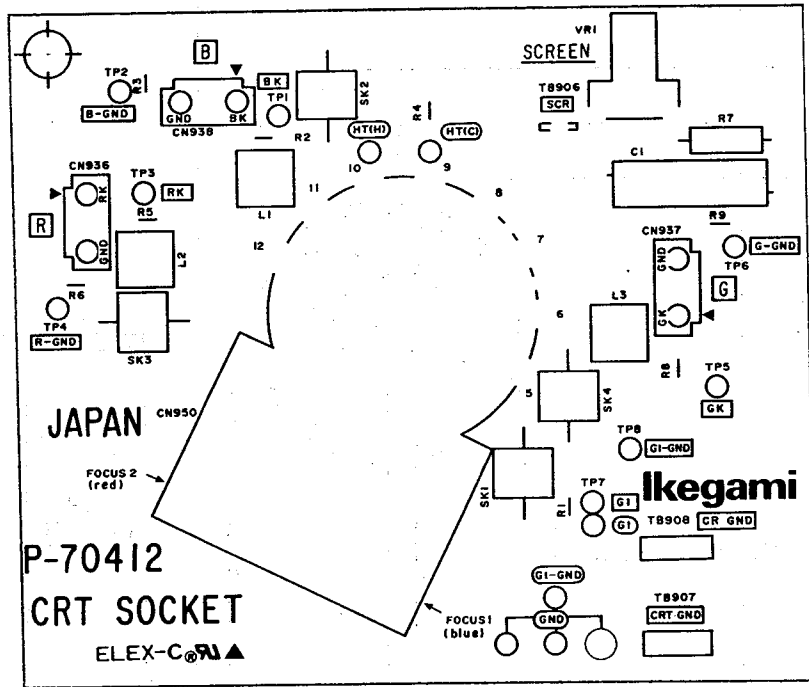


**20 SERIES
CRT SOCKET BOARD
PARTS LOCATION
P-8600D**

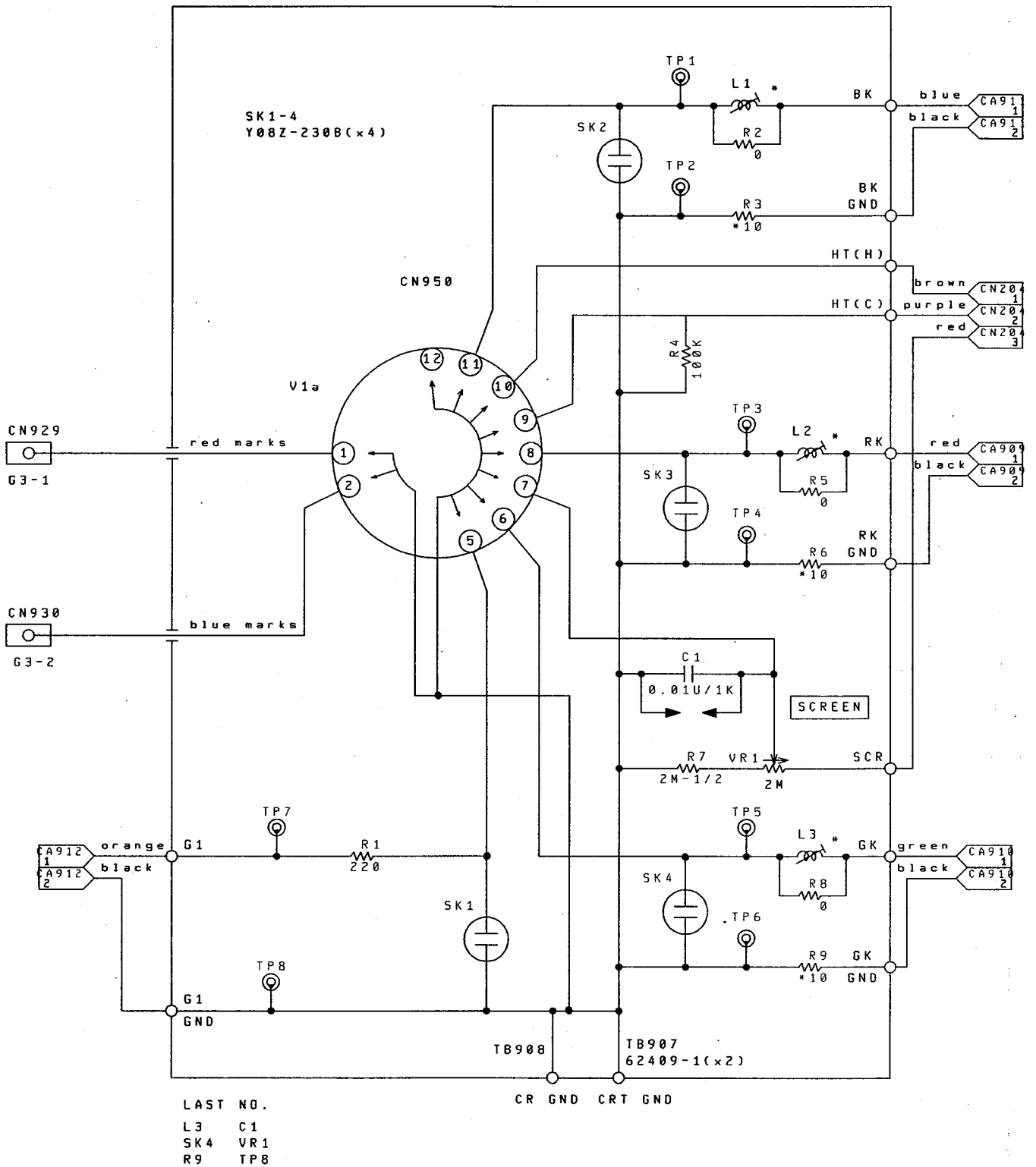


NOTE: 1. All resistors are in ohms 5% (parts marked F:1%), 1/4 watt unless otherwise specified.
 2. All capacitors are in farads, 300V unless otherwise specified.
 3. All inductors are in henry unless otherwise specified.
 4. Waveforms are taken with a color bar signal input.
 5. Parts marked * are factory selected value.
 6. Parts marked ★ are critical components for X-radiation.

**20 SERIES
 COLOR MONITOR
 CRT SOCKET BOARD
 Schematic Diagram
 C4-902693B**



30 SERIES
 CRT SOCKET BOARD
 PARTS LOCATION
 P-70412



- NOTE:
1. All resistors are in ohms 5% (parts marked F:1%), 1/4 watt unless otherwise specified.
 2. All capacitors are in farads, 300V unless otherwise specified.
 3. All inductors are in henly unless otherwise specified.
 4. Waveforms are taken with a color bar signal input.
 5. Parts marked * are factory selected value.
 6. Parts marked ★ are critical components for X-radiation.

**30 SERIES
COLOR MONITOR
CRT SOCKET BOARD
Schematic Diagram
C4-904123**

1. DEF & POWER PROCESS

1-1. DEF BOARD & FBT BOARD /HV UNIT

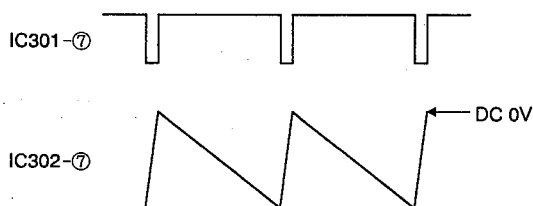
(1) Outline

Horizontal and vertical deflection are performed on the basis of the HD and VD supplied from the MOTHER BOARD. It also generates and controls the high voltage, etc., supplied to the CRT.

(2) Circuit Description

(a) Vertical deflection circuit

The IC105 functions as an unstable multivibrator and drives the Tr301 by using the pulse synchronizing with the VD supplied from the INTERFACE BOARD. The saw-tooth waveform is produced with Tr302 and C305 by shaping the pulse waveform at IC301. The level of this saw-tooth wave changes according to the DC voltage of the IC302-③ and this level determines the height of the screen. The wave of the IC302-⑦ as it is deflects only below the GND. This means that a picture appears only on the lower half portion of the screen. In order to avoid this, a DC bias is applied to the ⑤ and ⑥ pins of the IC303 so that the middle point of the saw-tooth wave may be 0V



(b) Horizontal deflection circuit

The main component of the horizontal deflection drive circuit is the IC105.

This circuit mainly consists of the circuits having the following four functions.

- ① The phase of the horizontal deflection pulse is changed by the IC102 monostable multivibrator to control the H. phase.
- ② The phase of the AFC pulse of ① and that of the HD of the IC105-⑮ are compared to control the oscillating frequency.(AFC circuit)
- ③ The deflection drive pulse is shut off upon receipt of DC from the protection circuit.

- ④ It switches the AFC time constant together with the time constant switching IC(IC104).

The IC105 consists of four circuits mainly.

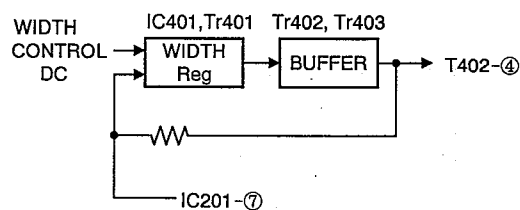
The IC105 oscillating circuit is actuated at 15.75KHz and its drive pulse is applied to the Tr404 of the horizontal drive circuit via the Tr103 and Tr104 buffers.

The current is amplified in Tr404 and T401 of the horizontal circuit to drive the Tr405.

The power supply to the horizontal deflection circuit is applied to the T402-④ pin.

The regulator composed of IC401, Tr401, Tr402 and Tr403 compares the supply voltage with the DC voltage of the WIDTH CONTROL and stabilizes it.

A parabolic waveform is applied from the IC201-⑦ pin to correct the side pin.



(c) High voltage output circuit

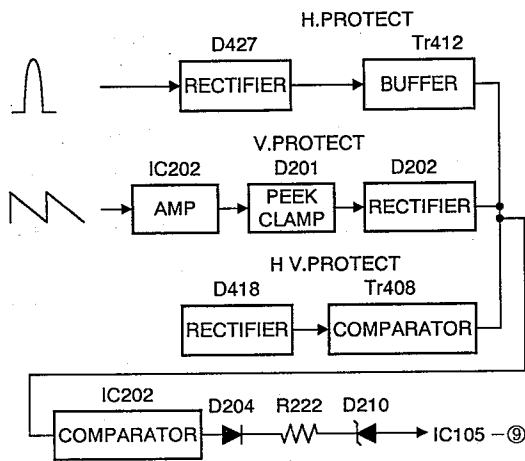
Like the horizontal output circuit, the high voltage output circuit applies the drive pulses transmitted from the Tr103 and Tr104 buffers to the Tr409 and drives the Tr410 via the Tr429 and Tr430 buffers to generate a high voltage by the flyback method. The series regulator composed of Tr421, Tr422, Tr423, Tr424, Tr425 and Tr432 controls the power supplied to the No.1 pin of the flyback transformer to stabilize the high voltage output.

(d) Protection circuit

When any of the following states occurs due to the trouble of the deflection circuits or high voltage circuit, the protection circuit functions to shut down the high voltage output by increasing the DC voltage of the IC105-⑨ pin.

- ① When the high voltage output is excessively increased and the flyback pulse level becomes higher than the setting.
- ② When no current comes to flow to the T403-③ and-④ pins connected to the cold side of the horizontal deflection yoke and no pulse waveform is generated between the T403-① and ②.

- ③ When the saw-tooth wave disappears from the cold side of the vertical deflection yoke or the DC bias is excessively shifted.



(e) **Horizontal centering circuit**

The deflecting position can be adjusted by flowing a DC current to the hot side of the horizontal deflection yoke through the L402. This control is accomplished by inputting the DC for H.CENT control to IC402.

The Tr414 and Tr415 serve as buffers for the IC402.

(f) **Rotation adjusting circuit**

This circuit functions to control the DC current applied to the rotation coil to cancel the horizontal magnetic field which has influence on the monitor. The current which has flown into the rotation coil is applied to the R231. This circuit compares the voltage generated from such currents with the reference voltage and controls to flow a constant current.

(g) **Dynamic focus circuit (30 Series only)**

HD is applied from the INTERFACE BOARD to the base of the Tr426 and the HD is integrated in the Tr426 and C443 to become a saw-tooth wave. The saw-tooth wave is integrated furthermore in the IC403, C445 and R482 to become a "H" parabolic waveform.

This parabolic waveform and the "V" parabolic waveform from the IC201- ⑦ are applied to the IC404 ② to superpose the "V" parabolic waveform onto the "H" parabolic waveform, thereby adjusting the dynamic focus. The VR404 functions to adjust the focus on the both sides and the VR406 functions to adjust the focus on the top and bottom.

(3) **Adjustment Procedure**

(a) **VR101(H.HOLD)**

- ① Adjust VR101 so that there is horizontal sync on the screen.

(b) **VR102(V.HOLD 6)**

- ① Apply a PAL signal to the input terminal.
② Adjust VR102 so that there is vertical sync on the screen.

(c) **VR103(V.HOLD 5)**

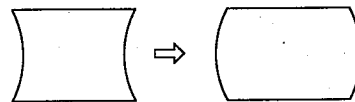
- ① This adjustment is performed after adjusting VR102.
② Apply a NTSC signal to the input terminal.
③ Adjust VR103 so that there is vertical sync on the screen.

(d) **VR201(PIN PHASE)**

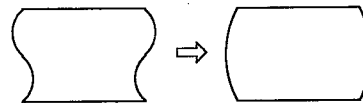
VR202(SIDE PIN ADJ)

VR407(PIN AMP)

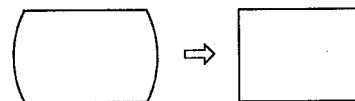
- ① Press the **TEST** switch to select the "CROSS HATCH" signal.
② Set VR407 to MAX.



- ③ Adjust VR201 so that the portion with maximum lateral protrusion comes into the center as shown in the figure below.

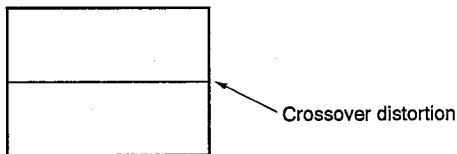


- ④ Adjust VR407 so that the vertical lines on the left and right of the screen are straight.



(e) **VR301(V.BIAS)**

- ① Press the **TEST** switch to select the "FLAT FIELD" signal.
- ② Eliminate crossover on the screen using VR301 only when Tr304 or Tr305 is replaced and crossover can be confirmed on the screen.

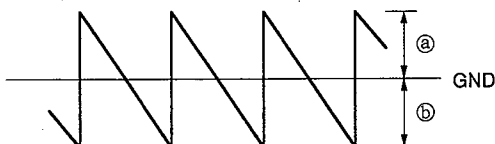


(f) **VR302(625 HEIGHT)**

- ① Apply a NTSC signal to the input terminal. Adjust the HEIGHT with the **HEIGHT** switch on the pull-out panel. (Refer to 5-3.(1) in the OPERATION MANUAL.)
- ② Apply a PAL signal to the input terminal and adjust the height using VR302.

(g) **VR303 (DC OS)**

- ① Connect the probe to IC303-7.
- ② Set the V.CENT to 50% with the **V.CENT** switch on the pull-out panel.
- ③ Adjust VR303 so that the ratio of the levels of ① and ② in the figure below is 1:1 centered on GND. ($a = b$)



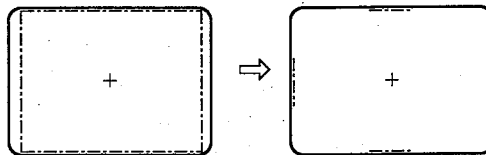
(h) **VR304(V.LIN)**

- ① Press the **TEST** switch to select the "CROSS HATCH" signal.
- ② Adjust VR304 so that the vertical linearity is optimum.

(i) **VR401(WIDTH ADJ)**

- ① Press the **SAFE TITLE** switch to select the "95%".
- ② Press the **SCAN** switch to select the "OVER SCAN".
- ③ Set the WIDTH to 90% with the **WIDTH** switch on the pull-out panel.

- ④ Adjust VR401 so that the 95% SAFE TITLE marker comes into the position of the escutcheon frame.

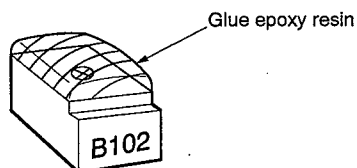


(j) **VR402 (H.V PROTECT)**
VR403 (HV ADJ)

- ① Connect a high-voltage meter to the anode of the CRT.
- ② Set the high-voltage to 27.5kV using VR403.
- ③ Set the **CONT** and **BRIGHT** switch to "MANUAL" state and set the CONT and BRIGHT to the position where the **OVER LOAD** LED on the front left begins to light up.
- ④ Use VR402 to set to the position where the protection begins to operate. Then set the **POWER** switch to "OFF" position.
- ⑤ Turn VR403 slightly counterclockwise and set the **POWER** switch to "ON" position.
- ⑥ Adjust VR403 so that the high-voltage is 25kV.

Precaution

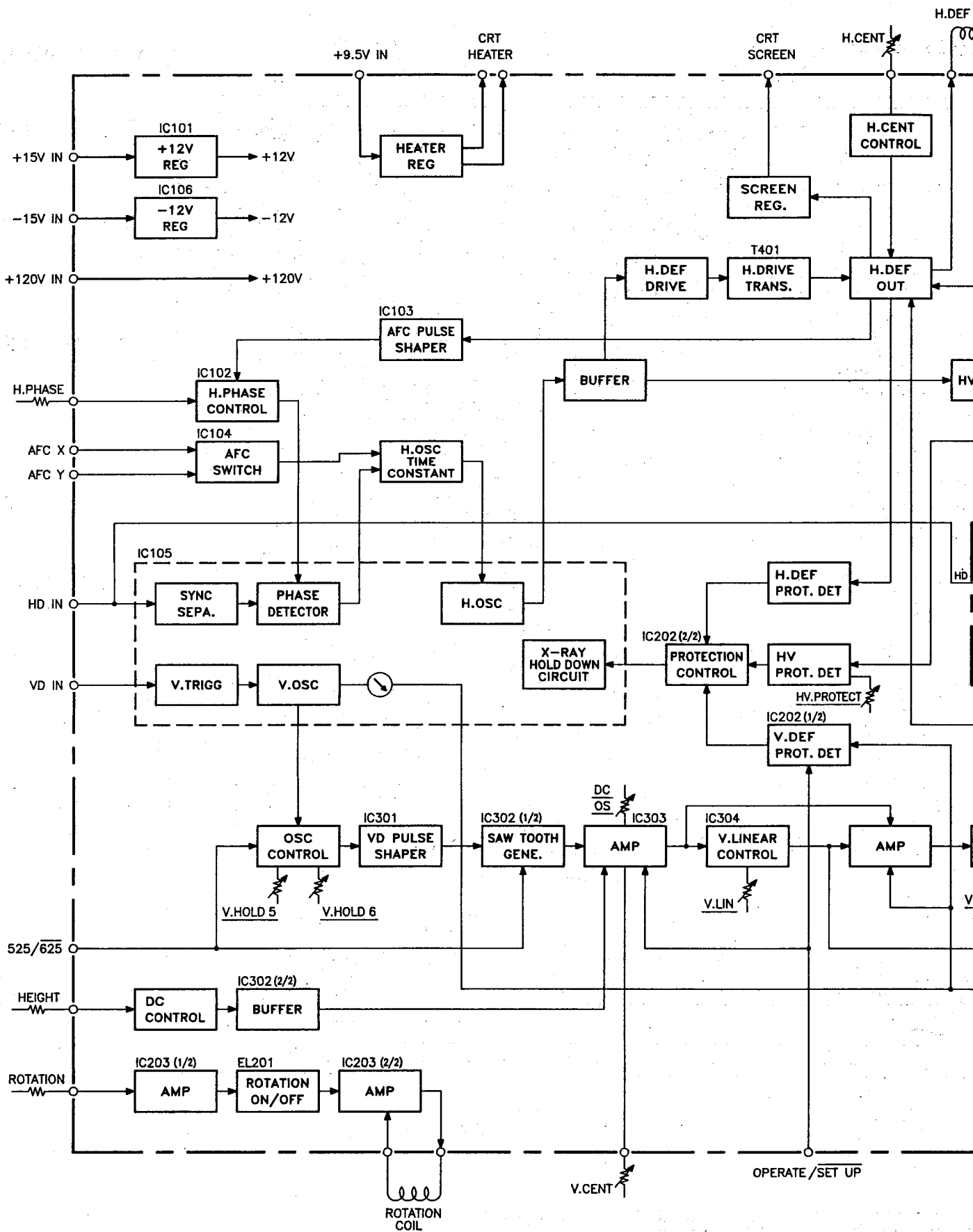
These controls are not for field servicing and are fixed with glue after setting to avoid X-ray radiation which may cause one component failure in the circuit and misadjustment of these controls. The sealing method is shown in the diagram.

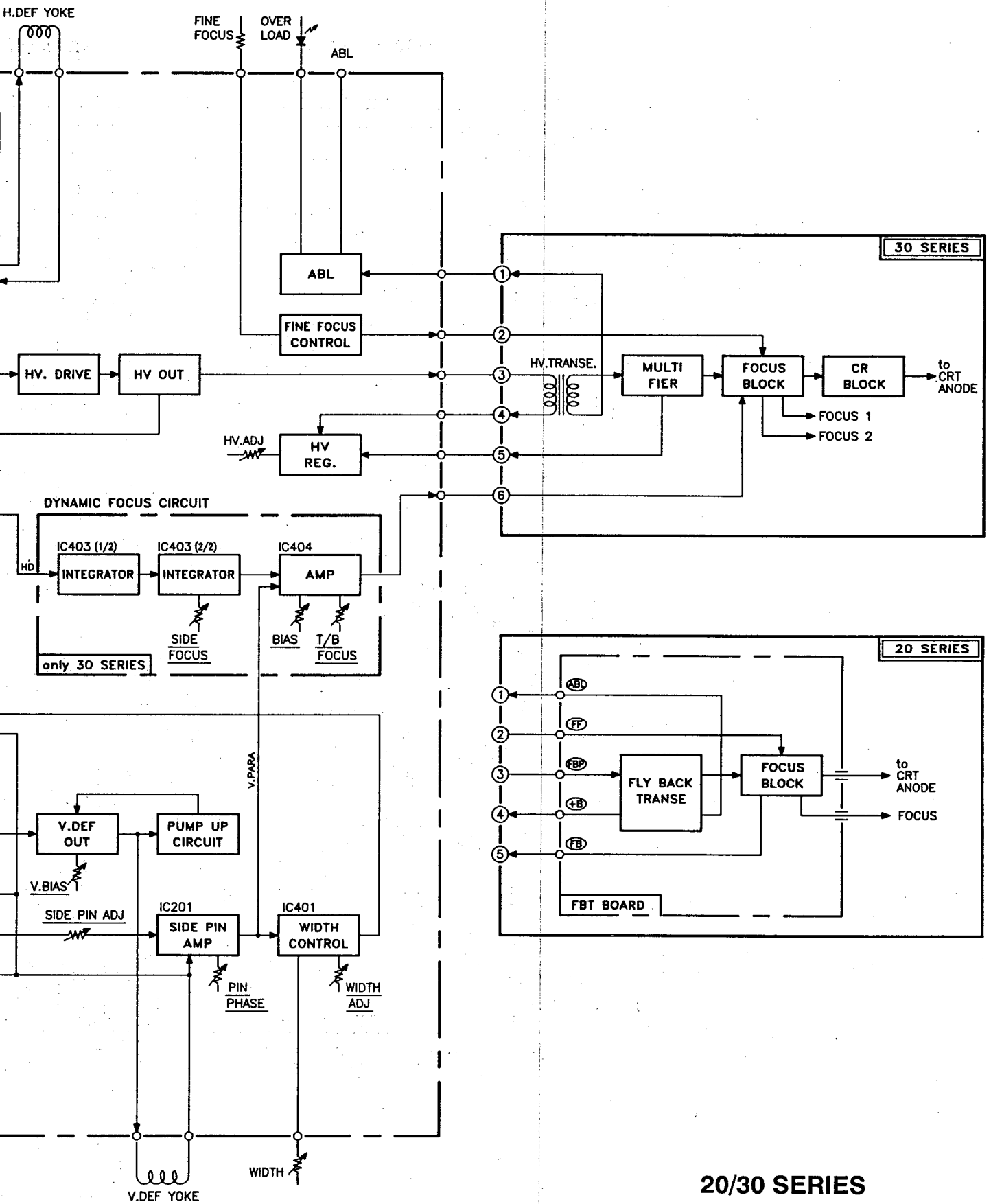


These adjustments are required when replacement of the parts marked with ★, ★ in the schematic diagram of the DEF BOARD is done.

(k) VR404 (SIDE FOCUS)
VR405 (BIAS) } only 30 SERIES
VR406 (T/B FOCUS)

- ① Set the FINE FOCUS to 50% with the FINE
FOCUS switch on the pull-out panel.
- ② Give the proper bias using VR405. Adjust VR404 and VR406 so that the focus of the left and right (VR404), and the upper and lower (VR406) is optimum.
At the same time, adjust the FOCUS 1 and FOCUS 2 described in the OPERATION MANUAL 7-2 (2).

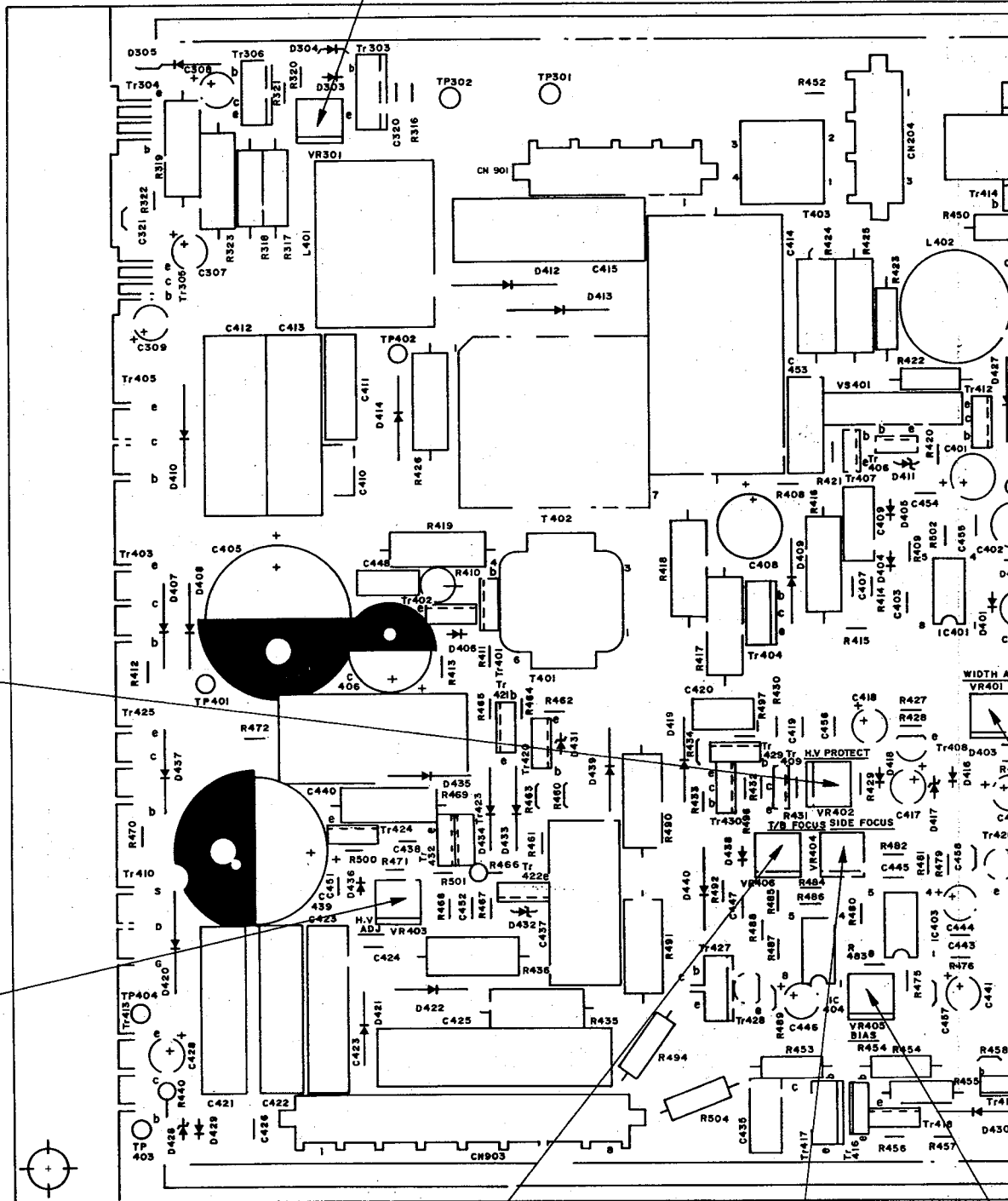




**20/30 SERIES
COLOR MONITOR
DEF BOARD & HV UNIT
Block Diagram**

C2-904339

VR301(V.BIAS)



VR402(HV PROTECT)

VR403(HV ADJ)

VR406(T/B FOCUS)

VR404(SIDE FOC

20/30 SERIES
DEF BOARD
PARTS LOCATION
P-70398D

..... 30 SERIES ONLY

04 (V. LIN)

HEIGHT)

VR303 (DC OS)

IC303

VR103 (V. HOLD 5)

VR102 (V. HOLD 6)

VR202 (SIDE PIN ADJ)

VR101 (H. HOLD)

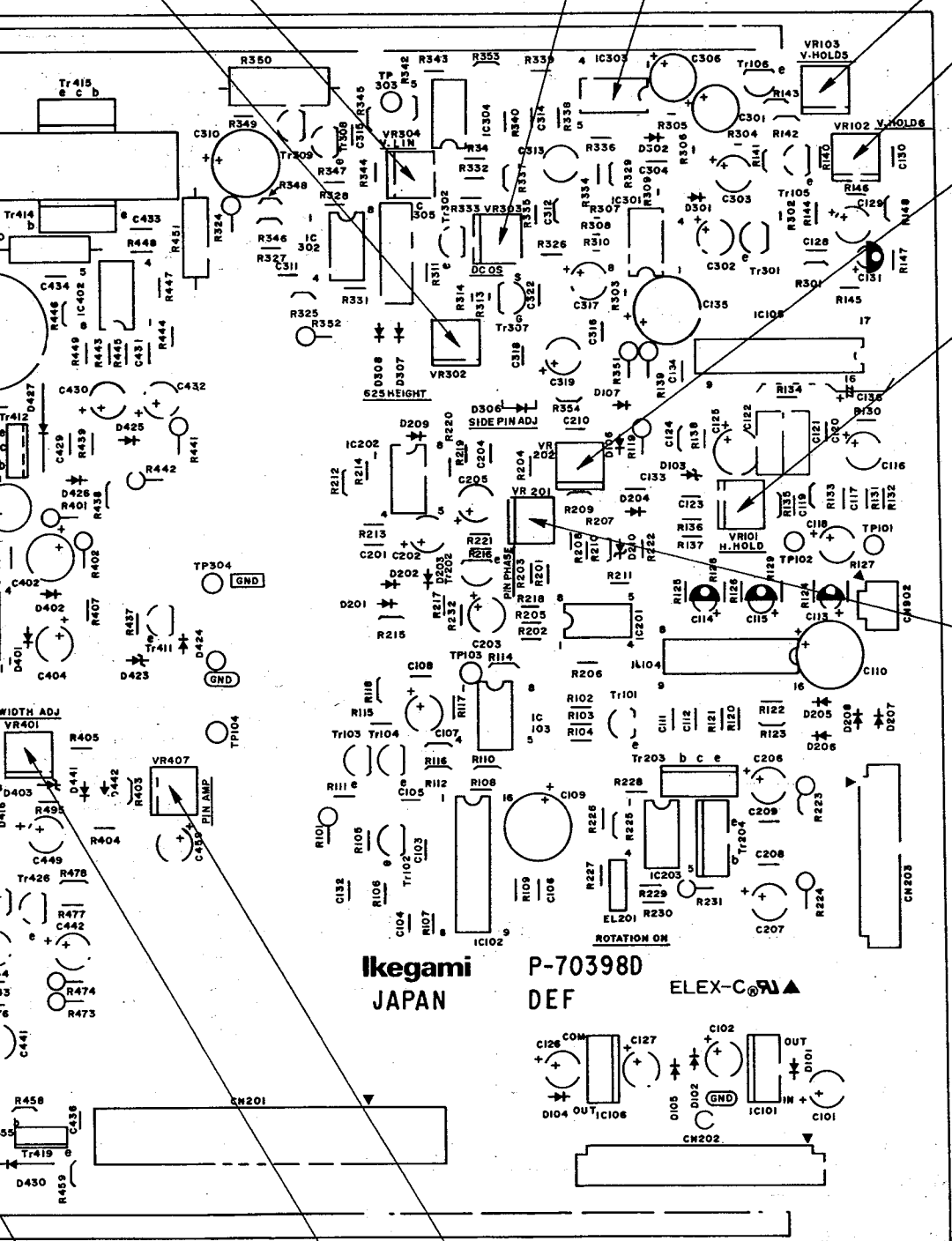
VR201 (PIN PHASE)

VR407 (PIN AMP)

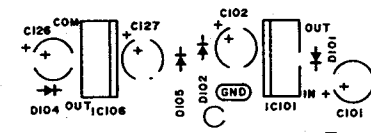
VR401 (WIDTH ADJ)

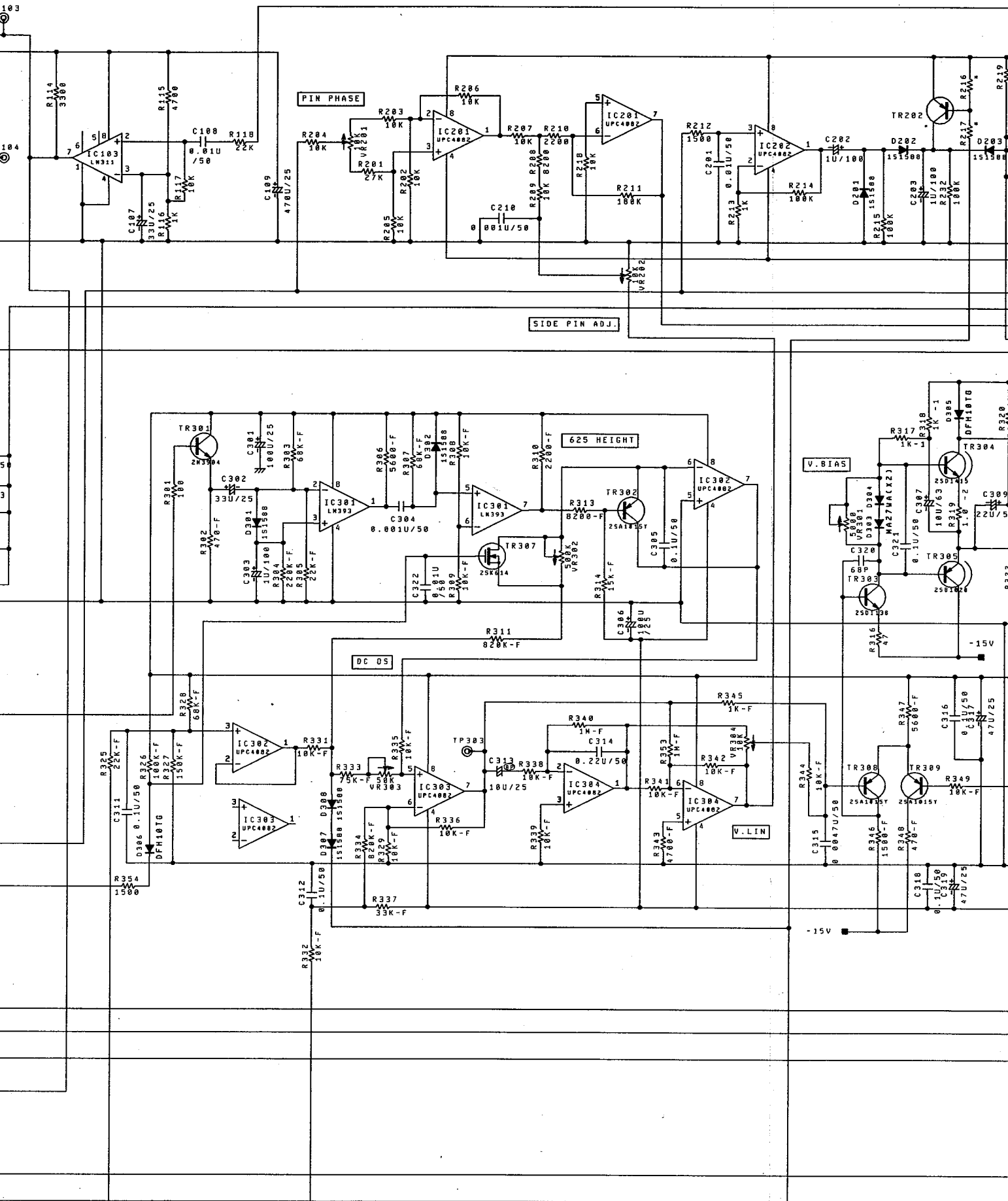
VR405 (BIAS)

FOCUS)



Ikegami JAPAN P-70398D DEF ELEX-C



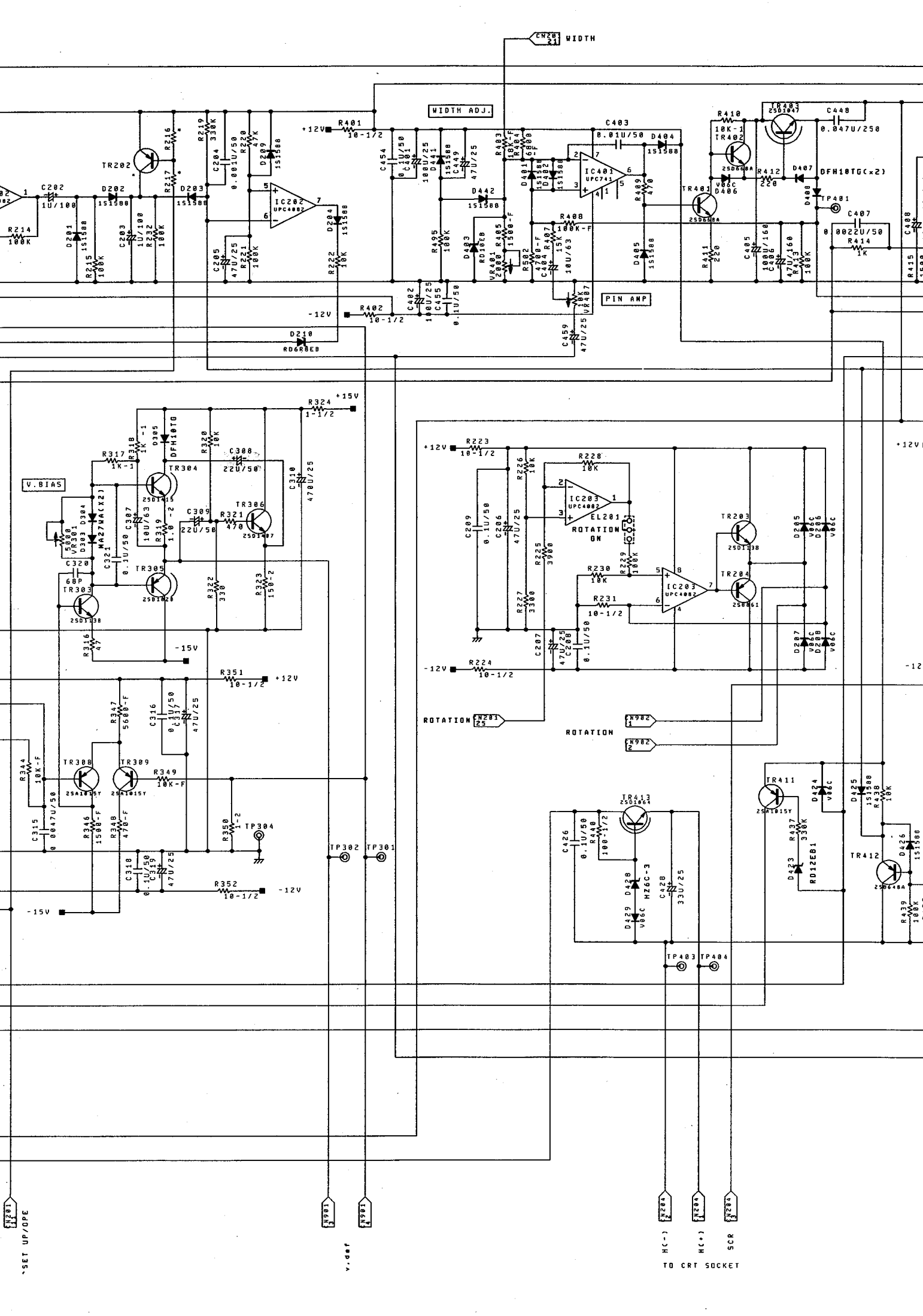


- | | |
|-----------------------|----------------------|
| LAST NO. | LOST NO. |
| IC 186, 203, 304, 404 | R 113, 312, 315, 330 |
| R 148, 232, 354, 504 | 485, 503 |
| C 136, 210, 322, 460 | TR 201, 431 |
| T 186, 204, 309, 432 | C 416, 427, 460 |
| D 185, 210, 308, 443 | L 403 |
| VR 183, 202, 304, 407 | D 415, 443 |
| VS 401 | |
| T 403 | |
| L 402 | |
| TP 184, 304, 404 | |

HEIGHT 2281

V-CENT 2281

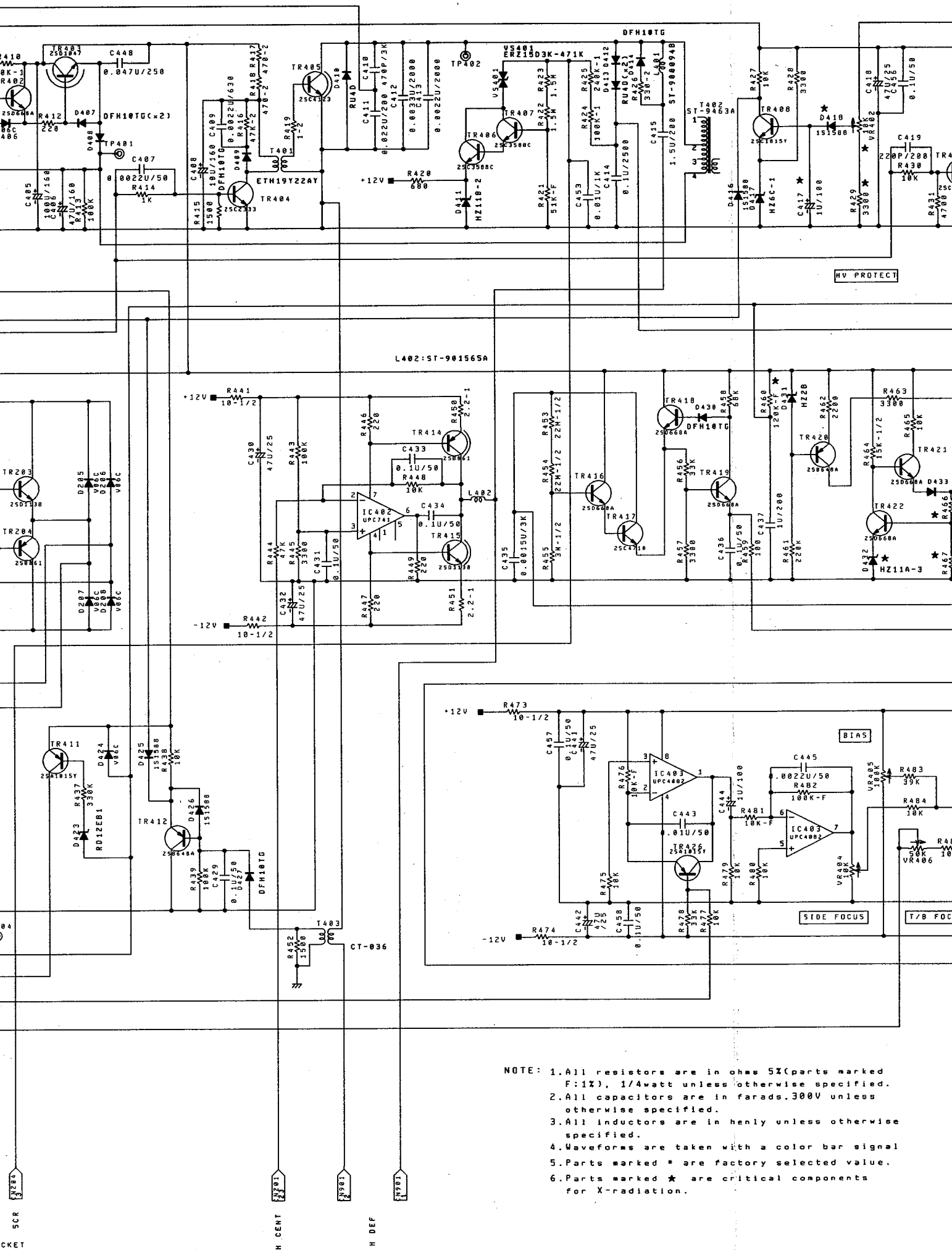
SET UP/DPE 2281



*SET UP/OPE

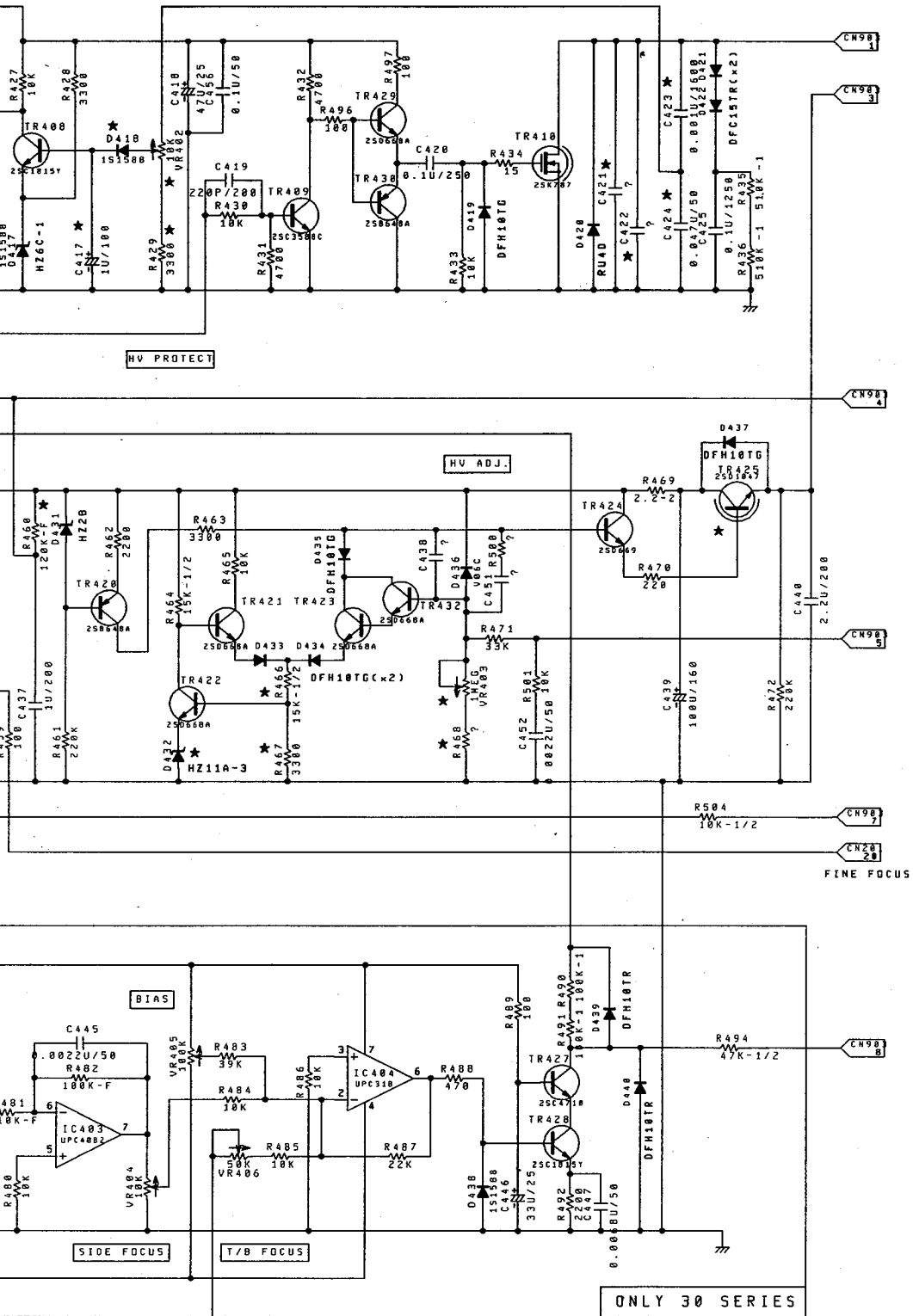
v. def

TO CRT SOCKET



NOTE: 1. All resistors are in ohms 5X (parts marked F:1%), 1/4 watt unless otherwise specified.
 2. All capacitors are in farads. 300V unless otherwise specified.
 3. All inductors are in henry unless otherwise specified.
 4. Waveforms are taken with a color bar signal
 5. Parts marked * are factory selected value.
 6. Parts marked * are critical components for X-radiation.

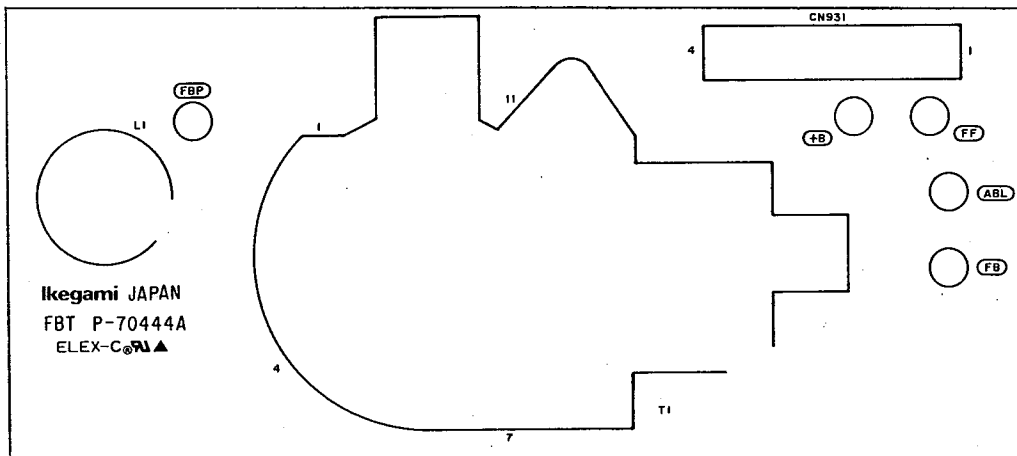
SCR
 CKET
 H. CENT
 H. DEF



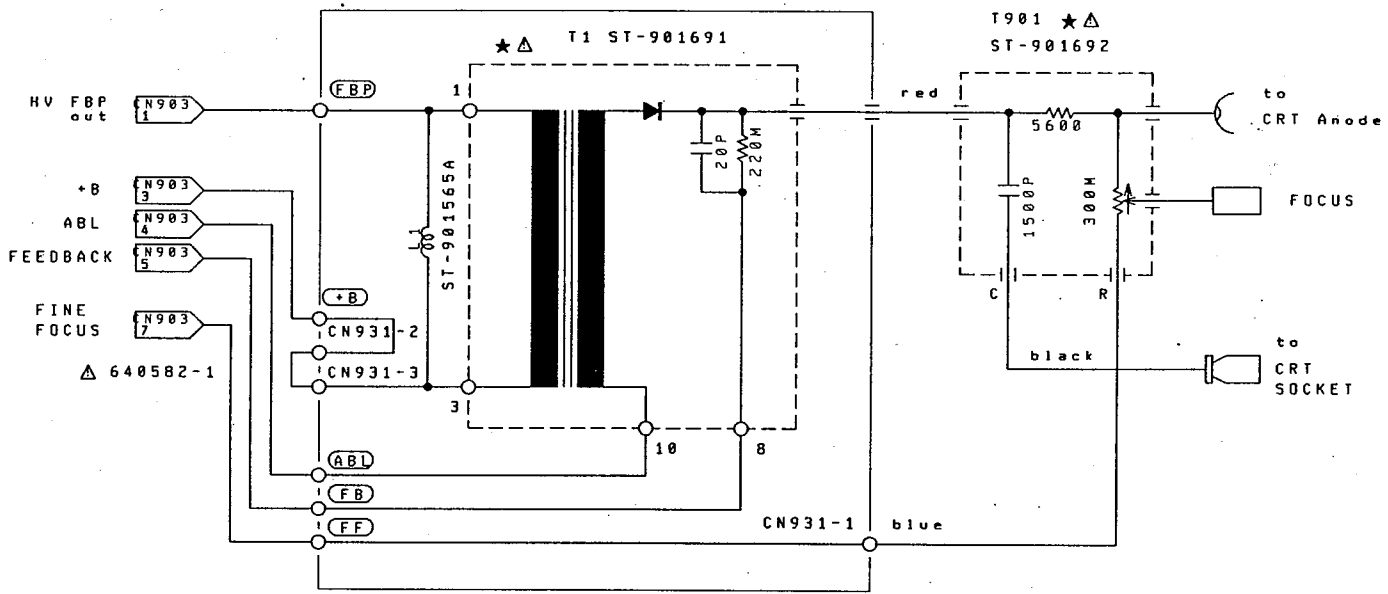
?	C421	C422	C438	C451	R468	R500
205	3300PF	2200PF	470PF	0.0047U/50	10K	12K
305	5600PF	4700PF	220PF	*	820K	*

5% parts marked
 otherwise specified.
 300V unless
 otherwise specified.
 only unless otherwise
 specified.
 a color bar signal
 selected value.
 critical components

**20/30 SERIES
 COLOR MONITOR
 DEF BOARD
 Schematic Diagram
 C11-904361**



**20 SERIES
 FBT BOARD
 PARTS LOCATION
 P-70444A**



NOTE:

1. All resistors are in ohms 5% (parts marked F:1%), 1/10 watt unless otherwise specified.
2. All capacitors are in farads, 300V unless otherwise specified.
3. All inductors are in henly unless otherwise specified.
4. Waveforms are taken with a color bar signal input.
5. Parts marked * are factory selected value.
6. Parts marked ★ are critical components for X-radiation.

**20 SERIES
COLOR MONITOR
FBT BOARD
Schematic Diagram
C4-904247**

1-2. POWER BOARD

(1) Outline

The AC power supplied to the monitor is inputted to this board and the DC voltage (such as +120V, $\pm 15V$, +155V, and +9.5V) used for deflection and video system boards is outputted.

(2) Circuit Description

(a) AC Voltage Switching Circuit

As shown in Fig. 3-2-1. and Fig. 3-2-2., RL2 is OFF for 220V and ON for 120V when the AC input is rectified, and the DC output has roughly the same voltage. By turning this relay (RL2) ON/OFF by the circuit described in section (b), there is no need for manual switching of AC input voltage.

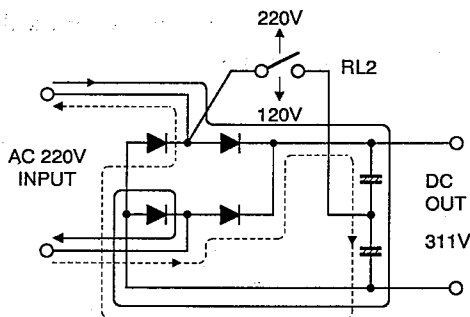


Fig. 3-2-1

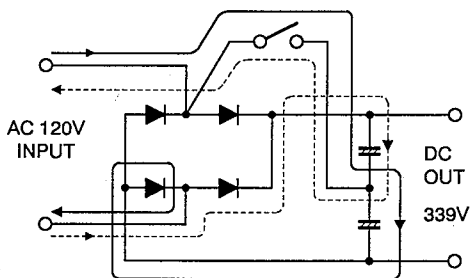


Fig. 3-2-2

(b) AC Voltage Detection Circuit

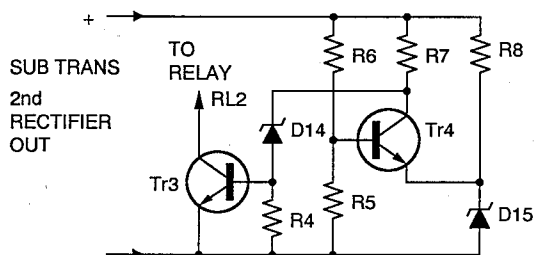


Fig. 3-2-3

The secondary voltage of power transformer T2 for AC voltage detection and power for driving the relay is AC 16V when AC 120V is inputted, and 29V when AC 220V is inputted. Rectifiers D2 through D5 are connected to the secondary side of this transformer, and the output is applied as shown in Fig. 3-2-3. When AC 220V is applied, the voltage at the base of Tr4 rises past the voltage (6V) of Zener D15, and Tr4 is turned ON. As this also turns D14 (6.8V, Zener) OFF, Tr3 is also turned OFF, and RL2 is OFF. When AC 120V, RL2 is turned ON by the completely opposite process.

(c) Switching Control Circuit

Switching control is performed by IC1. The main functions of this IC are as follows.

- ① There is a built-in oscillation circuit and the free-run frequency is approximately 90kHz.
- ② Fluctuations of the +120V output are amplified by IC2, pass through a photocoupler (PC1), and are inputted to comparator input IC1-①. The other input of the comparator is Pin ⑭, and the Pin ⑧ DC voltage is divided by R24 and R25 and applied to Pin ⑭ as the reference voltage.
- ③ There is a built-in shunt regulator for the reference voltage and internal power supply. The input/output is by Pin ⑧, and the voltage is approximately 6.6V.
- ④ In order to protect against overcurrent, there is 0.33 Ω of resistance (R21) on the ground side of the output transistor, and IC output pulses are cut off by returning this voltage to Pin ⑤.

(d) Switching Output Circuit

The pulses from Pin ⑦ of IC1 are inverted by Tr6, pass through the Tr7 and Tr8 buffers, and are applied to the output transistor (Tr9). Transformer operation is by the ON/OFF system.

(f) DEGAUSS Circuit

The power supply for the degauss coil is directly supplied from AC. ON/OFF is by RL3 and the Tr10 drive circuit. RL3 also functions as the prevention of the rush current when the power is turned on. Also, RL3 functions as ON/OFF of the R1 resistor.

(3) Adjustment Procedure

a) VR1(MIN. VOL)

- ① Connect the probe to the drain of Tr9.

Note Because of the primary side, be sure to connect the GND of the probe to the anode of D16.

- ② Set the AC input voltage to 85V.
- ③ Adjust VR1 so that the drain waveform is duty-limited.

b) VR2(+B ADJ)

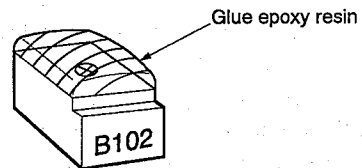
- ① Connect a high-accuracy digital voltmeter to TP5.

Note Because of the secondary side, be sure to connect the minus side of voltmeter to TP7.

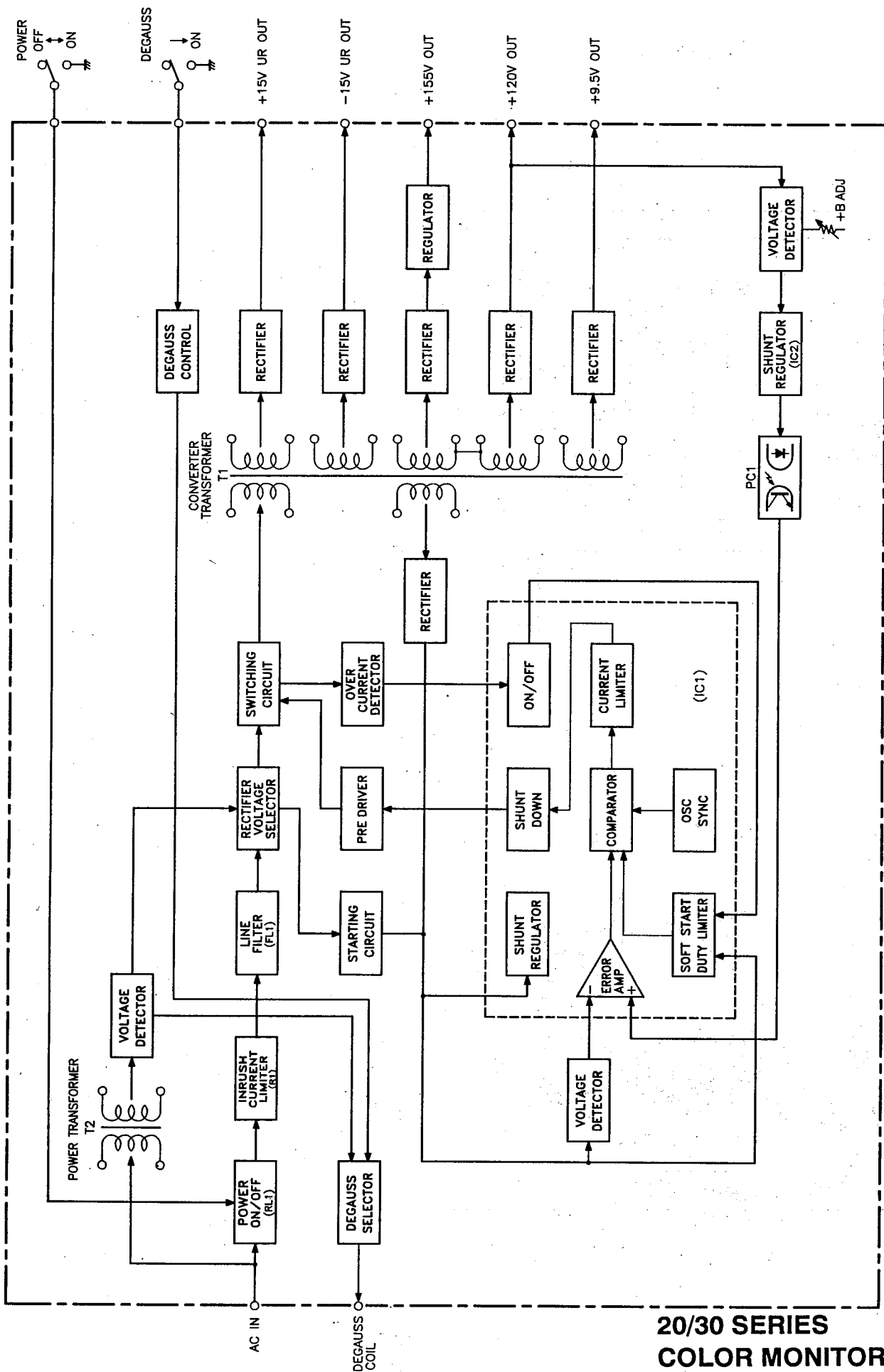
- ② Adjust VR2 so that the DC voltage is +120V.

Precaution

- The section marked as **PRIMARY** in the schematic diagram is the primary side of the power supply. Be sure not to short with the secondary side.
- This control is not for field servicing and is fixed with glue after setting to avoid X-ray radiation which may cause one component failure in the circuit and misadjustment of this control. The sealing method is shown in the diagram.



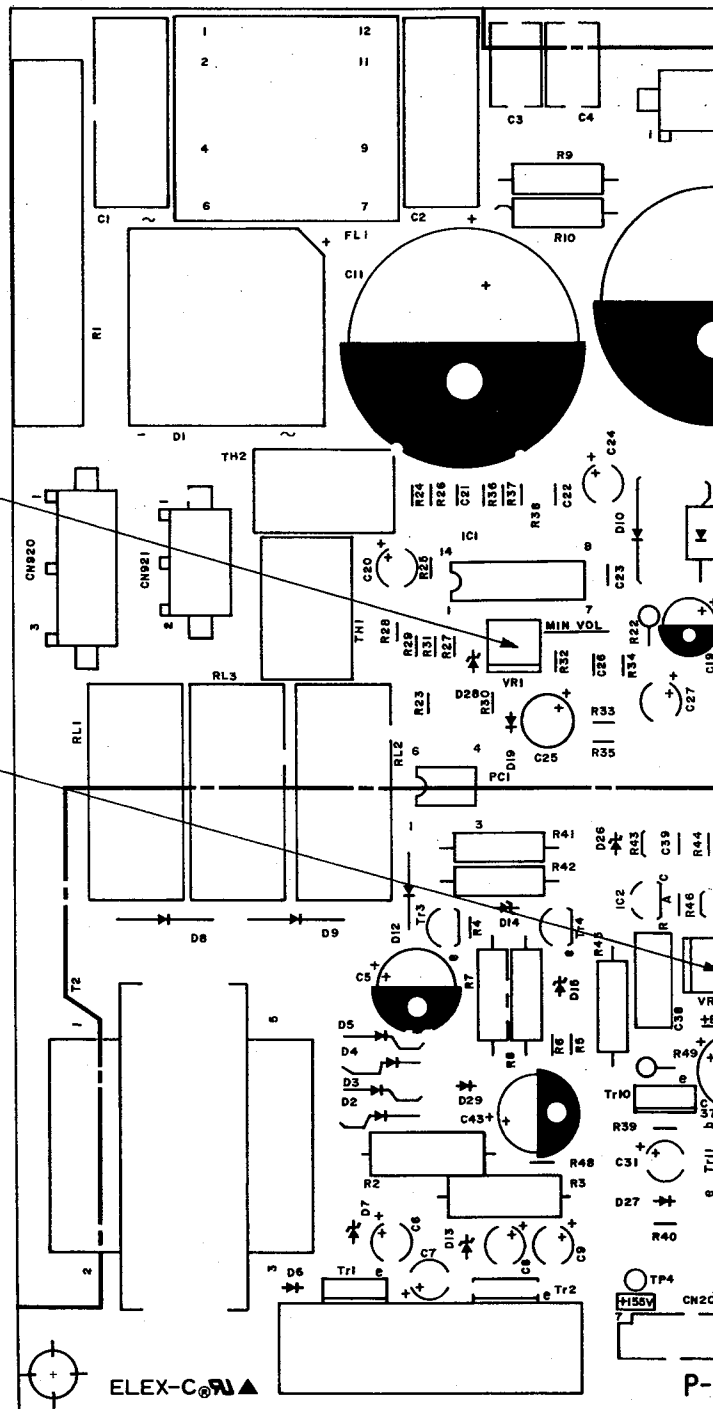
This adjustment is required when replacement of the parts marked with ★, (★) in the schematic diagram of the POWER BOARD is done.



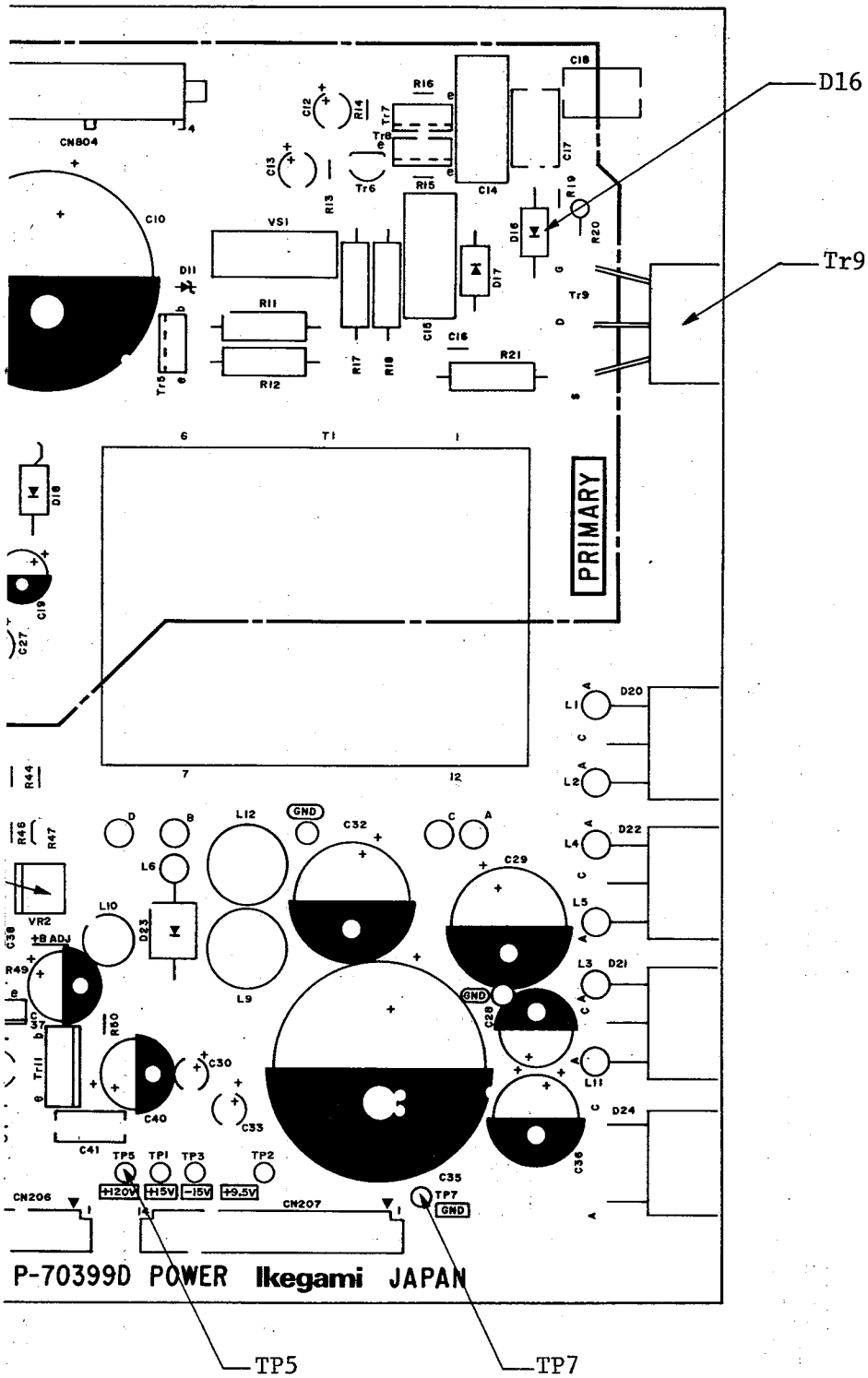
20/30 SERIES
 COLOR MONITOR
 POWER BOARD
 Block Diagram
 C3-904324

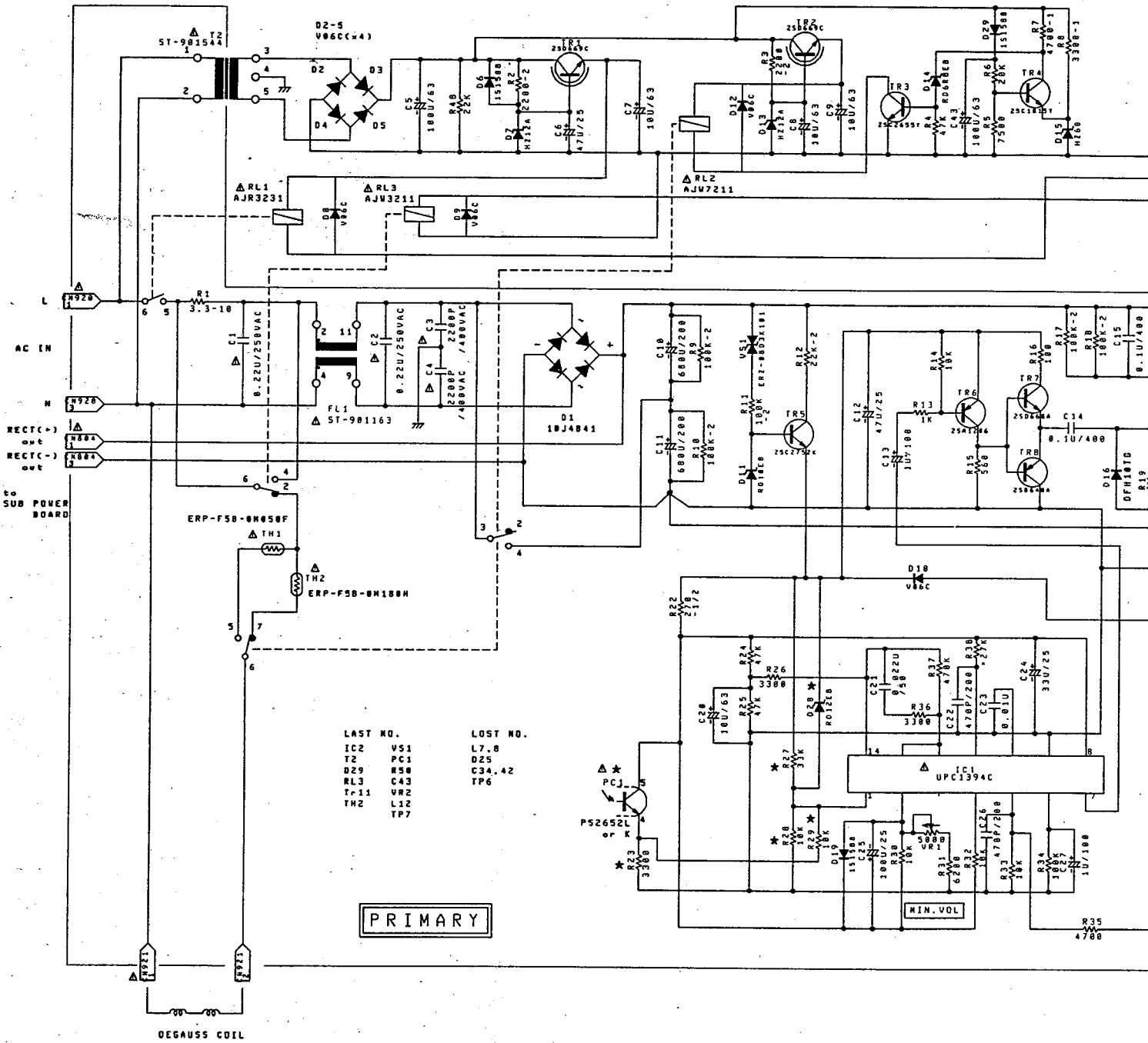
VR1 (MIN. VOL)

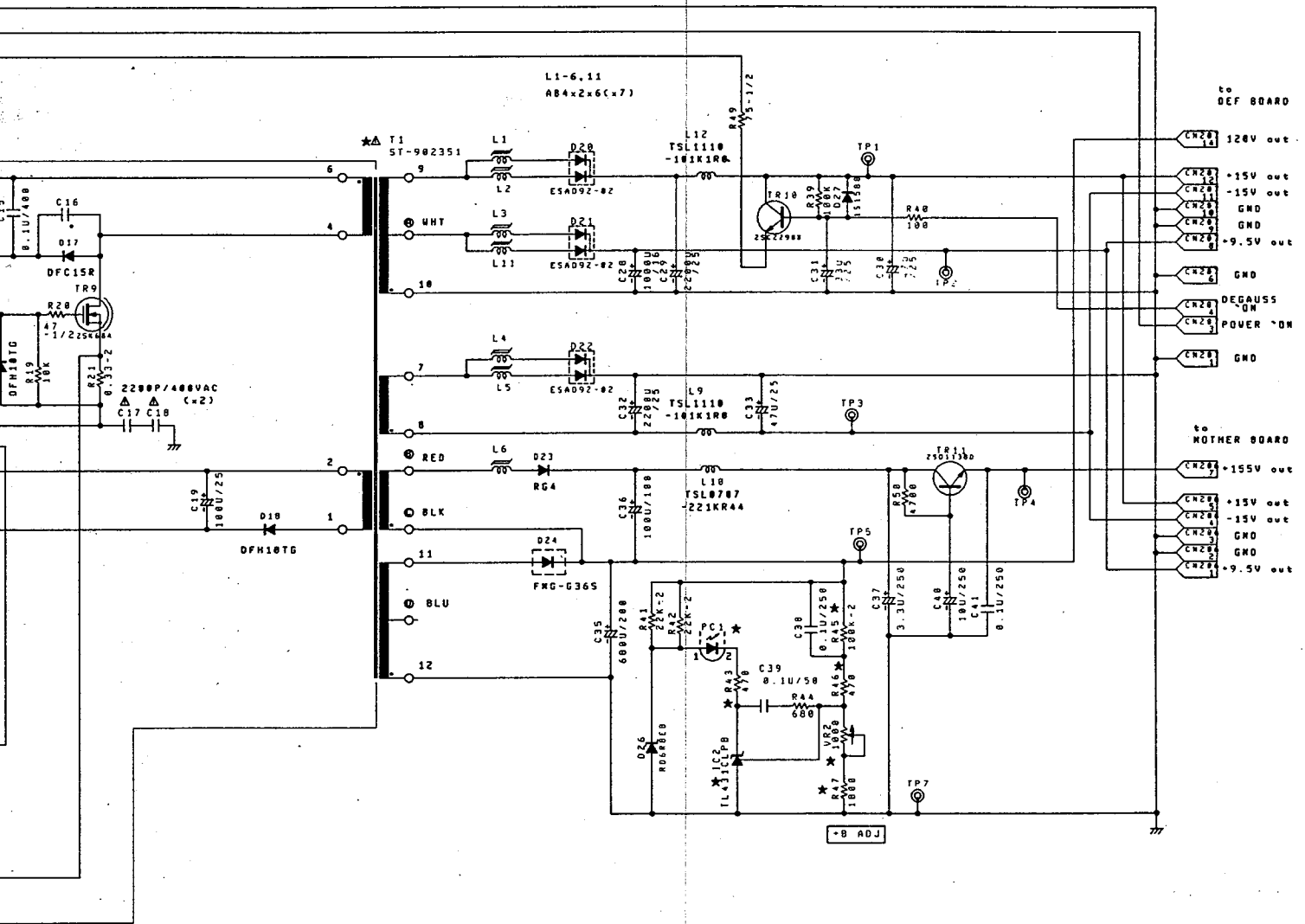
VR2 (+B ADJ)



20/30 SERIES
POWER BOARD
PARTS LOCATION
P-70399D







NOTE:

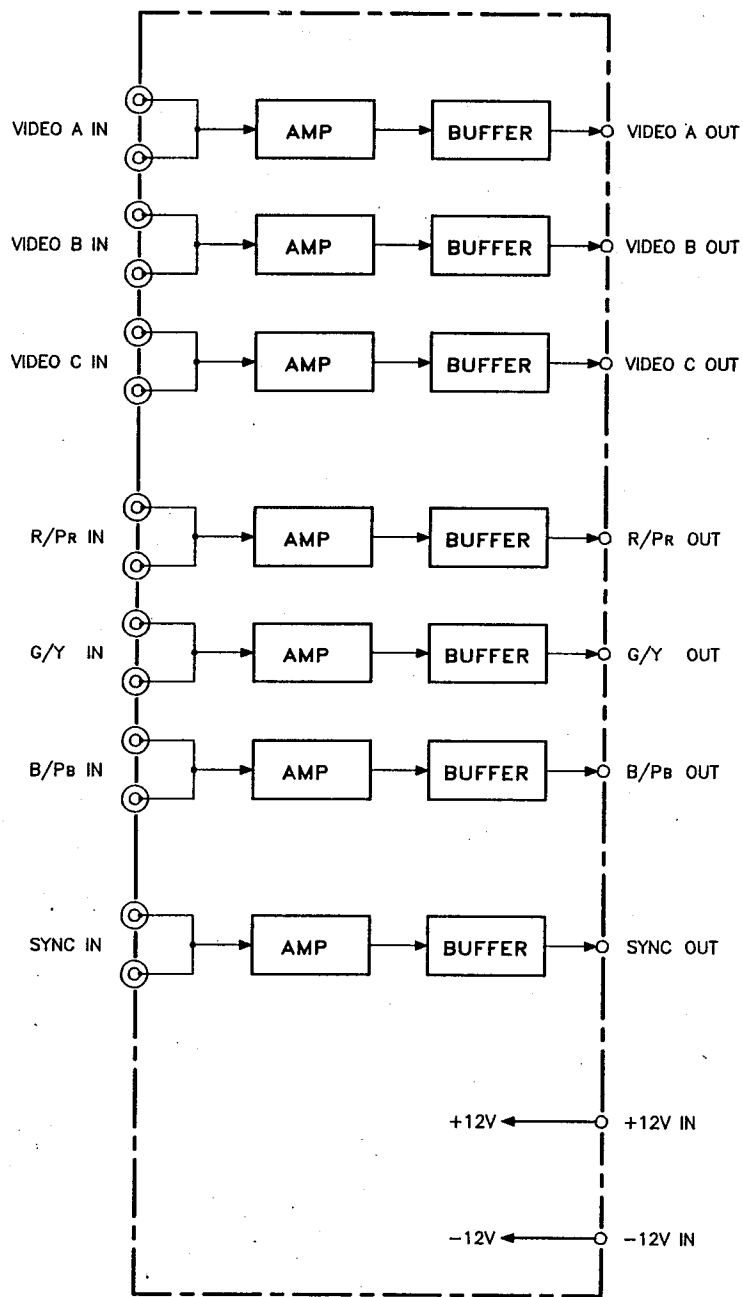
1. All resistors are in ohms 5% (parts marked F:1%), 1/10 watt unless otherwise specified.
2. All capacitors are in farads, 300V unless otherwise specified.
3. All inductors are in henry unless otherwise specified.
4. Waveforms are taken with a color bar signal input.
5. Parts marked * are factory selected value.
6. Parts marked ★ are critical components for X-radiation.

**20/30 SERIES
COLOR MONITOR
POWER BOARD
Schematic Diagram
C21-904182B**

4. CONNECTOR BOARD

(1) Outline

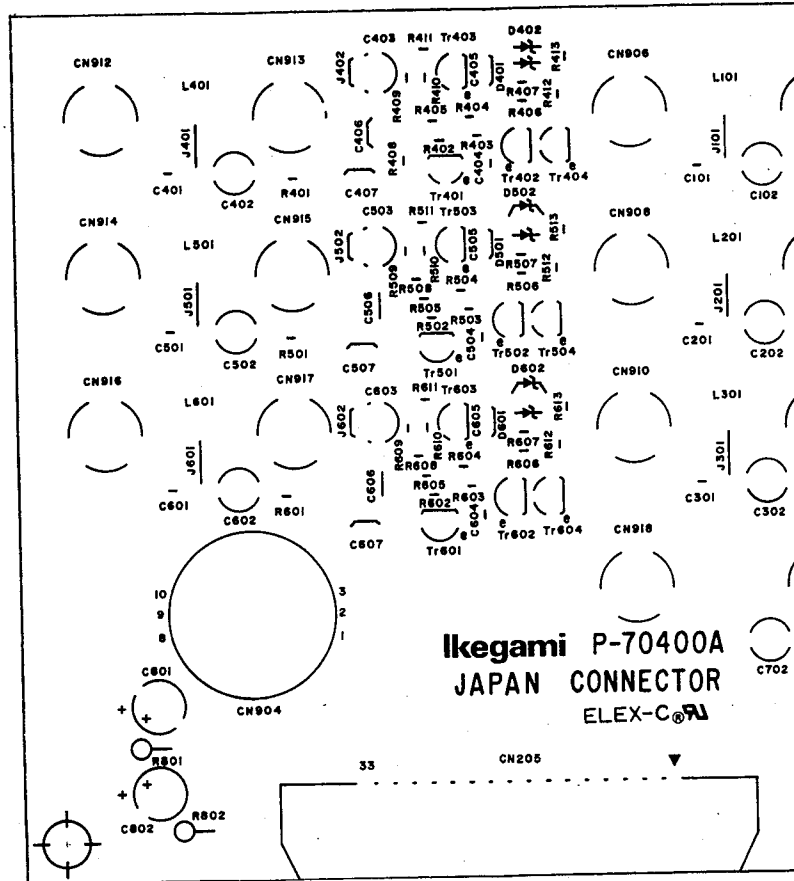
This board supplies various video signals inputted from BNC connectors to the INTERFACE BOARD.

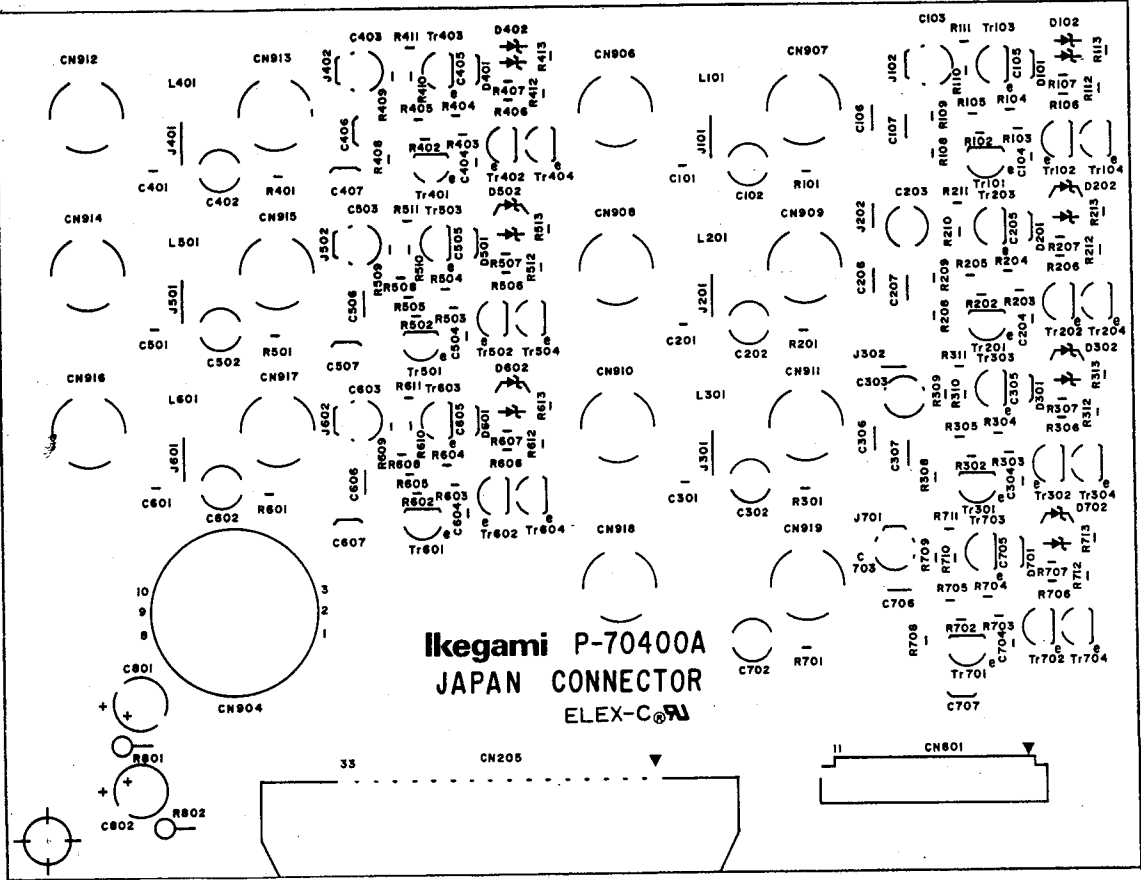


**20/30 SERIES
 COLOR MONITOR
 CONNECTOR BOARD
 Block Diagram**

C4-904322

20/30 SERIES
CONNECTOR BOARD
PARTS LOCATION
P-70400A





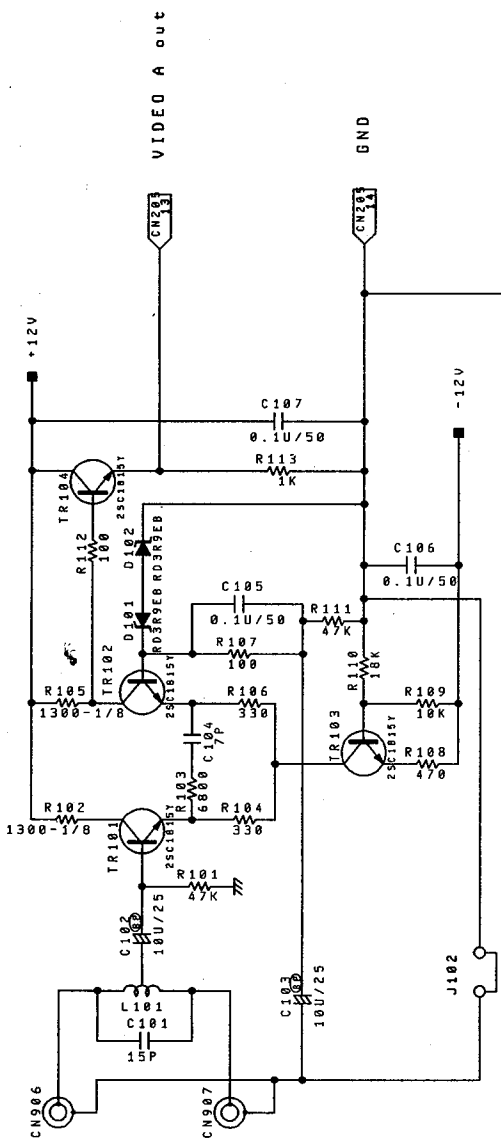
Ikegami P-70400A
 JAPAN CONNECTOR
 ELEX-C®

33 CN205

11 CN801

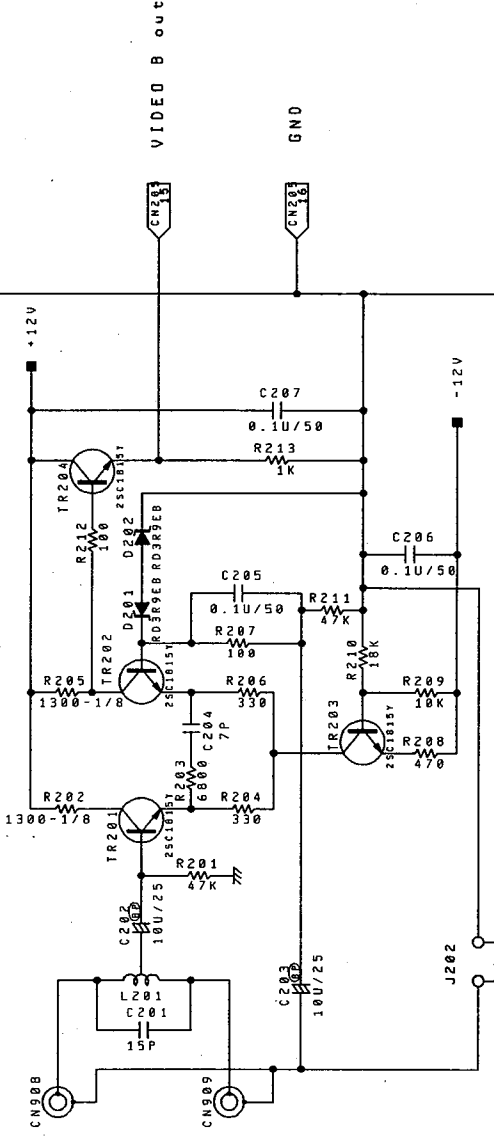
VIDEO A out

VIDEO A in



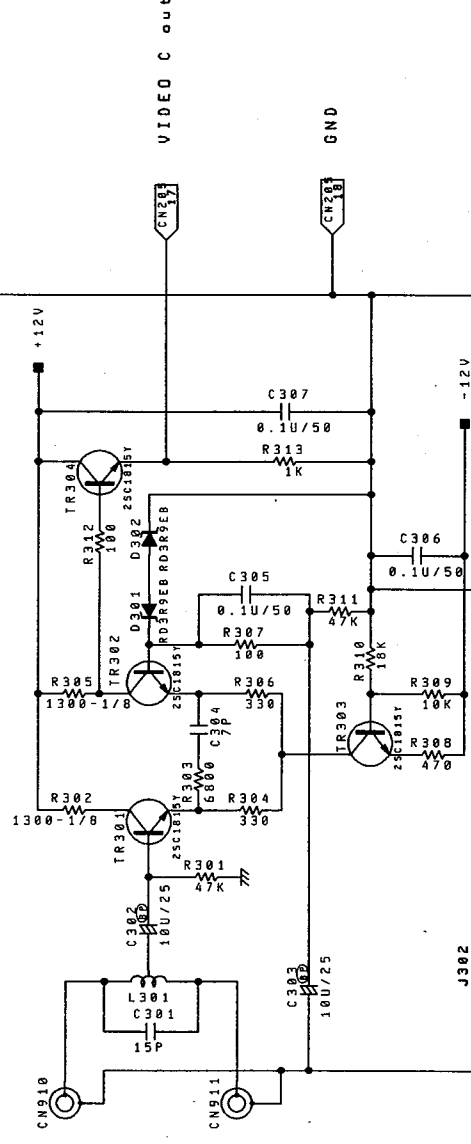
VIDEO B out

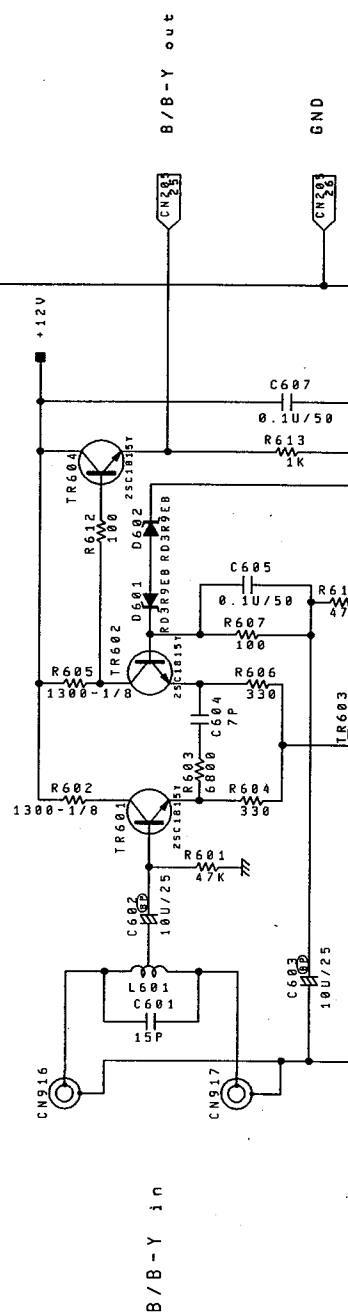
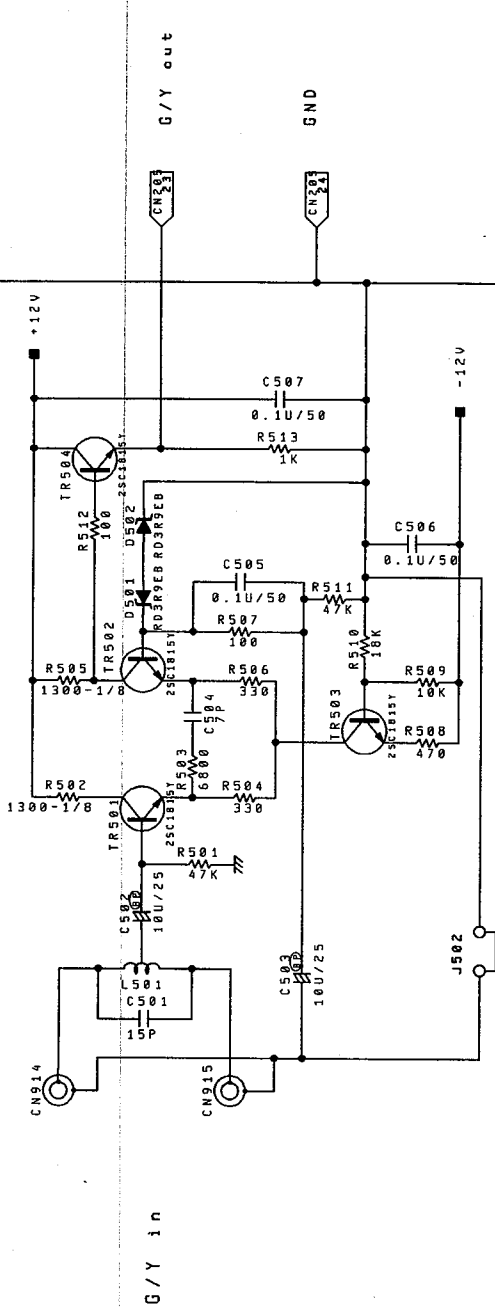
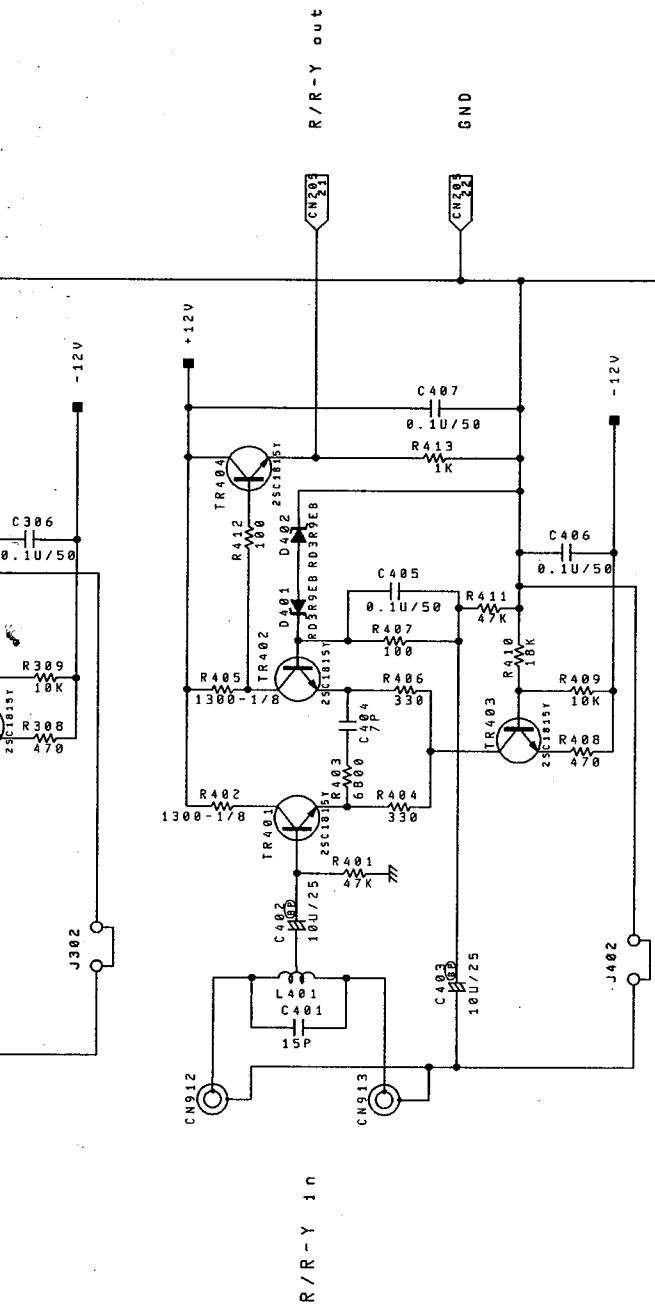
VIDEO B in

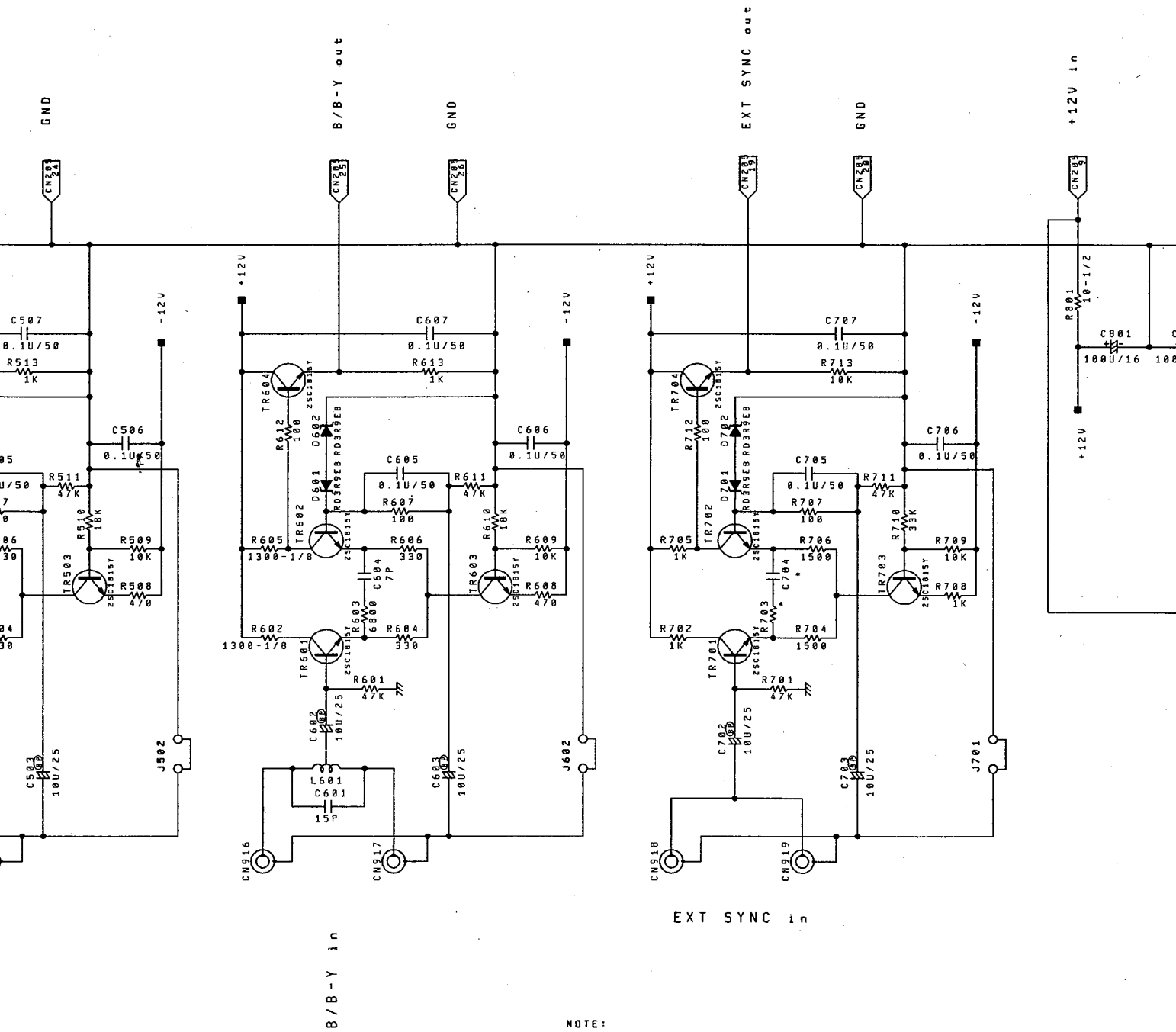


VIDEO C out

VIDEO C in

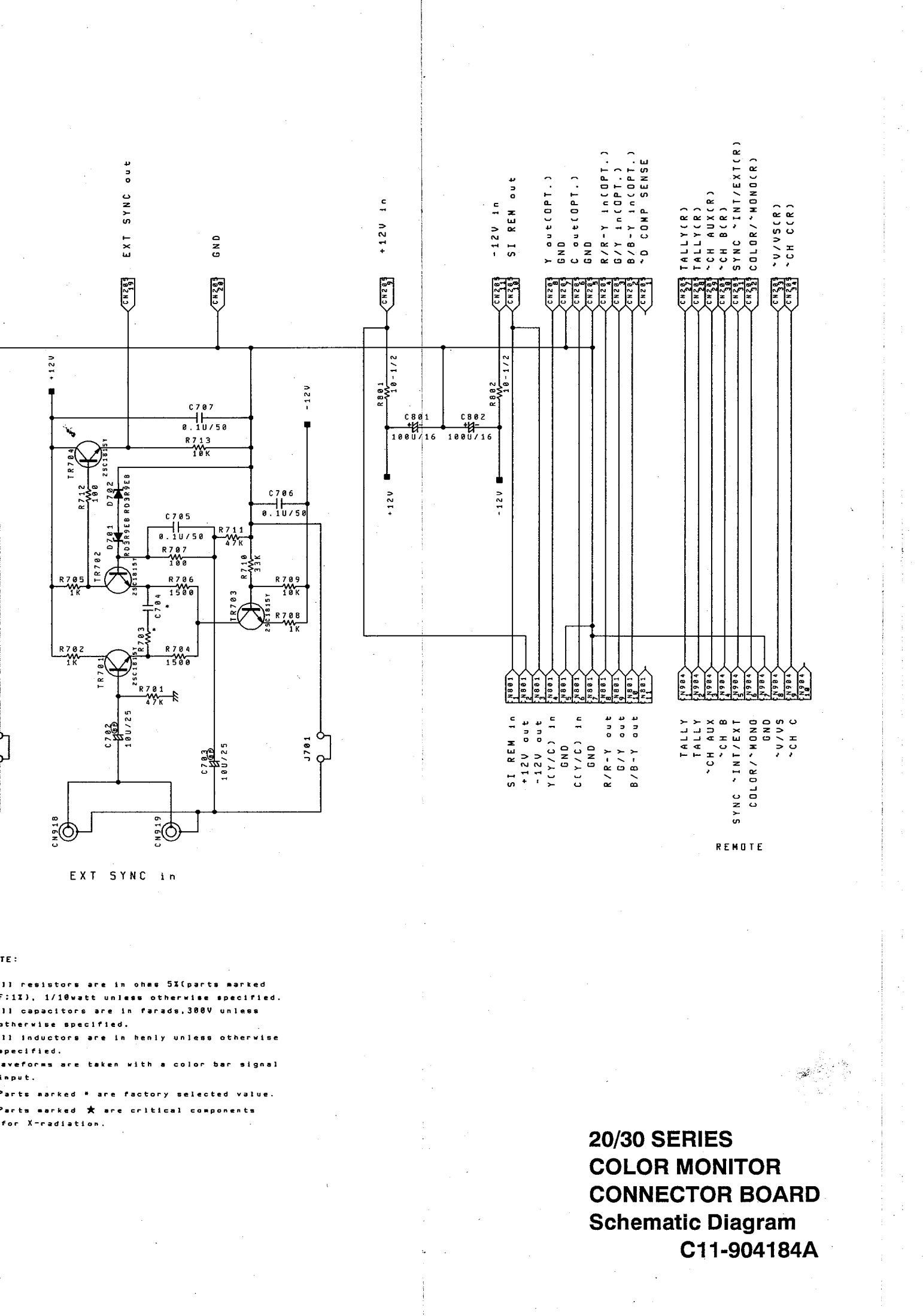






NOTE:

1. All resistors are in ohms 5X (parts marked F:IX), 1/10 watt unless otherwise specified.
2. All capacitors are in farads, 300V unless otherwise specified.
3. All inductors are in henry unless otherwise specified.
4. Waveforms are taken with a color bar signal input.
5. Parts marked * are factory selected value.
6. Parts marked ★ are critical components for X-radiation.



EXT SYNC in

EXT SYNC out

GND

+12V in

-12V in

SI REM out

Y out(OPT.)

GND

C out(OPT.)

GND

R/R-Y in(OPT.)

G/Y in(OPT.)

B/B-Y in(OPT.)

D COMP SENSE

TALLY(R)

TALLY

CH AUX(R)

CH B(R)

SYNC INT/EXT(R)

COLOR/MONO(R)

V/VS(R)

CH C(R)

SI REM in

+12V out

-12V out

Y(Y/C) in

GND

C(Y/C) in

GND

R/R-Y out

G/Y out

B/B-Y out

TALLY

TALLY

CH AUX

CH B

SYNC INT/EXT

COLOR/MONO

GND

V/VS

CH C

REMOTE

NOTE:
 1) resistors are in ohms 5%(parts marked 7:1%), 1/10watt unless otherwise specified.
 2) capacitors are in farads, 380V unless otherwise specified.
 3) inductors are in henry unless otherwise specified.
 Waveforms are taken with a color bar signal input.
 Parts marked * are factory selected value.
 Parts marked * are critical components for X-radiation.

**20/30 SERIES
 COLOR MONITOR
 CONNECTOR BOARD
 Schematic Diagram
 C11-904184A**

5. CONTROL SYSTEM

(1) Outline

This system reads the position datas of each switch, rotary encoder and remote number on the front panel, and transmits them to the CPU on the MPU BOARD. The CPU controls each LED, etc..

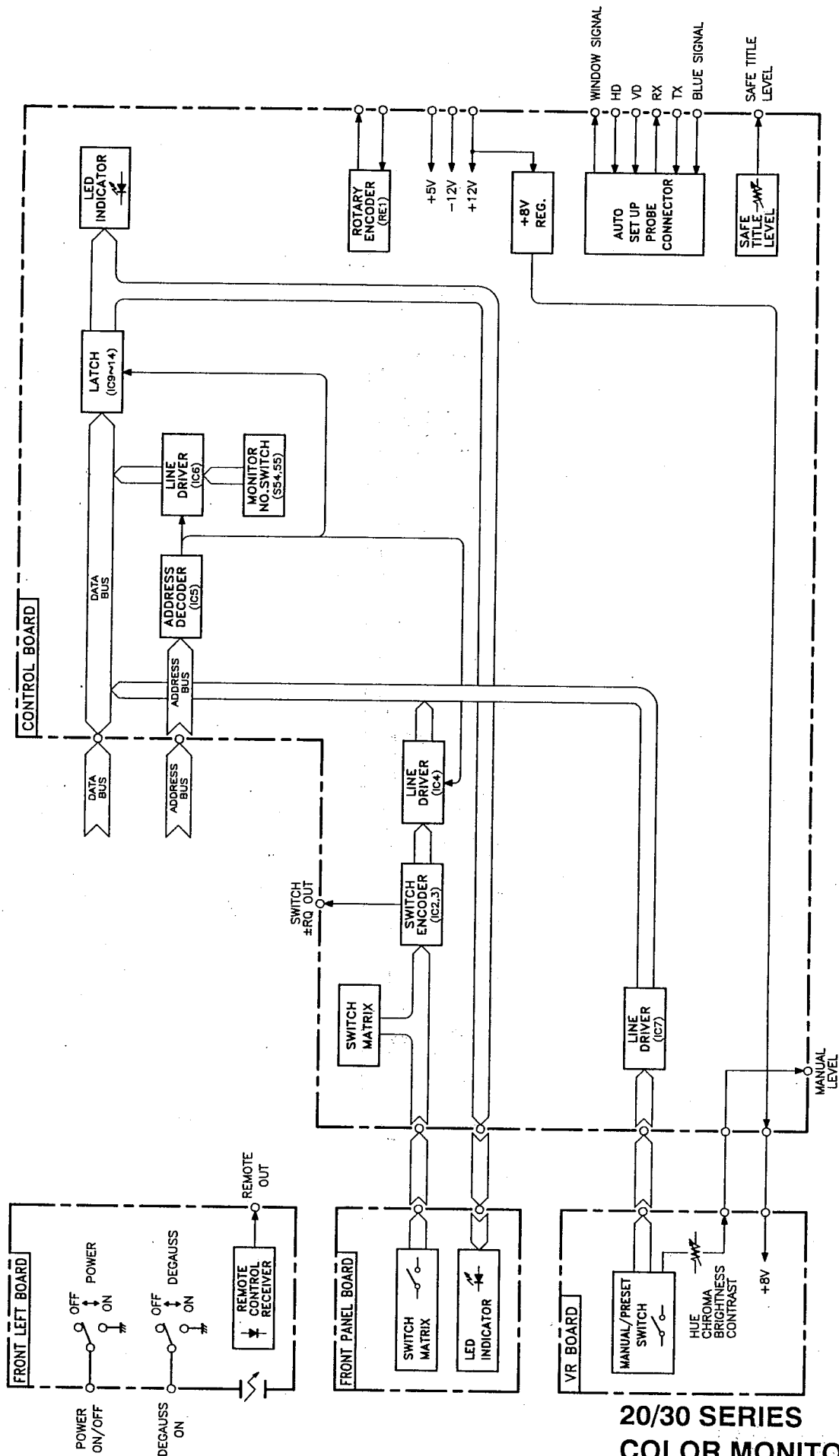
(2) Circuit Description

(a) *Switch matrix circuit*

The switches on the front panel are of a matrix configuration and consists of IC1 ~ IC3. IC2 and IC3 are provided for encoder. When the switch is pressed, the output turns into a 6-bit code, which is read in CPU. When the switch is pressed, Pin 15 of IC3 outputs "H". At this moment, CPU interrupts the program and reads the switch code.

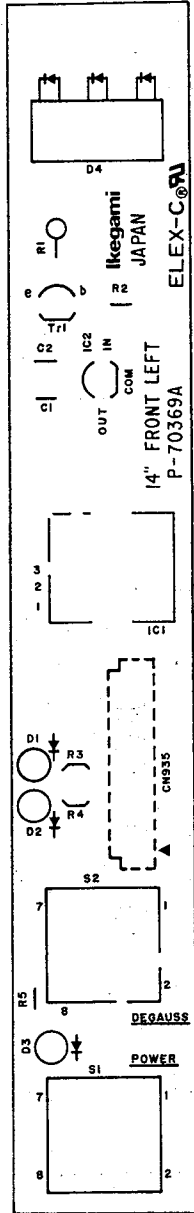
(b) *I/O processings*

The address bus connecting from the MPU BOARD is allocated to address 8 (\$2000 ~ \$2007) by the address decoder of IC5 and latches each input/output port. The latched data is transmitted to CPU through the data bus connecting from the MPU BOARD.

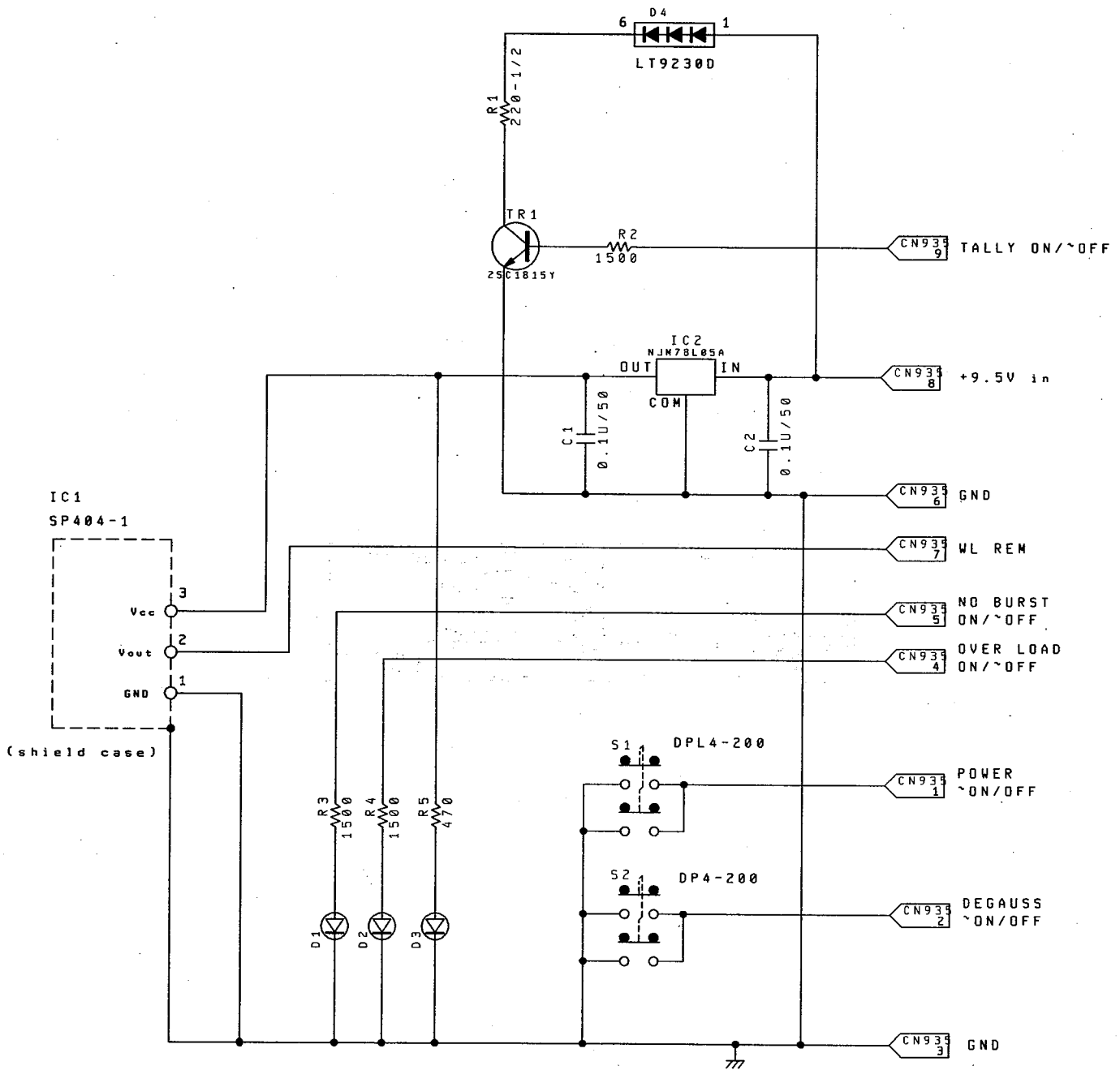


**20/30 SERIES
COLOR MONITOR
CONTROL SYSTEM
Block Diagram**

C3-904325

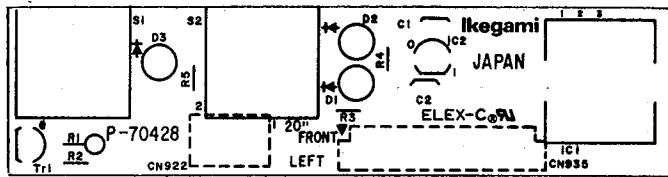


20/30 SERIES
14" FRONT LEFT BOARD
PARTS LOCATION
P-70369A

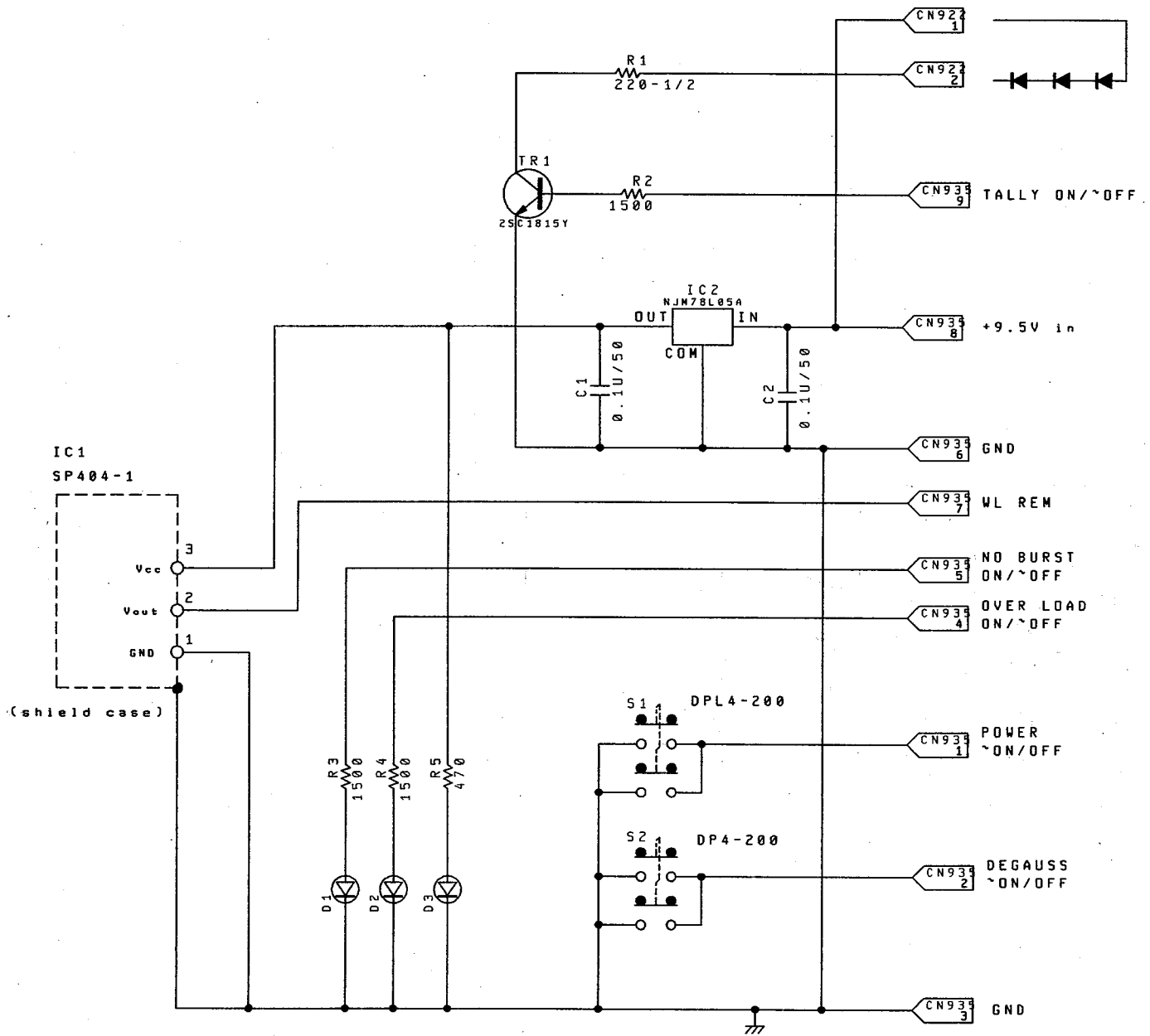


- NOTE:
1. All resistors are in ohms 5% (parts marked F:1%). 1/4 watt unless otherwise specified.
 2. All capacitors are in farads, 300V unless otherwise specified.
 3. All inductors are in henly unless otherwise specified.
 4. Waveforms are taken with a color bar signal input.
 5. Parts marked * are factory selected value.
 6. Parts marked ★ are critical components for X-radiation.

**20/30 SERIES
COLOR MONITOR
14" FRONT LEFT BOARD
Schematic Diagram
C4-904142A**



20/30 SERIES
20" FRONT LEFT BOARD
PARTS LOCATION
P-70428

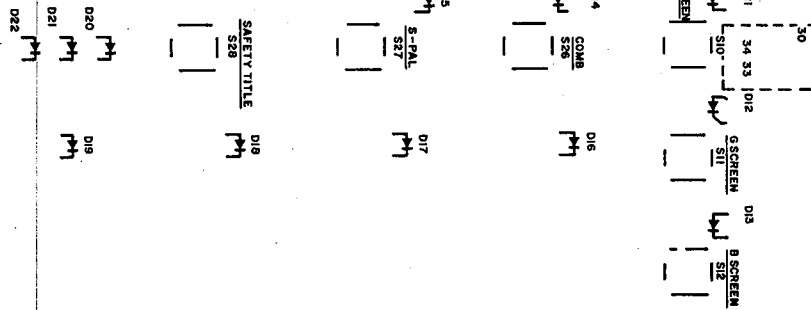


- NOTE:
1. All resistors are in ohms 5% (parts marked F:1%), 1/4 watt unless otherwise specified.
 2. All capacitors are in farads, 300V unless otherwise specified.
 3. All inductors are in henly unless otherwise specified.
 4. Waveforms are taken with a color bar signal input.
 5. Parts marked * are factory selected value.
 6. Parts marked ★ are critical components for X-radiation.

**20/30 SERIES
COLOR MONITOR
20" FRONT LEFT BOARD
Schematic Diagram
C4-904105A**

Ikegami JAPAN

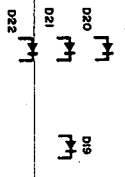
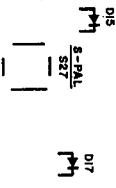
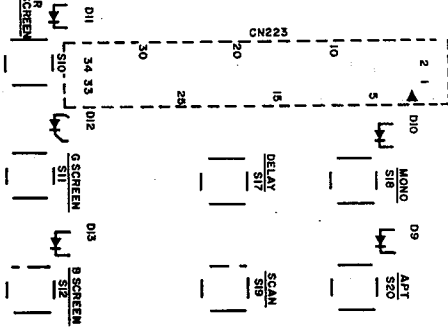
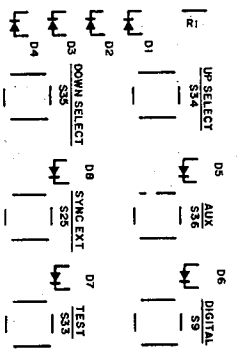
P-70443 14" FRONT PANEL ELEX-C[®]PA

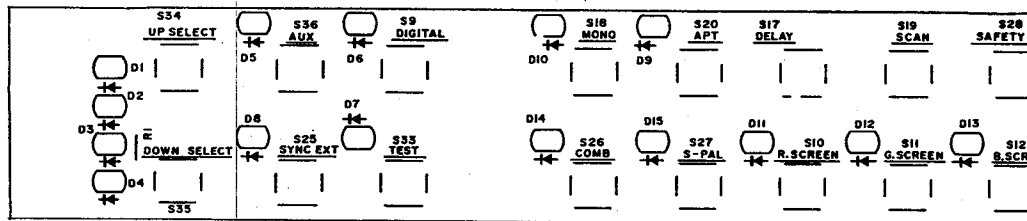


20/30 SERIES
14" FRONT PANEL BOARD
PARTS LOCATION
P-70443

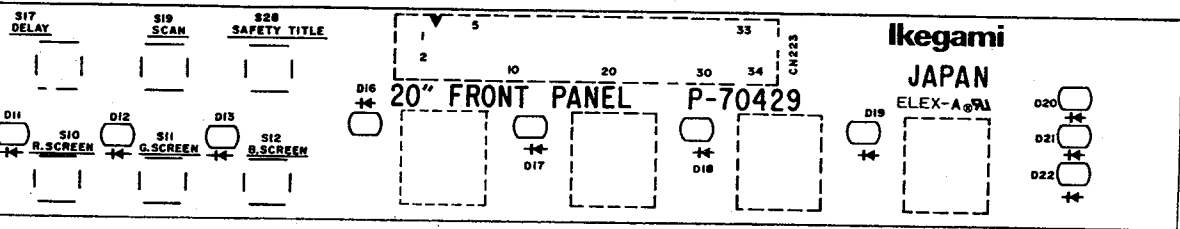
Ikegami JAPAN

P-70443 14" FRONT PANEL ELEX-C

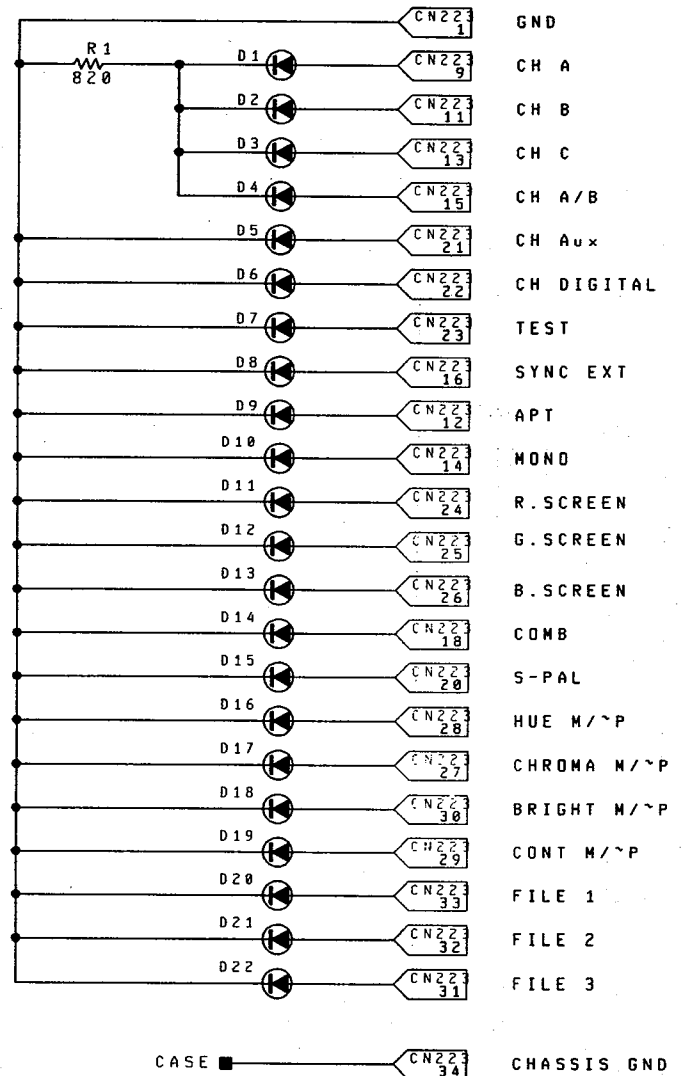




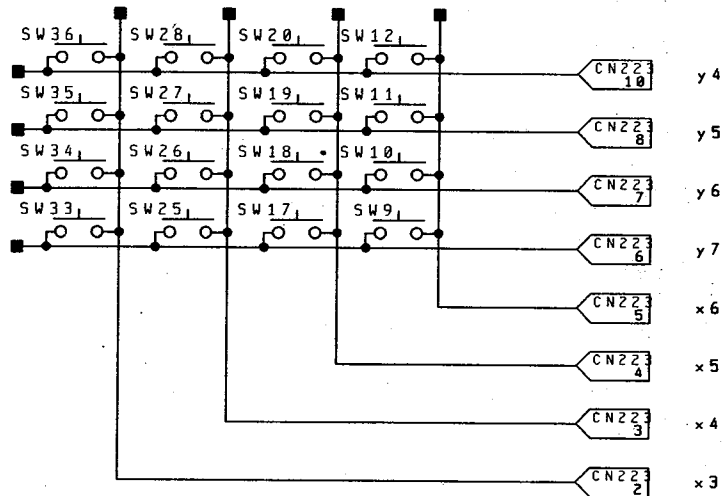
20/30 SERIES
20" FRONT PANEL BOARD
PARTS LOCATION
P-70429



D1 to D15 : TLG226
 D20 to D22 : TLG226
 D16 to D19 : TLY226

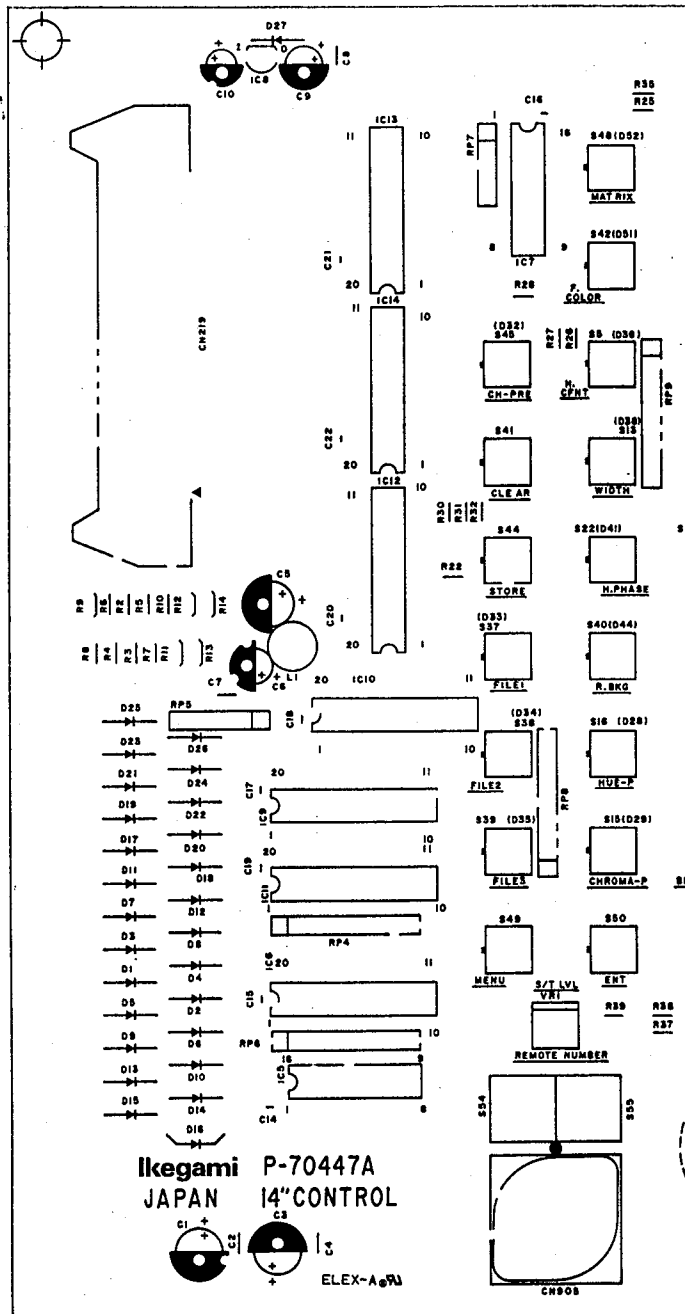


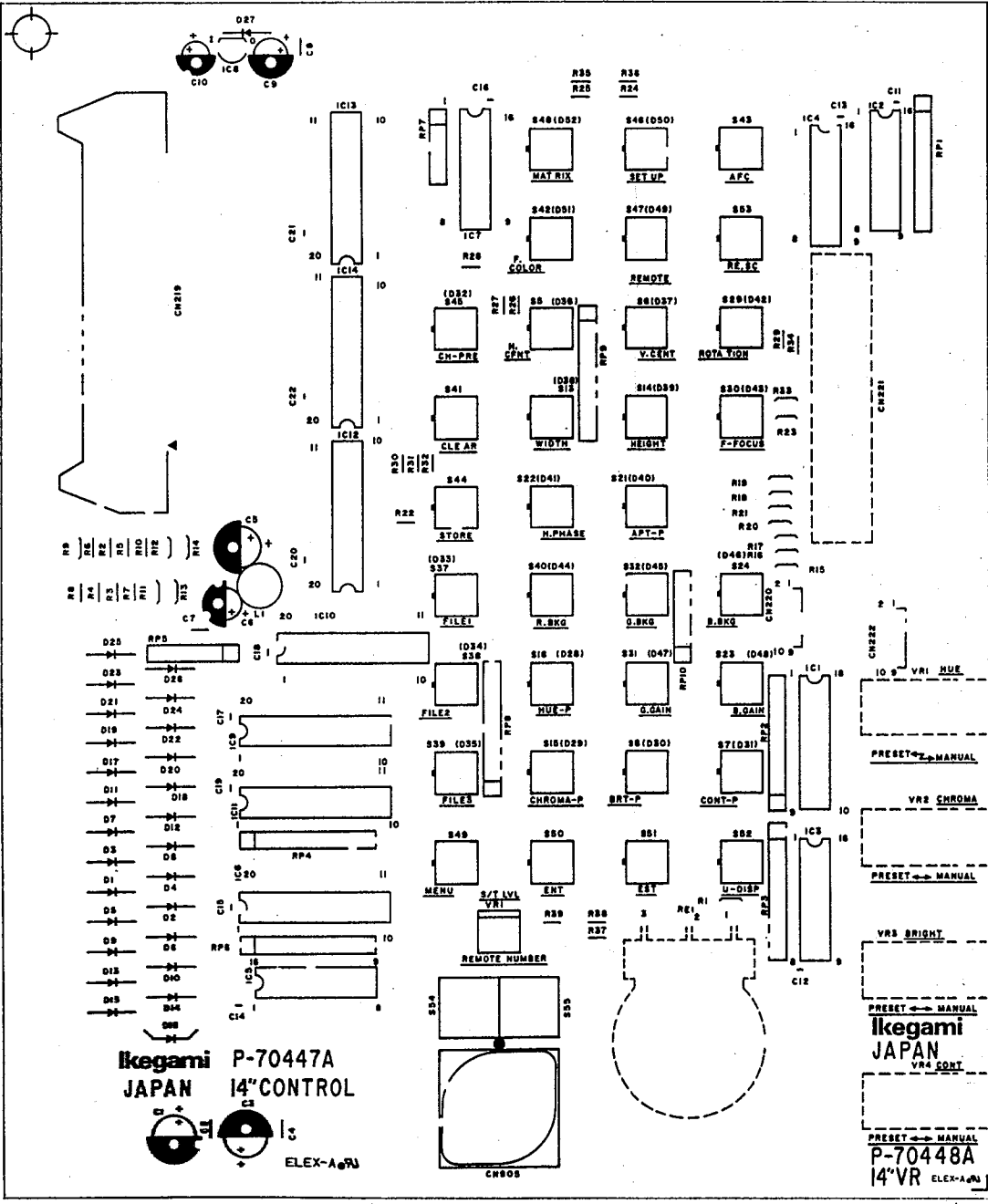
S9: DIGITAL
 S10: R. SCREEN
 S11: G. SCREEN
 S12: B. SCREEN
 S17: DELAY
 S18: MONO
 S19: SCAN
 S20: APT
 S25: SYNC EXT
 S26: COMB
 S27: S-PAL
 S28: SAFETY TITLE
 S33: TEST
 S34: UP SELECT
 S35: DOWN SELECT
 S36: AUX



**20/30 SERIES
 COLOR MONITOR
 14" /20"
 FRONT PANEL BOARD
 Schematic Diagram
 C4-904225**

20/30 SERIES
14" CONTROL & VR BOARDS
PARTS LOCATION
P-70447A/P-70448A

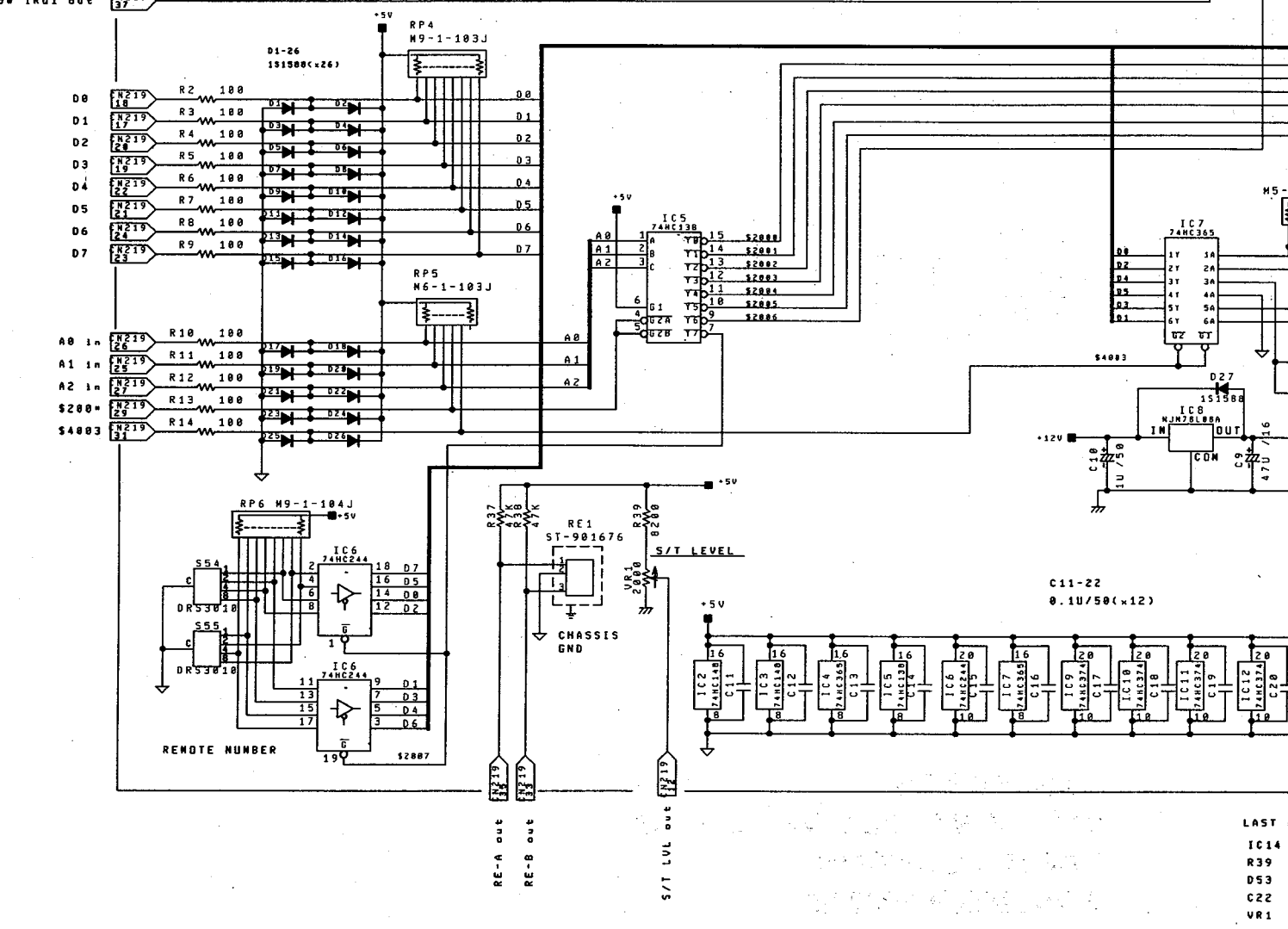
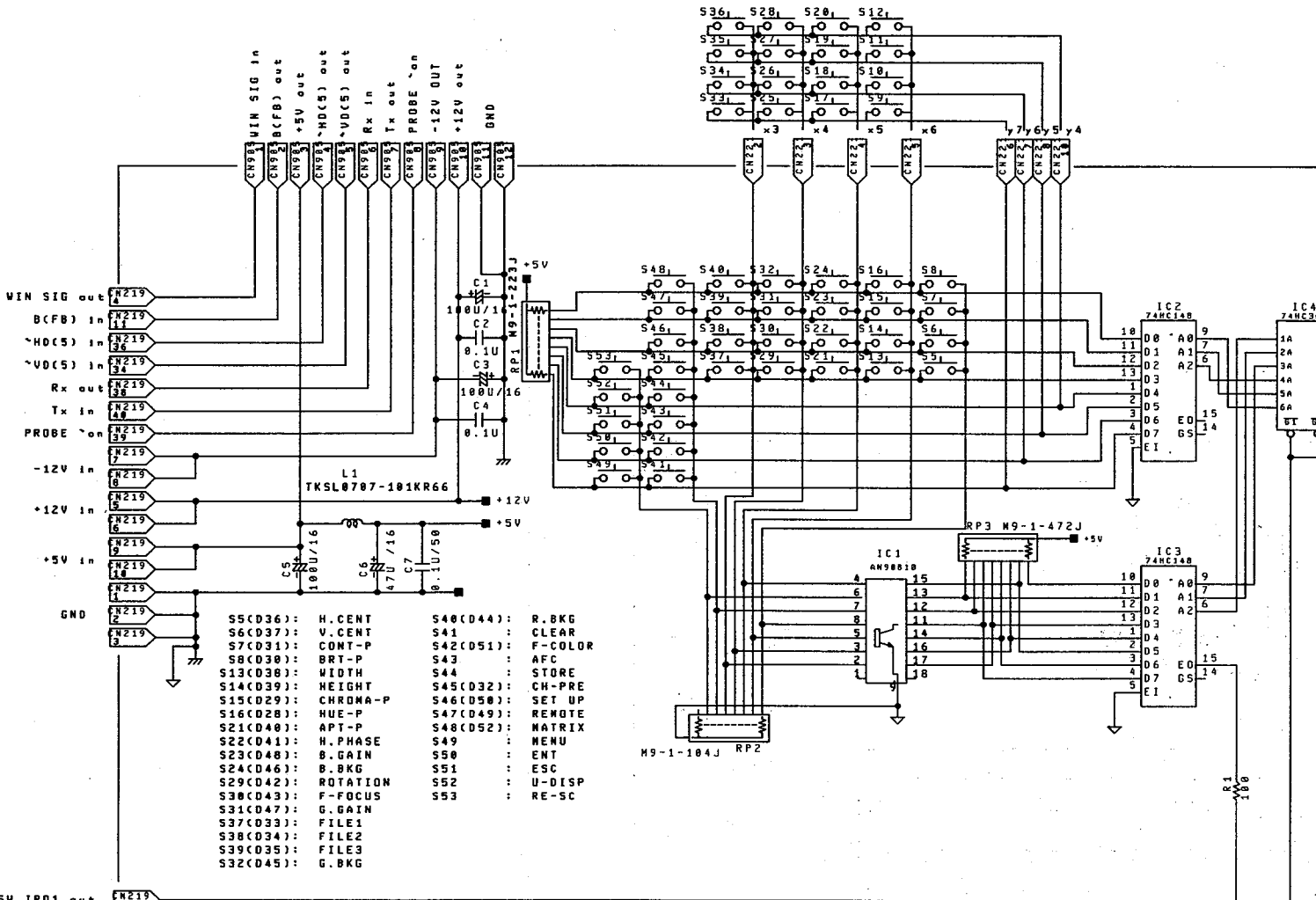




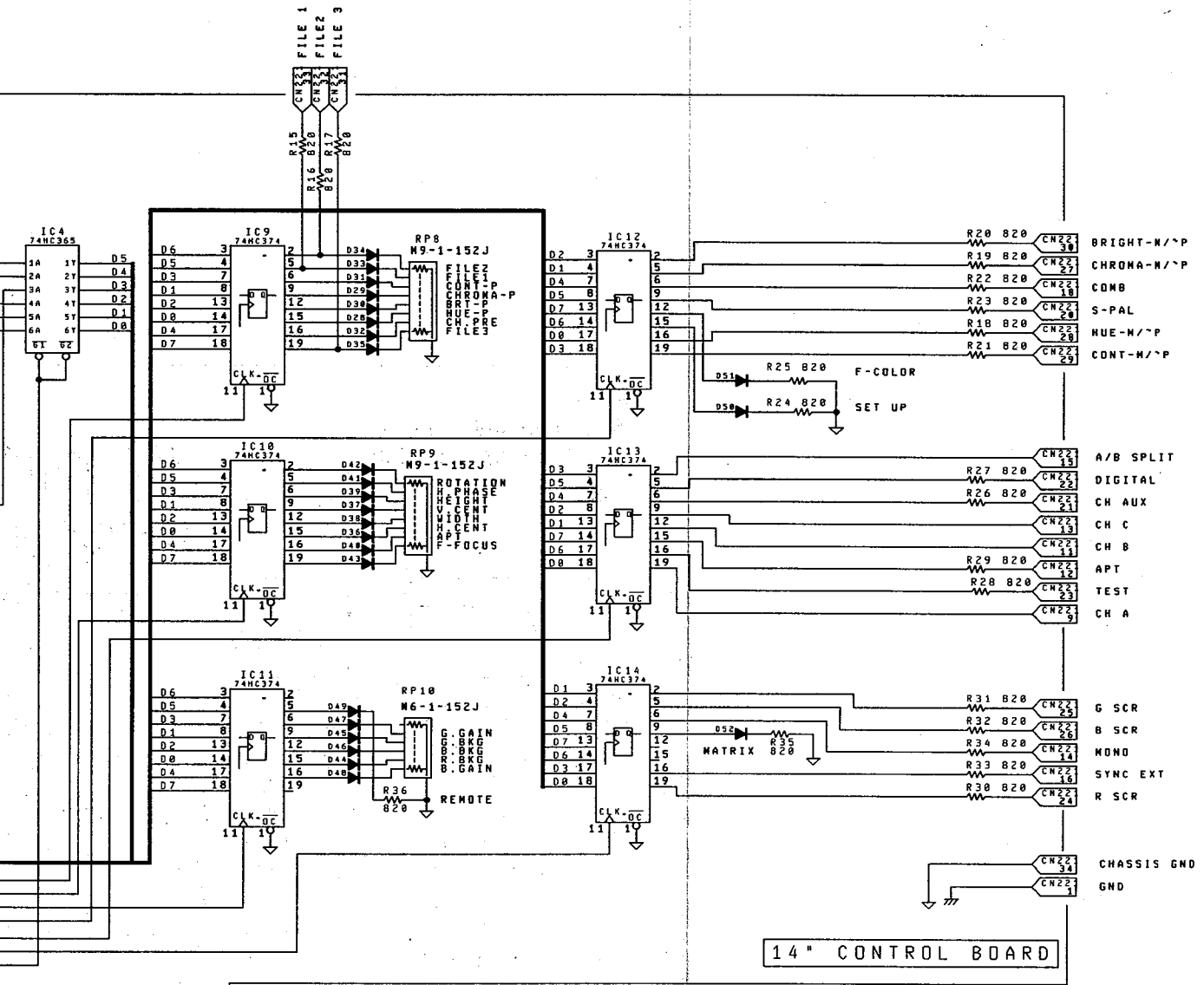
Ikegami P-70447A
 JAPAN 14" CONTROL



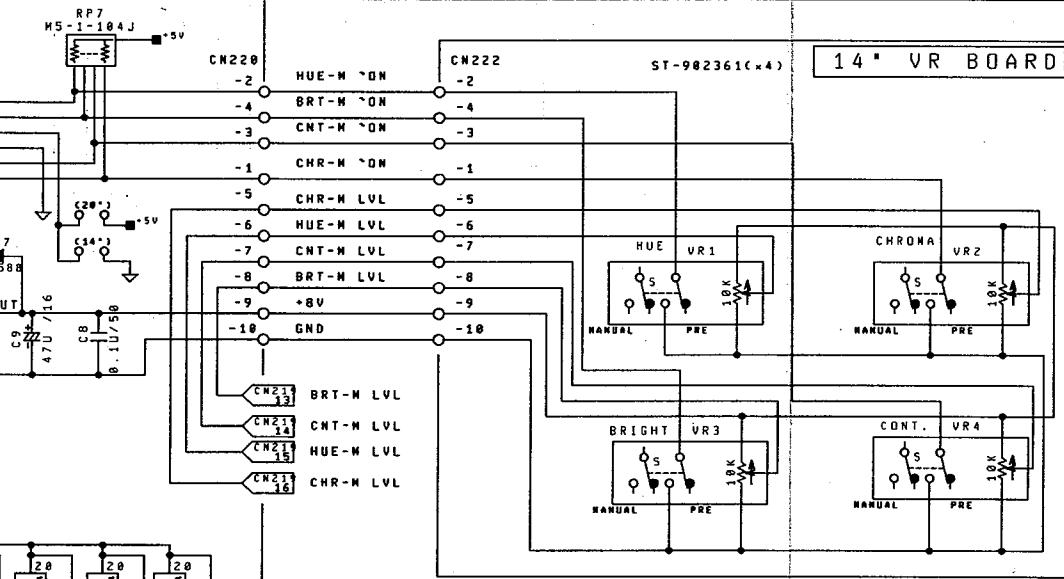
Ikegami
 JAPAN
 VR4 CONT
 PRESET → MANUAL
 P-70448A
 14" VR ELEX-A



LAST M
 IC14
 R39
 D53
 C22
 VR1



14" CONTROL BOARD



NOTE:

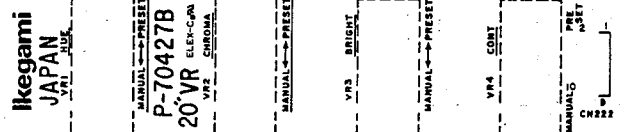
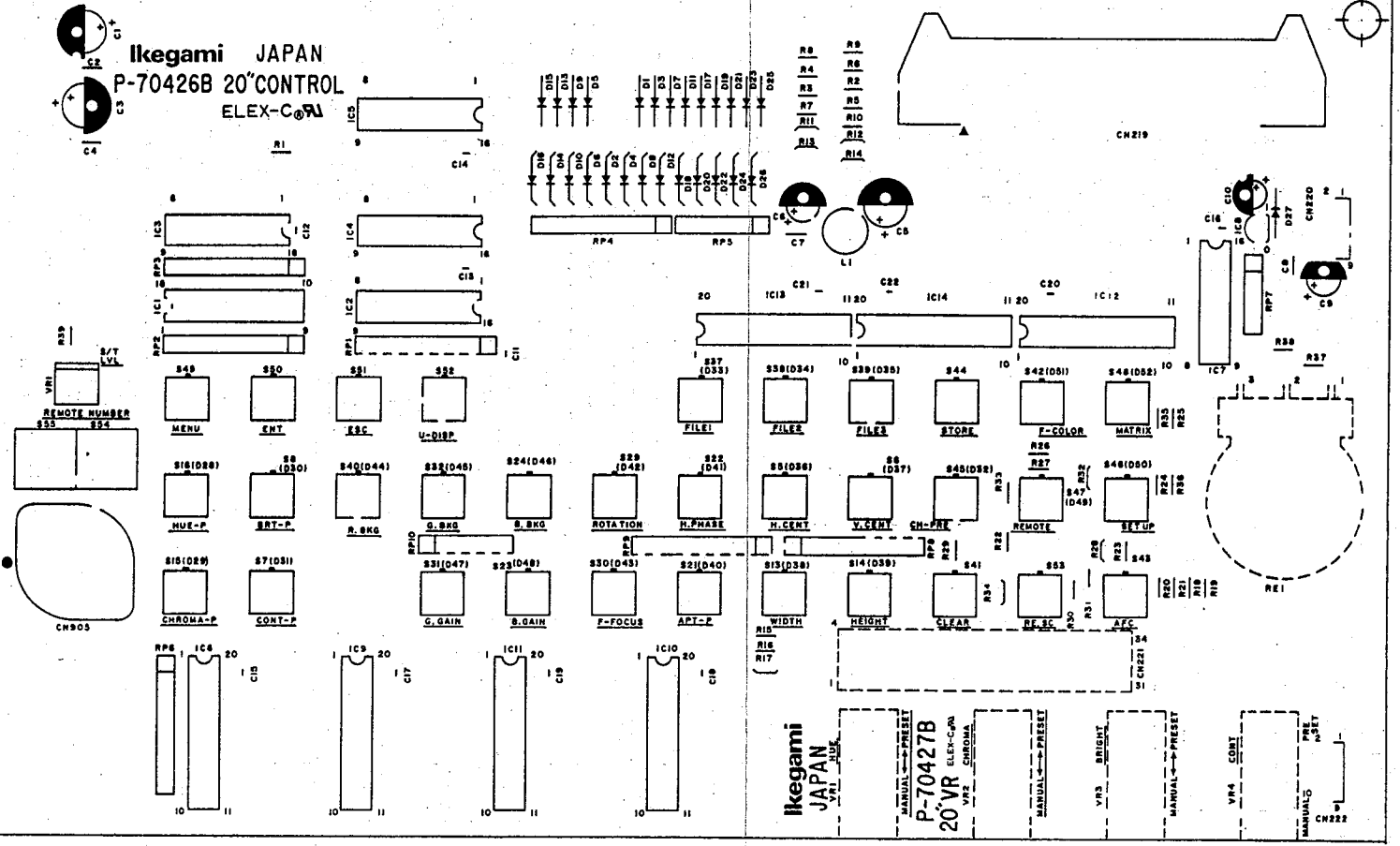
1. All resistors are in ohms 5% (parts marked F:1%), 1/4 watt unless otherwise specified.
2. All capacitors are in farads, 300V unless otherwise specified.
3. All inductors are in henry unless otherwise specified.
4. Waveforms are taken with a color bar signal input.
5. Parts marked * are factory selected value.
6. Parts marked * are critical components for X-radiation.

**20/30 SERIES
COLOR MONITOR
14" CONTROL BOARD
14" VR BOARD
Schematic Diagram
C21-904262A**

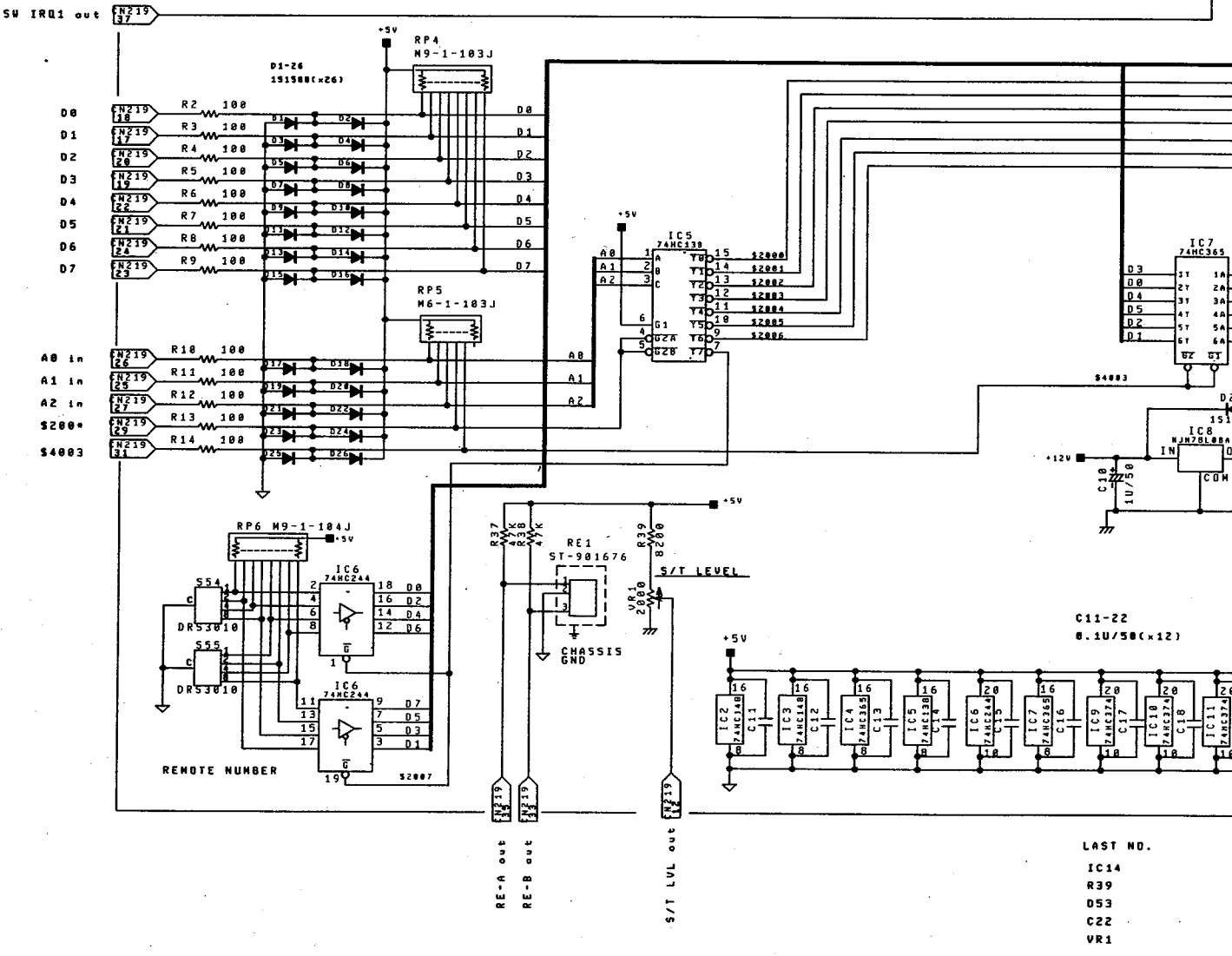
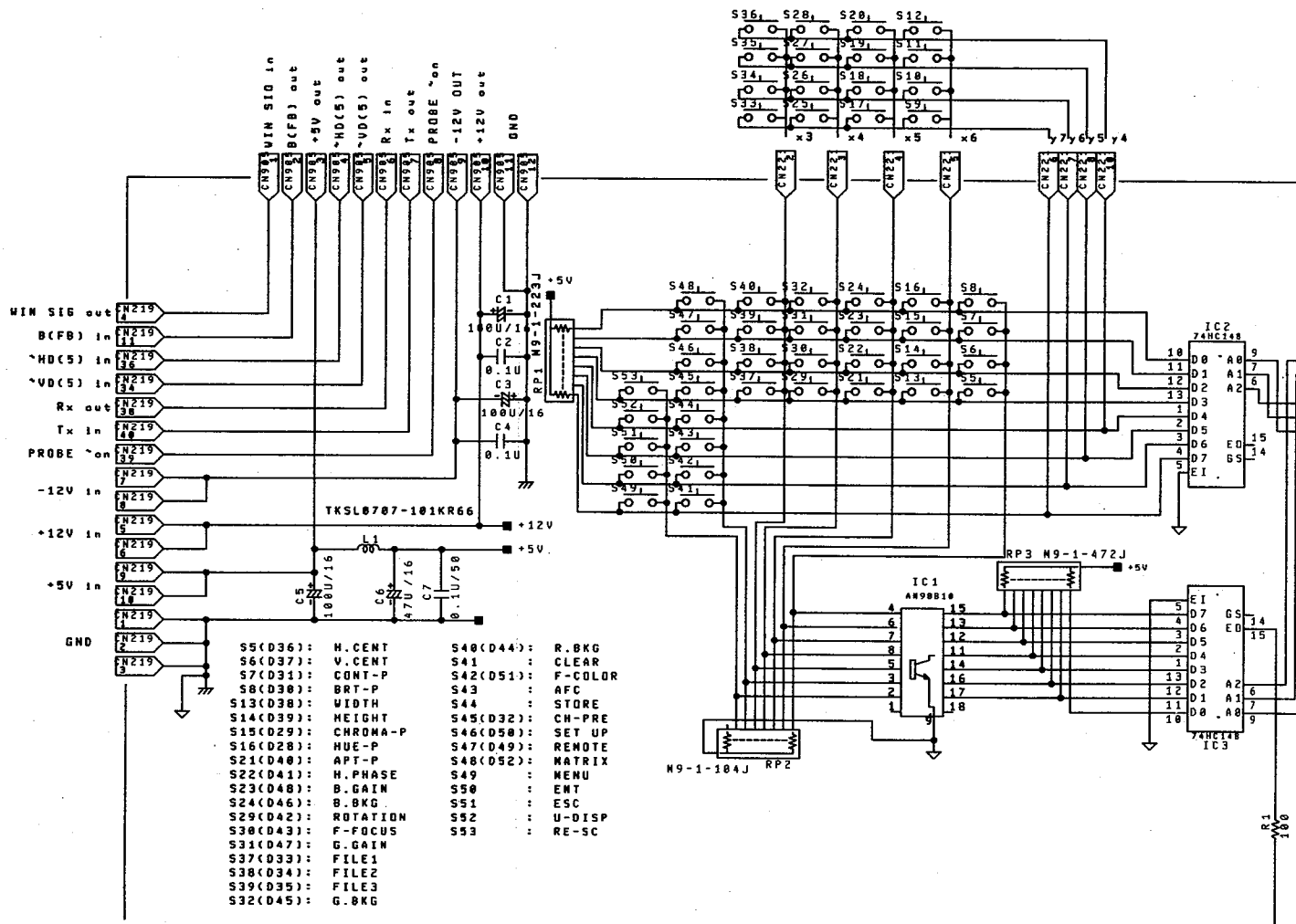
LAST NO.
IC14
R39
D53
C22
VR1

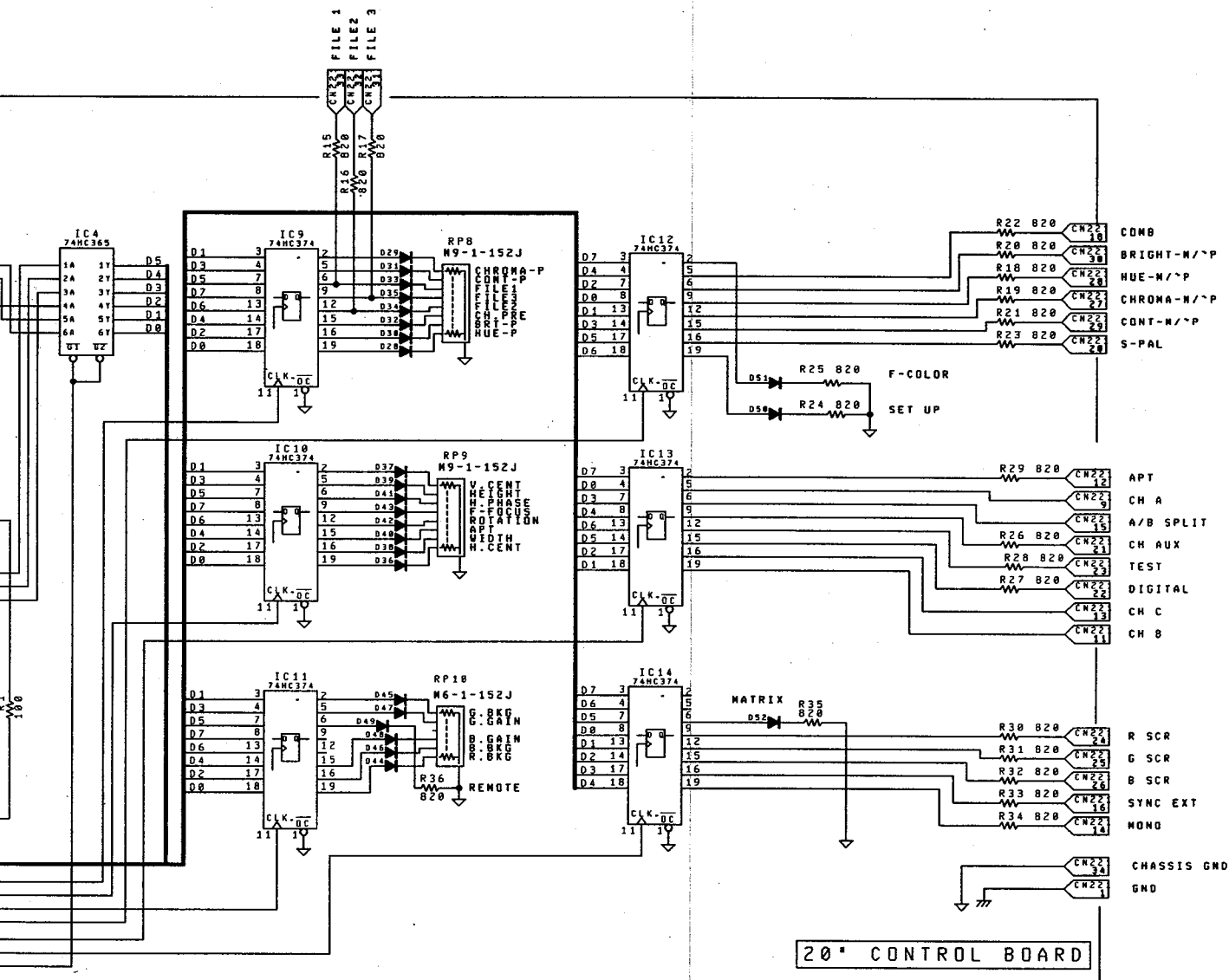


Ikegami JAPAN
P-70426B 20" CONTROL
ELEX-C@A

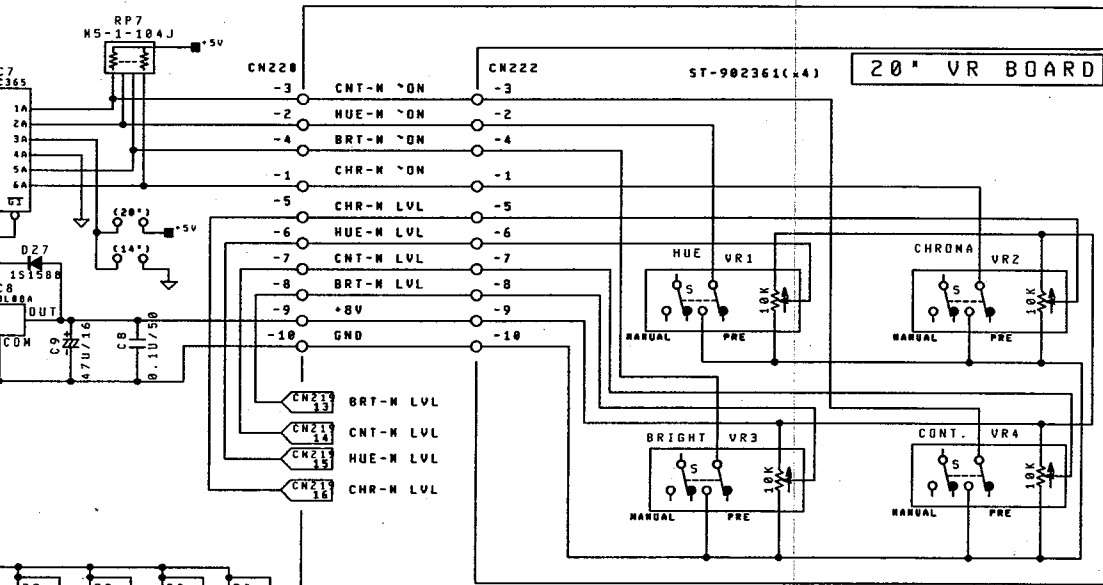


20/30 SERIES
20" CONTROL & VR BOARDS
PARTS LOCATION
P-70426B/P-70427B





20" CONTROL BOARD



20" VR BOARD

NOTE:

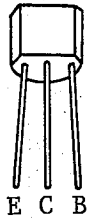
1. All resistors are in ohms 5% (parts marked F:1%), 1/4 watt unless otherwise specified.
2. All capacitors are in farads, 300V unless otherwise specified.
3. All inductors are in henry unless otherwise specified.
4. Waveforms are taken with a color bar signal input.
5. Parts marked * are factory selected value.
6. Parts marked * are critical components for X-radiation.

**20/30 SERIES
COLOR MONITOR
20" CONTROL BOARD
20" VR BOARD
Schematic Diagram
C21-904185A**

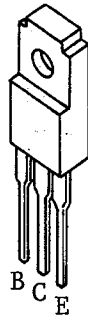
6. PARTS LIST

6-1. SEMICONDUCTORS PIN CONNECTION

2SA1015Y
2SC1815Y



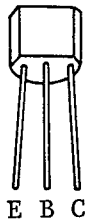
2SB1020
2SD1407Y
2SD1415



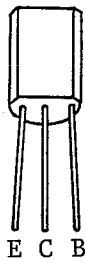
2SK192A-GR



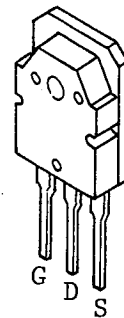
2SA1206
2SC2901
2N3904
2N3906



2SC2655Y



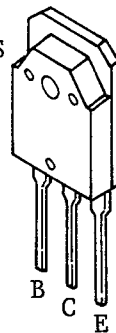
2SK684



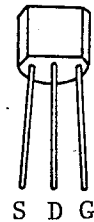
2SA1407E
2SB648AC
2SC2298B
2SC2752K
2SC3601E
2SD668AC
2SD669AC



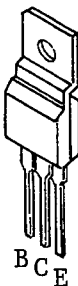
2SD1047E
2SD1064R or S



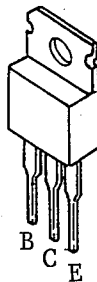
2SK614



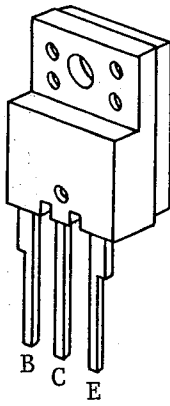
2SC1514



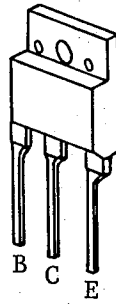
2SB861C
2SC2333K
2SD1138 C or D



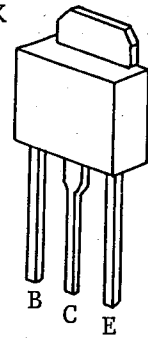
2SC4710



2SC4123

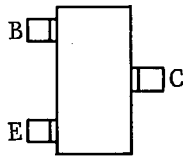


2SC3588K

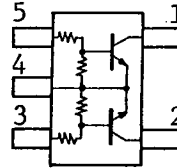


FN1A4M

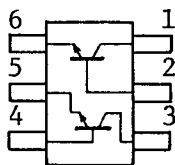
- 2SA812-M6.7
- 2SA1461-Y24
- 2SC1623-L6.7
- 2SC3398
- 2SC3734-B24
- 2SC3735-B35



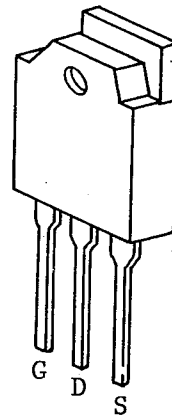
XN1212



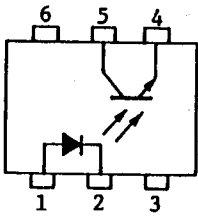
XN6501



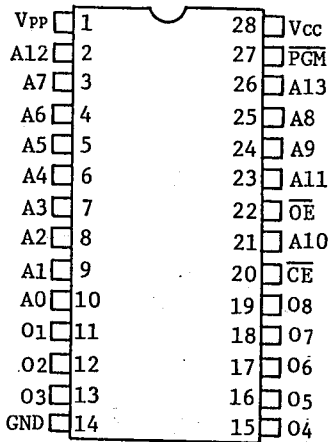
2SK787



PS2652L or K

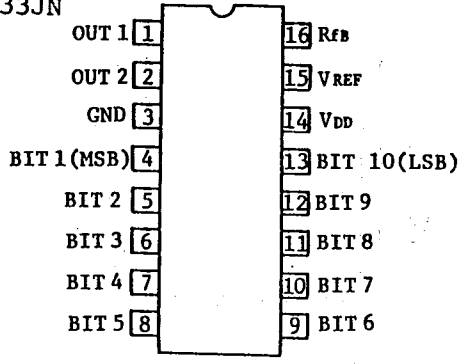
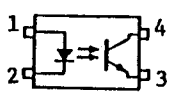


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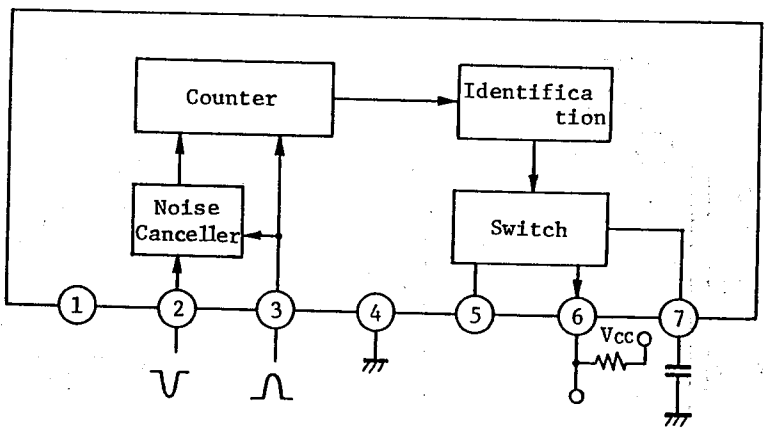


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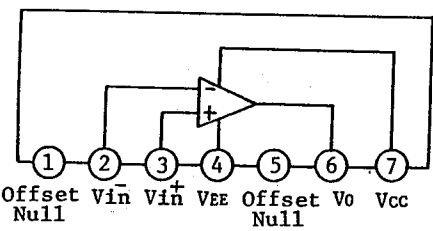
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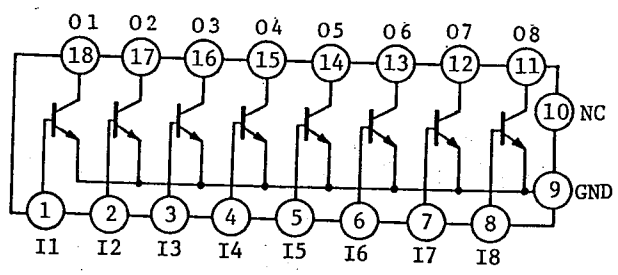
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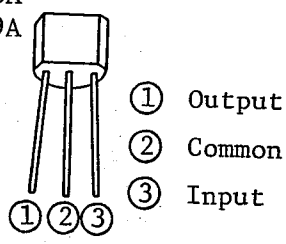
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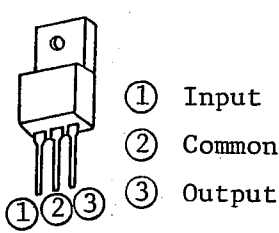
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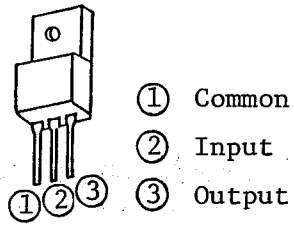
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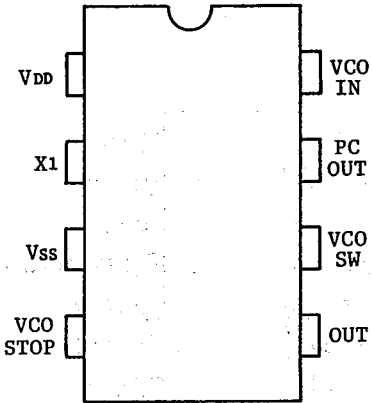
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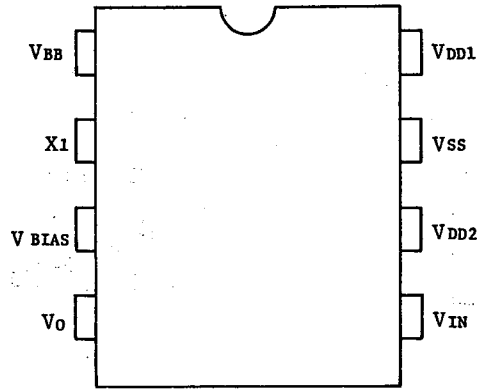
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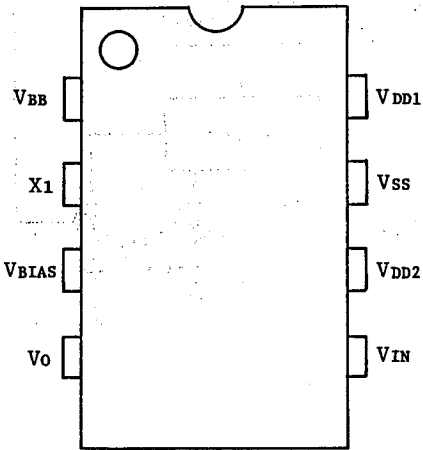
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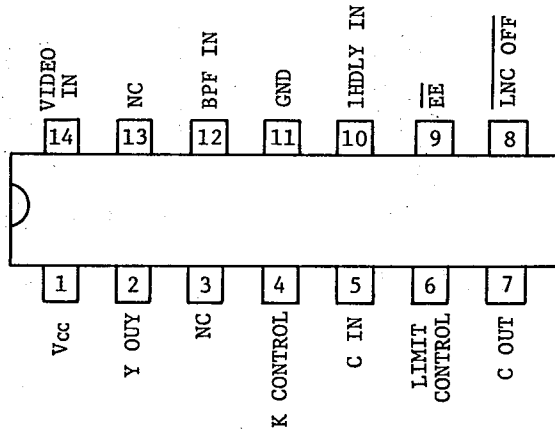
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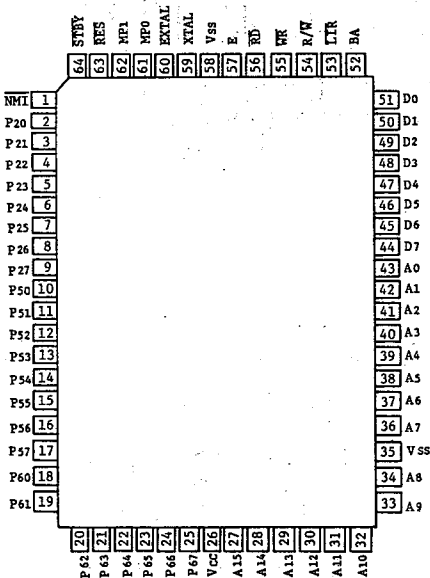
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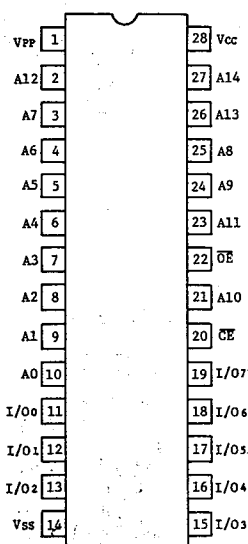
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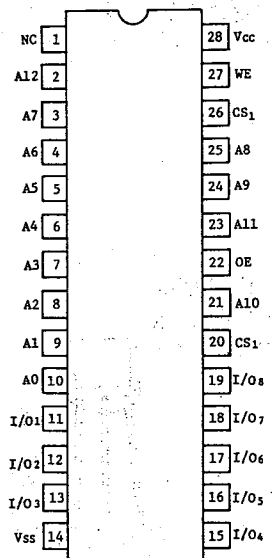
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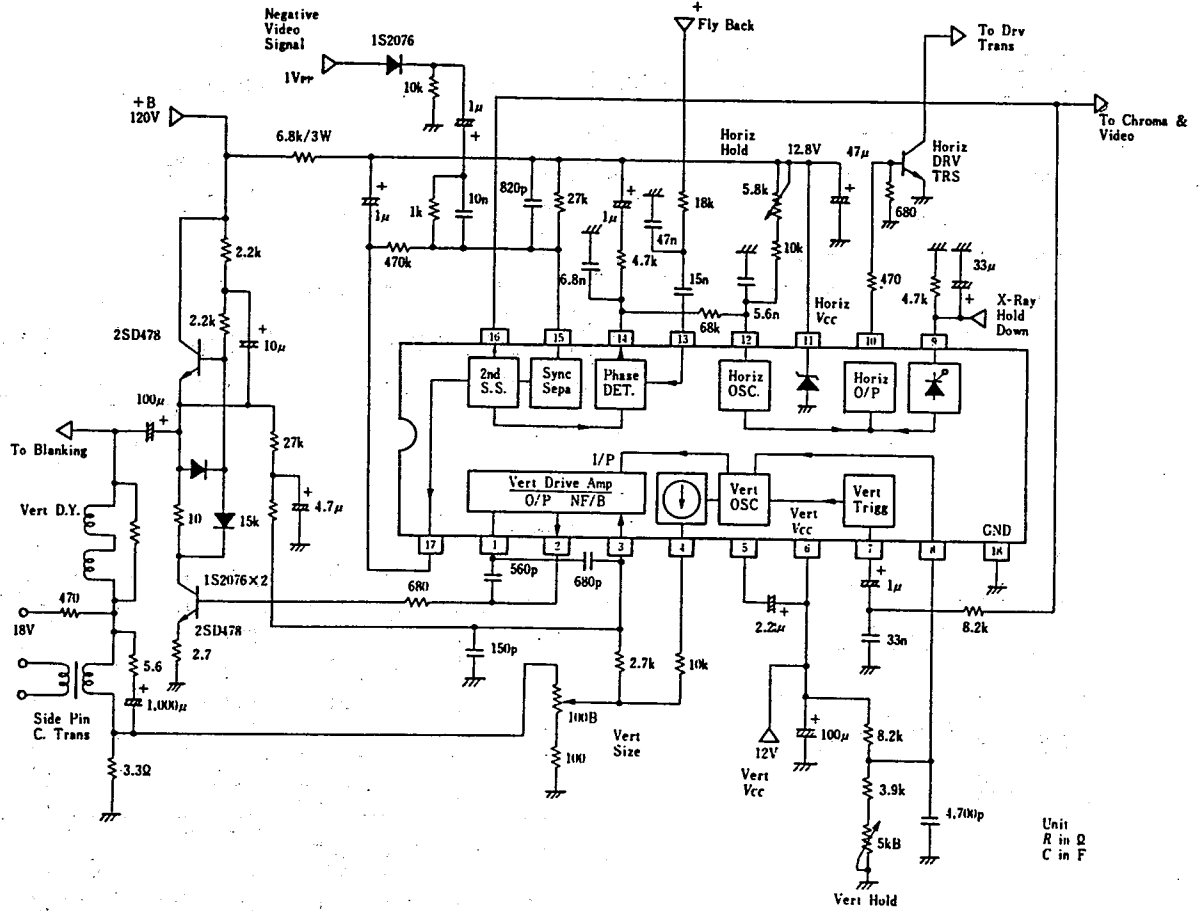
HN27C256G-20



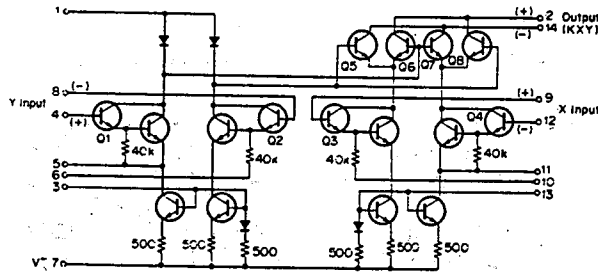
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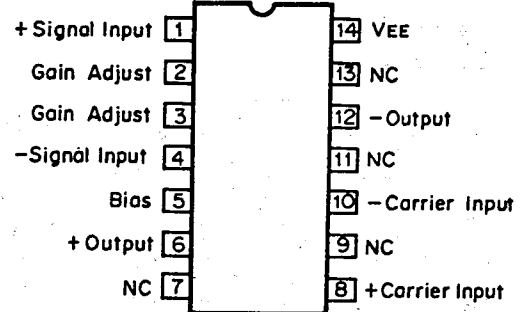
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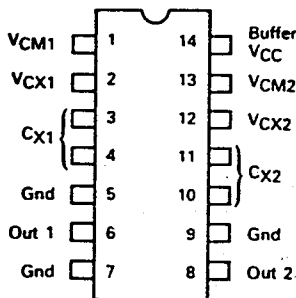
MC1495L
MC1495FR



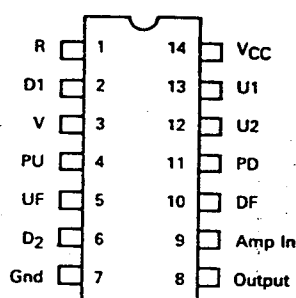
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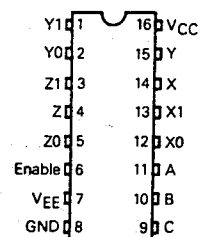
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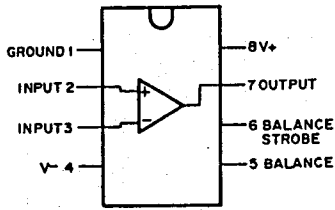
MC4044P



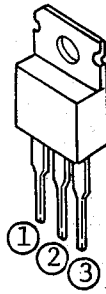
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TC4053BP
TC74HC 4053AF



LM311N
LM311PS

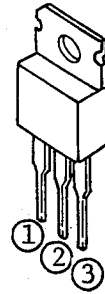


LM317T



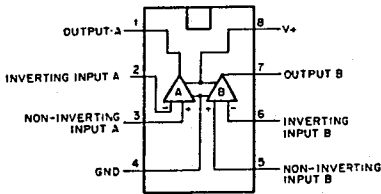
- ① ADJUSTMENT
- ② OUTPUT
- ③ INPUT

LM337T

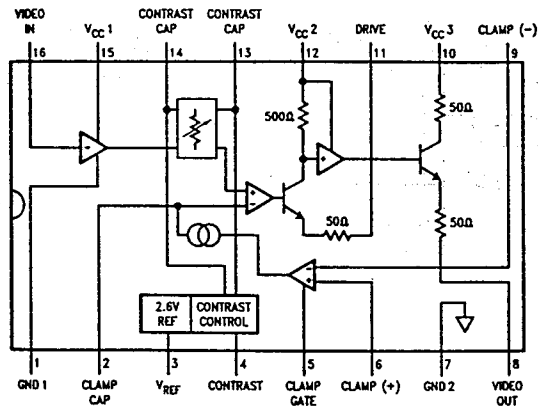


- ① ADJUSTMENT
- ② INPUT
- ③ OUTPUT

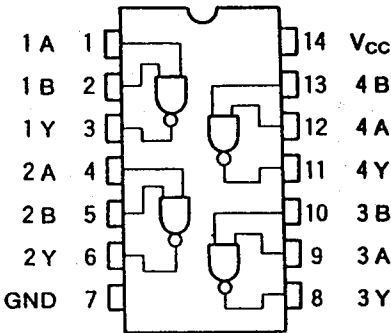
LM393N
μPC393G2



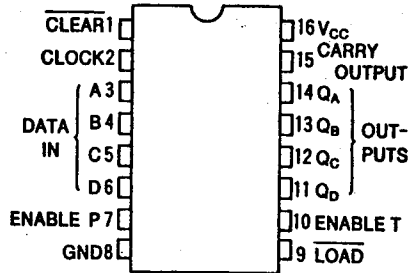
LM1201N



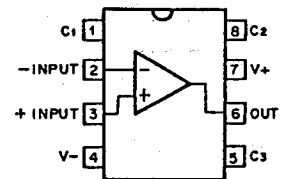
74AC00SJ
TC74HC00AF



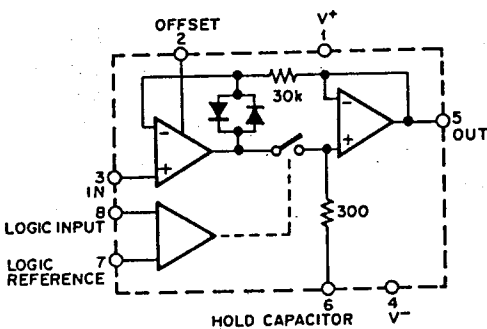
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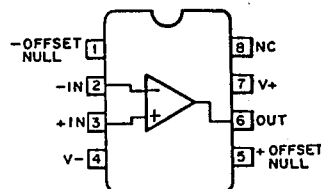
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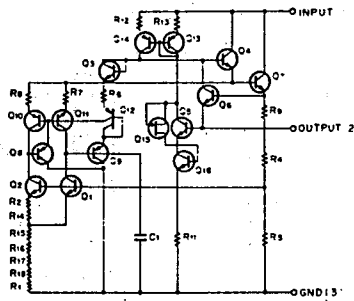
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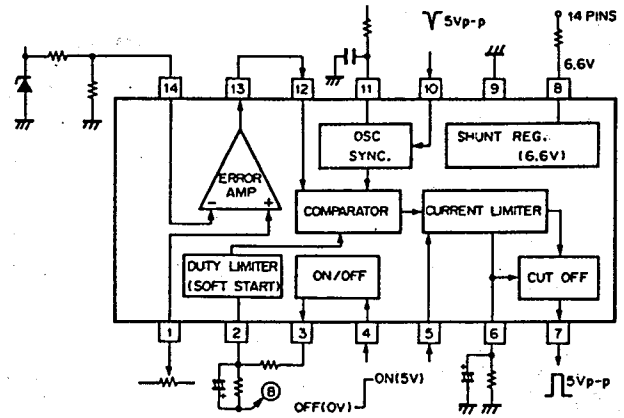
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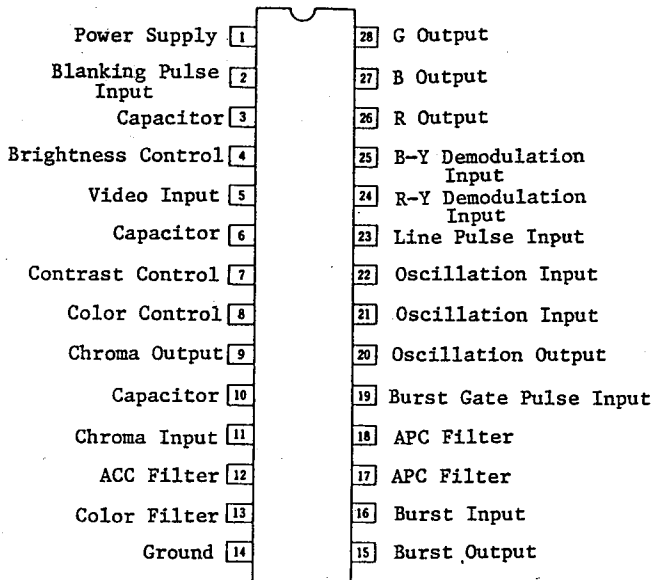
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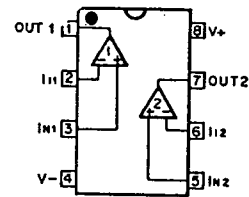
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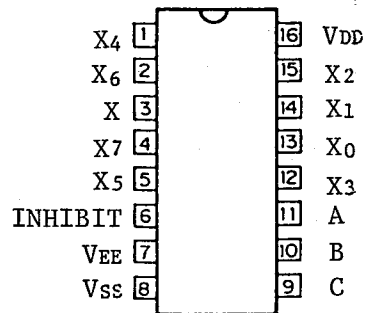
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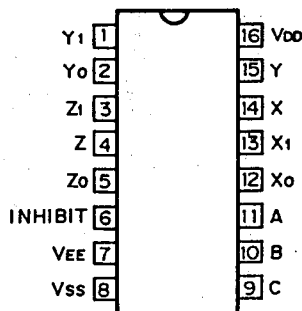
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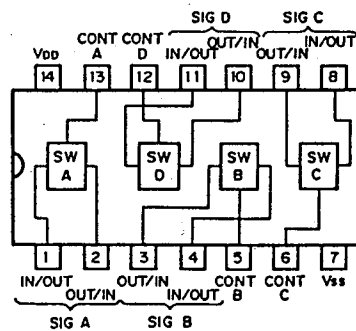
μPD4051BG



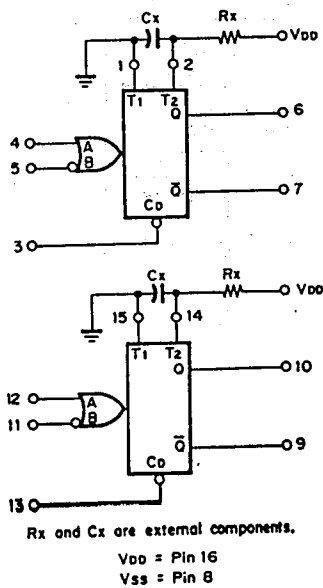
μPD4053BG



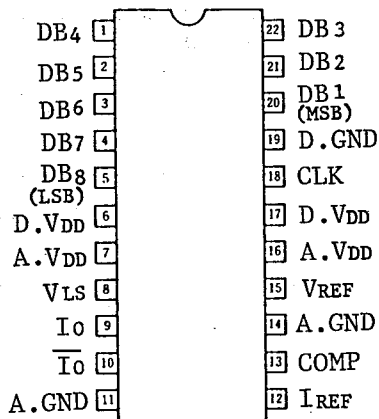
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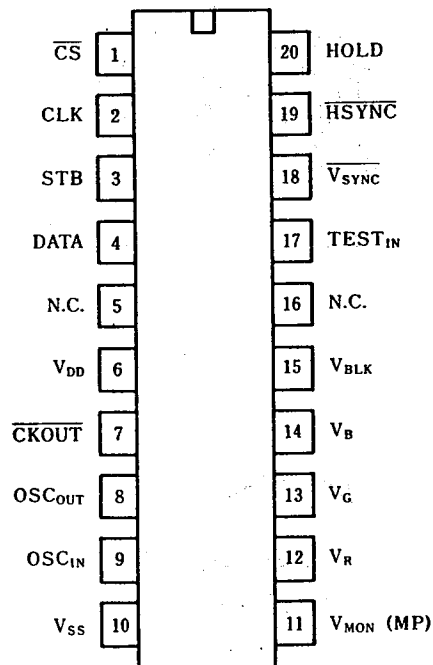
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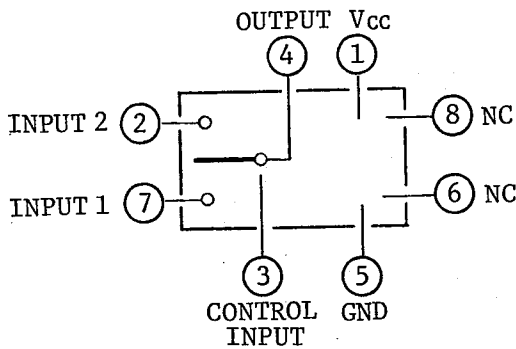
μPD6902C



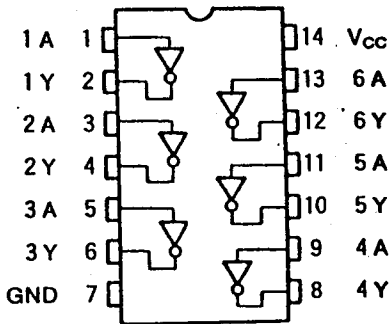
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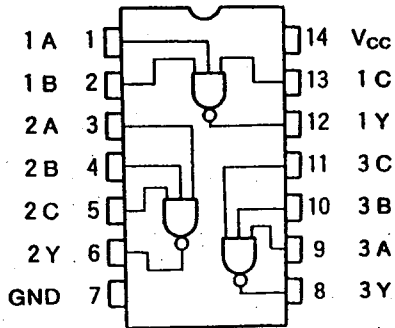
LA7016



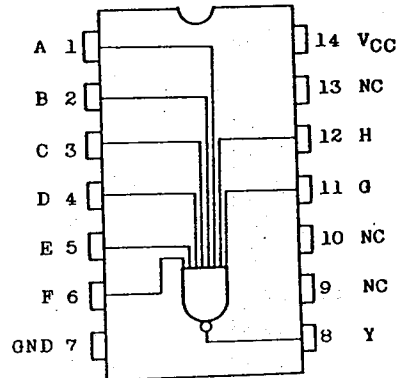
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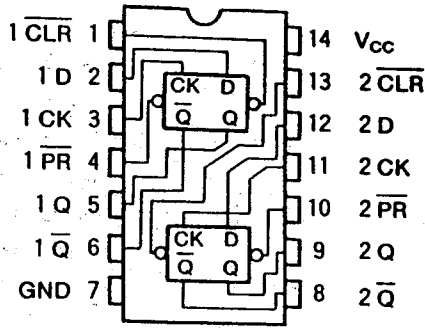
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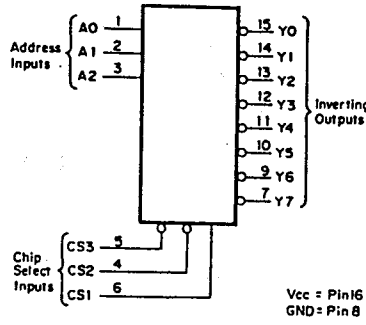
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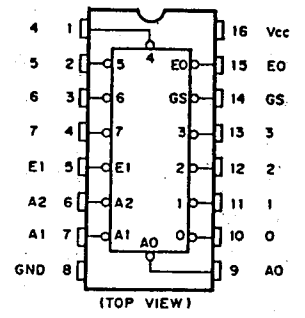
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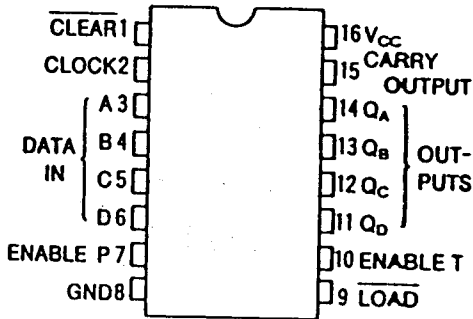
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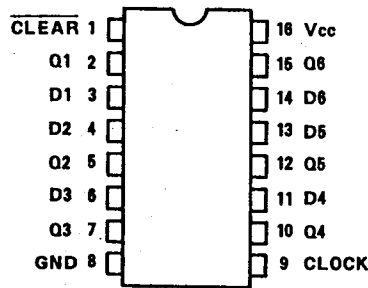
TC74HC148AP



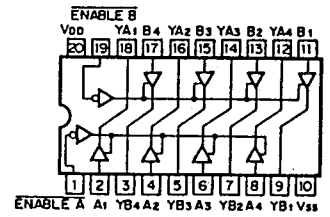
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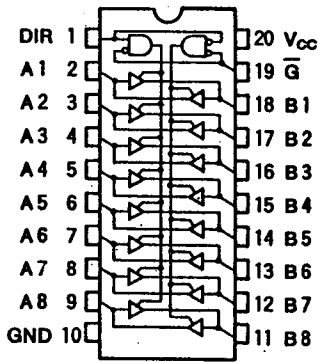
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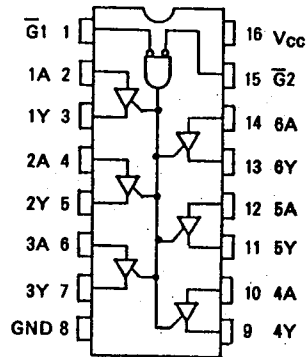
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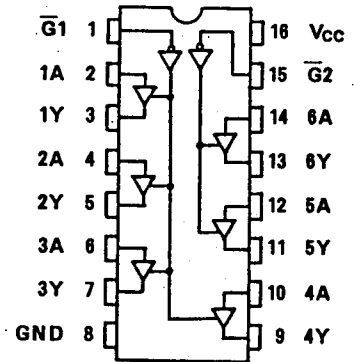
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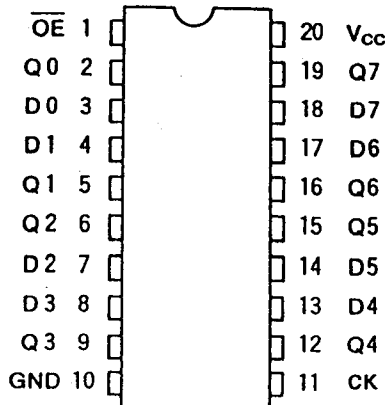
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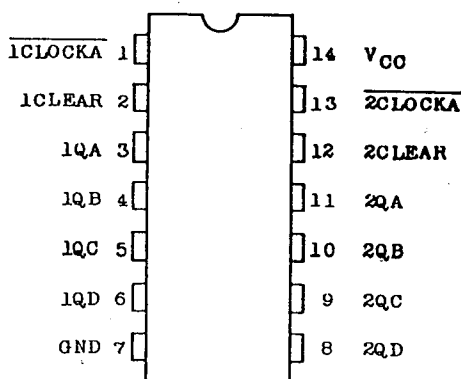
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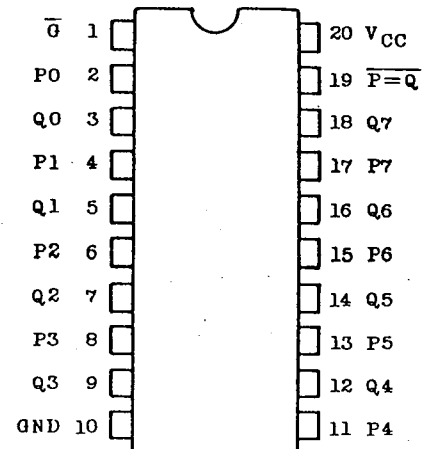
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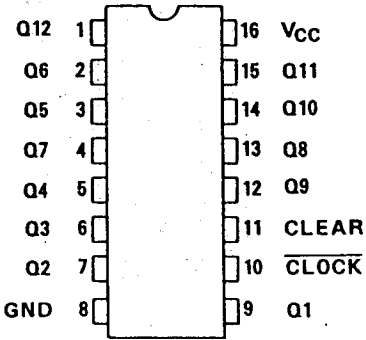
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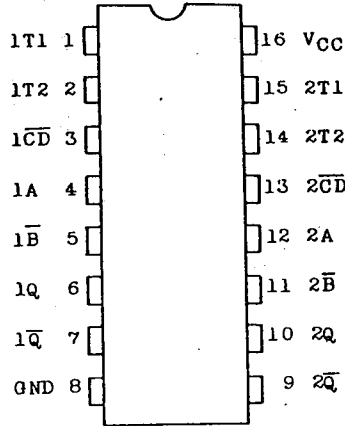
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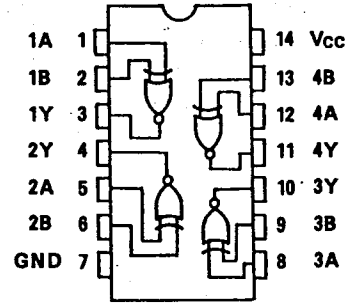
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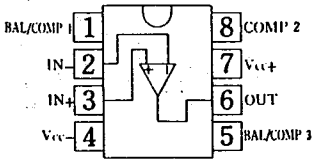
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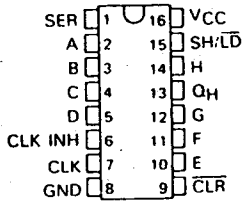
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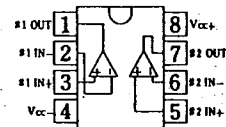
LM318PS



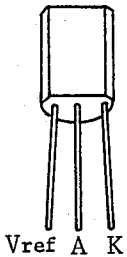
SN74HC166NS



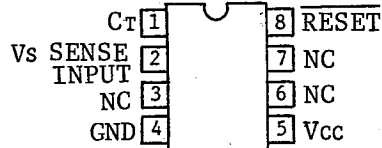
TL062CPS
TL082CPS



TL431CLPB



TL7700CPS



6-2. ELECTRIC PARTS LIST

(1) Guide for Reading the Parts List

The parts list for this color monitor consists of the following items.

Example :

(1) VIDEO AMP BOARD

(2) PARTS NO. (3) DESCRIPTION (4) MFD.

(5) < INTEGRATED CIRCUITS >

IC101 (CMOS) μ PD4053BC NEC

IC102 (CMOS) μ PD4528BC NEC

- (1) Name of unit
- (2) Part No. shown in schematic diagram
- (3) Type designation of parts
- (4) Company name (refer to the next page.)
- (5) Part name

Note

1. When touching the following parts, pay special attention.
CMOS IC, delay line, X^{tal} oscillator, transformer
2. Parts marked with ※ are for adjustment use.
3. Asterisked parts are parts having important factors against X-ray radiation.
4. All the parts may be subject to change for further improvement.

(2) Manufacture Code

AGT	AUGAT INC.	U.S.A.
ALP	ALPS ELECTRIC CO.,LTD.	Japan
AMP	AMP, Ltd.	Japan
ANA	Analog Devices, Inc.	U.S.A.
ASA	ASAHI ELECTRONICS INC.	Japan
BEC	Beckman Industrial	U.S.A.
COS	TOKYO COSMOS ELECTRIC CO.,LTD.	Japan
CPL	COPAL ELECTRONICS CO.,LTD.	Japan
DDK	DAI-ICHI DENSHI KOGYO K.K.	Japan
DIT	DONG IL TECHNOLOGY LTD.	Korea
ELC	Elco International K.K.	Japan
EMD	EMUDEN MUSEN KOGYO CO.,LTD.	Japan
FDK	Fuji Electrochemical Co.,Ltd.	Japan
FJE	Fuji Electric Co.,Ltd.	Japan
FJS	Fujisoku Electric Co.,Ltd.	Japan
FJT	FUJITSU LIMITED	Japan
FKD	Fukuda S.S	Japan
FKK	Fujimoto Kinzoku Co.,Ltd.	Japan
FOS	FOSTER ELECTRIC CO.,LTD.	Japan
HDK	HOKURIKU ELECTRIC INDUSTRY CO.,LTD.	Japan
HIM	HEINEMANN ELECTRIC COMPANY	U.S.A.
HIR	HIROSE ELECTRIC CO.,LTD.	Japan
HIT	Hitachi, Ltd.	Japan
HOS	Hosiden Electronics Co.,Ltd.	Japan
HRA	HIRAKAWA ELECTRIC WIRE MFG.CO.,LTD.	Japan
HRN	HARUNA DENSHI co.,ltd.	Japan
IKE	Ikegami Tsushinki Co.,Ltd.	Japan
ISI	Ishizuka Electronics Corporation	Japan
IWT	IWATSU SEIMITSU CO.,LTD.	Japan
JAE	JAPAN AVIATION ELECTRONICS IND.LTD	Japan
JFC	JAPAN FINE CHEMICAL CORP.	Japan
KCK	KCK CO.,LTD.	Japan
KDK	KAWASAKI ELECTRIC WIRE CO.,LTD.	Japan
KEL	KEL CORPORATION	Japan
KIN	KINSEKI, LIMITED	Japan
KMY	KAMAYA ELECTRIC CO.,LTD.	Japan
KOA	KOA CORPORATION	Japan
KYC	KYOCERA CORPORATION	Japan
LTL	LITTELFUSE	U.S.A.
MAC	MAC EIGHT Co.,Ltd.	Japan
MAR	MARCON ELECTRONICS CO.,LTD.	Japan
MAT	Matsushita Electric Industrial Co.,Ltd.	Japan
MIZ	MIZUTANI ELECTRIC IND.CO.,LTD.	Japan
MMD	MORIMATSU CO.,LTD.	Japan
MMM	SUMITOMO 3M CO.LTD	Japan
MOT	MOTROLA INC	U.S.A.
MUR	MURATA MFG.CO.,LTD.	Japan
NAT	JAPAN SOLDERLESS TERMINAL MFG. CO.,LTD.	Japan
NBL	NOBLE MUSEN CO.,LTD.	Japan
NCC	MATSUO ELECTRIC CO.,LTD.	Japan
NCH	NIPPON CHEMI-CON CORPORATION	Japan
NEC	NEC Corporation	Japan
NHK	NIHON HODEN KENKYUSHO	Japan
NKA	NIHON KAIHEIKI IND.CO.,LTD.	Japan
NKM	NIKKOHM CO.,LTD.	Japan
NMO	Nihon Molex	Japan
NOB	TEIKOKU TSUSHIN KOGYO CO.,LTD.	Japan
NSC	National Semiconductor Corporation	U.S.A.
OEL	OSHINO ELECTRIC LAMP WORKS,LTD.	Japan
OKA	OKAYA ELECTRIC INDUSTRIES CO.,LTD.	Japan
OMR	OMRON Corporation	Japan
PRM	PRECI-DIP S.A.	Swiss
QQQ	CHUOMUSEN CO.,LTD.	Japan
RYO	Ryosan Company, Limited	Japan
SAT	SATO PARTS CO.,LTD.	Japan
SCH	SCHURTER	Swiss
SCS	SAITO CORD MFG.CO.,LTD.	Japan
SIN	SHINYEI KAISHA	Japan
SKK	Sinetsu Kagaku Kogyo Co.,Ltd.	Japan
SKN	SANKEN ELECTRIC CO.,LTD.	Japan
SKO	Sankosha	Japan

SMK	SMK Corporation	Japan
SON	Sony Corporation.	Japan
SOS	SOSHIN ELECTRIC CO.,LTD.	Japan
SRP	Sharp Corporation	Japan
SSM	SUSUMU CO.,LTD.	Japan
STL	STANLEY ELECTRIC CO.,LTD.	Japan
SUD	SUMIDA ELECTRIC CO.,LTD.	Japan
SWC	SHOWA ELECTRIC WIRE & CABLE CO.,LTD.	Japan
SYO	SANYO ELECTRIC CO.,LTD.	Japan
TAD	TAIKC DENKI CO.,LTD.	Japan
TAJ	TAJIMI ELECTRONICS CO.,LTD.	Japan
TAM	TAMA ELECTRIC Co.,Ltd.	Japan
TDK	TDK Corporation	Japan
TEL	TODAI ELECTRIC LTD.	Japan
TEX	TEXAS INSTRUMENTS	U.S.A.
TKO	TOKO, INC.	Japan
TND	TANAKA ELECTRONICS IND.CO.,LTD.	Japan
TOK	TOKAI COMMUNICATION INDUSTRY CO.,LTD.	Japan
TOS	TOSHIBA CORPORATION.	Japan
TYO	TAIYO TSUSHIN KOGYO K.K.	Japan
YTD	YAMATE ELECTRIC CO.,LTD.	Japan

(3) Parts List

.....
 • 20/30SERIES •
 • MAIN CHASSIS •

.....
 • 20/30SERIES •
 • MOTHER BOARD •

No.	DESCRIPTION	MFD. PARTS - CODE	No.	DESCRIPTION	MFD. PARTS - CODE
<CRT>			<INTEGRATED CIRCUITS>		
V901Δ★	~ TM14-20RH/RP ~ M34JFS01X/C18 (USA) M34JFS01X/H18 (JPN) M34JFS01X/E18 (EUR)	MAT 72110-01810 MAT 72110-01710 MAT 72110-01910	IC1 LM317T IC2 LM337T IC3 LM317T	NSC 01332-00301 NSC 01332-00650 NSC 01332-00301	
	~ TM20-20RH/RP ~ M48JBY02X/C28 (USA) M48JBY02X/H28 (JPN) M48JBY02X/E28 (EUR)	MAT 72110-02110 MAT 72110-02010 MAT 72110-02240	<DIODES>		
	~ TM20-30RH/RP ~ M48KTU102X/C10 (USA) M48KTU102X/H10 (JPN) M48KTU102X/E10 (EUR)	MAT 72110-03320 MAT 72110-03300 MAT 72110-03310	D1 V06C D2 V06C D3 V06C D4 V06C D5 V06C D6 V06C	HIT 03631-00200 HIT 03631-00200 HIT 03631-00200 HIT 03631-00200 HIT 03631-00200 HIT 03631-00200	
<TRANSFORMER>			<VARIABLE RESISTORS>		
T901	ST-901692 (only 20SERIES)	IKE 57995-16920	VR1 GF06UT2 500 Ω VR2 GF06UT2 500 Ω VR3 GF06UT2 500 Ω	COS 15194-50111 COS 15194-50111 COS 15194-50111	
T903	ST-902354 (30SERIES) (only 30SERIES)	IKE 57995-23540	<RESISTORS>		
<COILS>			R1 RN26C2E 240 Ω F R2 RN26C2E 1800 Ω F R3 RN26C2E 750 Ω F R4 RN26C2E 120 Ω F R5 RN26C2E 240 Ω F R6 RN26C2E 470 Ω F	KOA 10355-24111 KOA 10355-18211 KOA 10355-75111 KOA 10355-12111 KOA 10355-24111 KOA 10355-47111	
L902	ST-901928 (14") ST-901929 (20")	IKE 40985-19280 IKE 40985-19290	<CAPACITORS>		
L903	ST-901405 (14") ST-901402B (20")	IKE 40985-14050 IKE 40985-14022	C1 ECQ-V1H 104JZ2 C2 ECEA 1JU 100B C3 ECEA 1EU 221B C4 ECQ-V1H 104JZ2 C5 ECEA 1JU 100B C6 ECEA 1EU 221B C7 ECQ-V1H 104JZ2 C8 ECEA 1JU 100B C9 ECEA 1EU 221B	MAT 22137-10450 MAT 20123-10663 MAT 20123-22725 MAT 22137-10450 MAT 20123-10663 MAT 20123-22725 MAT 22137-10450 MAT 20123-10663 MAT 20123-22725	
<SWITCH>			<CONNECTORS>		
S901Δ	EST-15 702V	MAT 36007-00300	CN208 H1F3BA-34PA-2.54DSA CN209 H1F3F-40PA-2.54DS CN210 H1F3FC-20PA-2.54DSA CN211 H1F3F-26PA-2.54DS CN212 DFI-7P-2.5DSA	HIR 30164-05701 HIR 30164-07260 HIR 30079-00700	
<CONNECTORS>			CN802 H1F3F-30PA-2.54DS CN803 DFI-5P-2.5DSA	HIR 30079-00500	
CN901Δ	350779-1	AMP 30501-01200	J1 PCN10-50S-2.54DSA J2 PCN10-50S-2.54DSA J3 PCN10-50S-2.54DSA J4 PCN10-50S-2.54DSA J5 PCN10-50S-2.54DSA J6 PCN10-50S-2.54DSA J7 PCN10-50S-2.54DSA J8 PCN10-50S-2.54DSA J9 PCN10-50S-2.54DSA	HIR 30079-00250 HIR 30079-00290 AMP 30501-00950 AMP 30501-01000 ELC 30508-00900 ELC 30508-00900 OKA 43548-00400 AMP 30501-01200 AMP 30501-01400 NMO 30561-00200 NMO 30562-00200 TAD 30423-00380	
CN902	DFI-2S-2.5R24 DFI-2A1.33	HIR 30079-00250 HIR 30079-00290	<TEST POINTS>		
CN920Δ	350766-1	AMP 30501-00950	TP1 TBP-6 TP2 TBP-6 TP3 TBP-6 TP4 TBP-6	SJD 51572-00100 SJD 51572-00100 SJD 51572-00100 SJD 51572-00100	
CN921Δ	350777-1	AMP 30501-01000	<OTHERS>		
CN922	60-9021-3024-10-000 (only 20")	ELC 30508-00900	TC-80A(TO-220) (For IC1,2,3)	SKK 59001-01051	
CN923	60-9021-3024-10-000 (only 20")	ELC 30508-00900	<CABLE ASS'IES>		
CN925Δ	SUP-D3G-E	OKA 43548-00400	CA901	ST-902380 (CONNECTOR-MOTHER)	IKE 66995-23800
CN926Δ	350779-1	AMP 30501-01200	CA902	ST-902394 (14") ST-902382 (20") (MOTHER-CONTROL)	IKE 66995-23940 IKE 66995-23820
CN927	350780-1	AMP 30501-01400	CA903	ST-902383 (MOTHER-RGB OUT)	IKE 66995-23830
CN928	1951R (only 20SERIES) 1381-TL (only 20SERIES)	NMO 30561-00200 NMO 30562-00200	CA904	ST-902384 (MOTHER-DEF)	IKE 66995-23840
CN931	TS-80H-04-A1 (only 20SERIES)	TAD 30423-00380	CA905	ST-902386 (MOTHER-POWER)	IKE 66995-23860
<CABLE ASS'IES>			CA906	ST-902388 (POWER-DEF)	IKE 66995-23880
CA901	ST-902380 (CONNECTOR-MOTHER)	IKE 66995-23800	CA913	ST-902389 (DEF-FRONT LEFT)	IKE 66995-23890
CA902	ST-902394 (14") ST-902382 (20") (MOTHER-CONTROL)	IKE 66995-23940 IKE 66995-23820	<FUSE>		
CA903	ST-902383 (MOTHER-RGB OUT)	IKE 66995-23830	F901Δ	EAK4A (EUR) ASG3-4 (JPN,USA)	LTL 53002-00500 FKD 53008-00400
CA904	ST-902384 (MOTHER-DEF)	IKE 66995-23840	<FUSE HOLDER>		
CA905	ST-902386 (MOTHER-POWER)	IKE 66995-23860	F901Δ	FEU031-1673	SCH 53505-00100
CA906	ST-902388 (POWER-DEF)	IKE 66995-23880	<FUSE CAP>		
CA913	ST-902389 (DEF-FRONT LEFT)	IKE 66995-23890	F901Δ	FEK031-1663 (EUR) FEK031-1661 (JPN,USA)	SCH 53504-00200 SCH 53504-00100
<FUSE>			<FASTEN RECEPTACLES>		
F901Δ	EAK4A (EUR) ASG3-4 (JPN,USA)	LTL 53002-00500 FKD 53008-00400	TB907	170043-2	AMP 30560-00030
<FUSE HOLDER>			TB908	170046-2	AMP 30560-00060
F901Δ	FEU031-1673	SCH 53505-00100	<CABLE CLAMP>		
<FUSE CAP>			NO.3484-1000 (x 5)	MMM	
F901Δ	FEK031-1663 (EUR) FEK031-1661 (JPN,USA)	SCH 53504-00200 SCH 53504-00100			

.....
• 20/30SERIES •
• MPU BOARD •
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No.	DESCRIPTION	MFD. PARTS - CODE	No.	DESCRIPTION	MFD. PARTS - CODE
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<CONNECTORS>

EL101	00-8261-0633-11-852	ELC 30507-00260			
	00-8261-0248-00-870	ELC 30507-00100			

<TEST POINTS>

TP201	HK-2-G	MAC 39510-00200			
TP202	HK-2-G	MAC 39510-00200			
TP203	HK-2-G	MAC 39510-00200			
TP204	HK-2-G	MAC 39510-00200			
TP205	HK-2-G	MAC 39510-00200			
TP206	HK-2-G	MAC 39510-00200			

<OTHER>

BT101	BR2330-1HF	MAT 57001-00650			
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 * 20/30SERIES *
 * PRE DECO(N) BOARD *

 * 20/30SERIES *
 * PRE SUB BOARD *

No. DESCRIPTION MFD. PARTS - CODE

No. DESCRIPTION MFD. PARTS - CODE

<CAPACITORS>

C52 ECEA 1EU 470B MAT 20123-47625
 C53 ECQ-V1H 104 JZ2 MAT 22137-10450
 C54 ECEA 1EU 470B MAT 20123-47625
 C55 ECQ-B1H 103 JZ4 MAT 22136-10350
 C56 DM 05C 680 J3 SOS 23097-68050
 C57 ECQ-B1H 103 JZ4 MAT 22136-10350
 C58 ECQ-B1H 103 JZ4 MAT 22136-10350
 C59 ECEA 1JU 100B MAT 20123-10663

<COILS>

L1 ST-9654 IKE 40995-96540
 L2 ST-9624 IKE 40995-96240
 L3 LF7.5-560K KOA 40337-56000
 L4 LF7.5-270K KOA 40337-27000
 L5 *
 L6 LF7.5-390K KOA 40337-39000
 L7 LF7.5-4R7K KOA 40337-04700
 L8 P-680 SUD 40451-68000
 L9 LF7.5-100K KOA 40337-10000
 L10 ST-9624 IKE 40995-96240

<DELAY LINES>

DL1 CN100 SWC 44062-00100
 DL2 ST-901432 SWC 44995-14320
 DL3 ZDL-850 SWC 44754-00400

<CONNECTORS>

J6 PCN10A-50P-2.54DS HIR 30333-09500
 CN1 14120-01-445 SCT 54003-00100

<TEST POINTS>

TP1 TBPS IKE
 TP2 TBPS IKE
 TP3 TBPS IKE
 TP4 TBPS IKE
 TP5 TBPS IKE
 TP6 TBPS IKE

<TRANSISTORS>

Tr36 2SC1623-L6.7 NEC 05824-00100
 Tr37 2SC1623-L6.7 NEC 05824-00100
 Tr38 2SC1623-L6.7 NEC 05824-00100
 Tr39 2SC1623-L6.7 NEC 05824-00100

<RESISTORS>

R117 RR1220P 122D SSM 16511-12281
 R118 RR1220P 222D SSM 16511-22281
 R119 RR1220P 332D SSM 16511-33281
 R120 RR1220P 153D SSM 16511-15381
 R121 RR1220P 222D SSM 16511-22281
 R122 RR1220P 222D SSM 16511-22281
 R123 RR1220P 222D SSM 16511-22281
 R124 RR1220P 102D SSM 16511-10281
 R125 RR1220P 222D SSM 16511-22281
 R128 RR1220P 222D SSM 16511-22281

<CAPACITORS>

C60 * CM21CH 220J 25VA T J1 KYC 26061-22025
 C61 GRM40B 104M 25M6305-R MUR 26183-10425

<CONNECTOR>

CN1 14120-04-451 SCT 54003-00200

<TEST POINTS>

TP7 HK-2-G MAC 39510-00200
 TP8 HK-2-G MAC 39510-00200

 * 20/30SERIES *
 * DECODER(N) BOARD *

No. DESCRIPTION MFD. PARTS - CODE No. DESCRIPTION MFD. PARTS - CODE

<RESISTORS>

R168	RN26C 2C 100Ω F T	KOA	10357-10181
R169	RN26C 2C 4700Ω F T	KOA	10357-47281
R170	RN26C 2C 47KΩ F T	KOA	10357-47381
R171	RN26C 2C 33KΩ F T	KOA	10357-33381
R172	RN26C 2C 150KΩ F T	KOA	10357-15481
R173	RN26C 2C 10KΩ F T	KOA	10357-10381
R174	RN26C 2C 100KΩ F T	KOA	10357-10481
R175	RN26C 2C 1000Ω F T	KOA	10357-10281
R176	RN26C 2C 22KΩ F T	KOA	10357-22381
R177	RN26C 2C 1000Ω F T	KOA	10357-10281
R178	RN26C 2C 100KΩ F T	KOA	10357-10481

<TEST POINTS>

TP1	TBPS	IKE
TP2	TBPS	IKE
TP3	TBPS	IKE
TP4	TBPS	IKE
TP5	TBPS	IKE
TP6	TBPS	IKE
TP7	TBPS	IKE
TP8	TBPS	IKE
TP9	TBPS	IKE
TP10	TBPS	IKE
TP11	TBPS	IKE
TP12	TBPS	IKE

<CAPACITORS>

C1	ECEA 1EU 330 B	MAT	20123-33625
C2	ECQ-V1H 104 J22	MAT	22137-10450
C3	ECQ-B1H 333 J24	MAT	22136-33350
C4	* ECEA 1EU 470 B	MAT	20123-47625
C5	ECEA 1EU 330 B	MAT	20123-33625
C6	ECEA 1EU 330 B	MAT	20123-33625
C7	ECEA 1EU 470 B	MAT	20123-47625
C8	* DM05C 330 J3	SOS	23097-33050
C9	* ECEA 1EU 330 B	MAT	20123-33625
C10	ECEA 1EU 330 B	MAT	20123-33625
C11	ECQ-V1H 104 J22	MAT	22137-10450
C12	ECQ-V1H 104 J22	MAT	22137-10450
C13	ECQ-V1H 104 J22	MAT	22137-10450
C14	ECEA 1EU 470 B	MAT	20123-47625
C15	ECQ-V1H 104 J22	MAT	22137-10450
C16	ECEA 1EU 470 B	MAT	20123-47625
C17	ECEA 1JU 100 B	MAT	20123-10663
C18	ECQ-V1H 104 J22	MAT	22137-10450
C19	ECQ-B1H 103 J24	MAT	22136-10350
C20	ECEA 1EU 470 B	MAT	20123-47625
C21	RT-HE40TKSL 220K	KCK	24518-22050
C22	ECEA 1EU 470 B	MAT	20123-47625
C23	NP 2D 121 JT	TYO	22393-12177
C24	ECQ-V1H 473 J22	MAT	22137-47350
C26	ECQ-V1H 104 J22	MAT	22137-10450
C27	ECQ-V1H 104 J22	MAT	22137-10450
C28	ECEA 1JU 100 B	MAT	20123-10663
C29	ECQ-B1H 102 J24	MAT	22136-10250
C30	ECQ-V1H 104 J22	MAT	22137-10450
C31	ECEA 1EU 470 B	MAT	20123-47625
C32	ECQ-V1H 104 J22	MAT	22137-10450
C33	ECEA 1EU 470 B	MAT	20123-47625
C34	ECQ-B1H 103 J24	MAT	22136-10350
C35	ECEA 1EU 330 B	MAT	20123-33625
C36	DM12C 561 J3	SOS	23093-56180
C37	DM05C 101 J3	SOS	23097-10150
C38	DM19C 222 J3	SOS	23095-22280
C39	NP 2D 121 JT	TYO	22393-12177
C40	ECEA 1JU 100 B	MAT	20123-10663
C41	ECQ-B1H 103 J24	MAT	22136-10350
C42	DM05C 360 J3	SOS	23097-36050
C43	DM05C 470 J3	SOS	23097-47050
C44	DM05C 360 J3	SOS	23097-36050
C45	ECQ-B1H 103 J24	MAT	22136-10350
C46	ECEA 1EU 330 B	MAT	20123-33625
C47	RT-HE40TKSL 220K	KCK	24518-22050
C48	NP 2D 121 JT	TYO	22393-12177
C50	ECEA 1EU 470 B	MAT	20123-47625
C52	ECEA 1EU 470 B	MAT	20123-47625
C53	ECQ-V1H 104 J22	MAT	22137-10450
C54	ECQ-V1H 104 J22	MAT	22137-10450
C55	ECEA 1JU 100 B	MAT	20123-10663
C56	ECQ-B1H 102 J24	MAT	22136-10250
C57	ECQ-V1H 104 J22	MAT	22137-10450
C58	ECEA 1HN 010 5B	MAT	20123-10550
C59	ECEA 1EU 470 B	MAT	20123-47625
C60	ECQ-V1H 104 J22	MAT	22137-10450
C61	ECEA 1EU 470 B	MAT	20123-47625
C62	ECQ-V1H 104 J22	MAT	22137-10450
C63	ECEA 1EU 470 B	MAT	20123-47625
C64	ECEA 1EU 470 B	MAT	20123-47625
C65	DHR 1V 225 M1S	NEC	21093-22535
C66	ECEA 1JU 100 B	MAT	20123-10663

<COILS>

L1	ST-202246A	IKE	40981-22460
L2	ST-202246A	IKE	40981-22460
L3	LF7.5-101K	KOA	40337-10100
L4	LF7.5-271K	KOA	40337-27100
L5	ST-300674	IKE	40992-06740
L6	ST-901284	IKE	40995-12840
L7	LF7.5-220K	KOA	40337-22000
L8	LF7.5-101K	KOA	40337-10100
L9	LF7.5-271K	KOA	40337-27100

<X'TAL>

X1	3.579545MHz (HC-49U-A)	KIN	45994-15610
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<CONNECTOR>

J7	PCN10A-50P-2.54DS	HIR	30333-09500
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<DELAY LINES>

DL1	ZT50-140S	SWC	44753-00200
DL2	ZDL-858	SWC	44754-00500

.....
 * 20/30SERIES *
 * RGB OUT BOARD *

No.	DESCRIPTION	MFD. PARTS - CODE	No.	DESCRIPTION	MFD. PARTS - CODE
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<COILS>

L101	LF7.5-6R8K	KOA	40337-06800
L201	LF7.5-6R8K	KOA	40337-06800
L301	LF7.5-6R8K	KOA	40337-06800

<CONNECTORS>

CN213	H1F3FC-20PA-2.54DSA	HIR	30164-18200
CN214	DF1-2P-2.5DSA	HIR	30079-00200
CN215	DF1-2P-2.5DSA	HIR	30079-00200
CN216	DF1-2P-2.5DSA	HIR	30079-00200
CN217	DF1-2P-2.5DSA	HIR	30079-00200

<TEST POINTS>

TP101	TBP-S	IKE
TP102	TBP-S	IKE

TP201	TBP-S	IKE
TP202	TBP-S	IKE

TP301	TBP-S	IKE
TP302	TBP-S	IKE

<OTHERS>

TC45BG(T0-220)(For Tr104)	SKK	59001-01002
TC45BG(T0-220)(For Tr105)	SKK	59001-01002
TC45BG(T0-220)(For Tr204)	SKK	59001-01002
TC45BG(T0-220)(For Tr205)	SKK	59001-01002
TC45BG(T0-220)(For Tr304)	SKK	59001-01002
TC45BG(T0-220)(For Tr305)	SKK	59001-01002

SP123K(For104)	MIZ	55522-01230
SP123K(For105)	MIZ	55522-01230
SP123K(For204)	MIZ	55522-01230
SP123K(For205)	MIZ	55522-01230
SP123K(For304)	MIZ	55522-01230
SP123K(For305)	MIZ	55522-01230

 * 20SERIES *
 * CRT SOCKET BOARD *

 * 30SERIES *
 * CRT SOCKET BOARD *

No.	DESCRIPTION	MFD.	PARTS - CODE	No.	DESCRIPTION	MFD.	PARTS - CODE
<VARIABLE RESISTOR>				<VARIABLE RESISTOR>			
VR1	EVM-J6U10KB26	MAT	15121-20500	VR1	EVM-J6U10KB26	MAT	15121-20500
<RESISTORS>				<RESISTORS>			
R1	RD 50S 100J	KOA	12202-10123	R1	ERDS2TJ 221T	MAT	12108-22113
R2	RD 50S 100J	KOA	12202-10123	R2	0 Ω		
R3	RD 50S 100J	KOA	12202-10123	R3	* ERDS2TJ 100T	MAT	12108-10013
R4	RD 50S 100J	KOA	12202-10123	R4	ERDS2TJ 104T	MAT	12108-10413
R5	ERC 12GJ 125	MAT	13004-12523	R5	0 Ω		
R6	*			R6	* ERDS2TJ 100T	MAT	12108-10013
R7	*			R7	ERC12GJ 205	MAT	13004-20523
R8	*			R8	0 Ω		
R9	ERD 50VJ 100	MAT	12105-10023	R9	* ERDS2TJ 100T	MAT	12108-10013
<CAPACITOR>				<CAPACITOR>			
C1	GC01-103YZB	TDK	24181-10300	C1	GC01-103YZB	TDK	24181-10300
<DISCRETE WIRE CONNECTOR A'SSIES>				<COILS>			
CA909	ST-901410	IKE	66995-14100	L1	*		
CA910	ST-901411	IKE	66995-14110	L2	*		
CA911	ST-901412	IKE	66995-14120	L3	*		
CA912	ST-901413	IKE	66995-14130	<DISCRETE WIRE CONNECTOR A'SSIES>			
<CONNECTORS>				CA909	ST-901410	IKE	66995-14100
CN204	350766-1	AMP	30501-00950	CA910	ST-901411	IKE	66995-14110
CN929	1951P 1380-TL	NMO	30561-00100 30562-00100	CA911	ST-901412	IKE	66995-14120
V1a	XB-0865	SMK	54307-00100	CA912	ST-901413	IKE	66995-14130
<SPARK GAPS>				<CONNECTORS>			
SG1	Y08Z-230B	SKO	59003-00301	CN204	350766-1	AMP	30501-00950
SG2	Y08Z-230B	SKO	59003-00301	CN929	PPMH0022 LPF-01T-2.5C	MUR NAT	30245-00050
SG3	Y08Z-230B	SKO	59003-00301	CN930	PPMH0022 LPF-01T-2.5C	MUR NAT	30245-00050
SG4	Y08Z-230B	SKO	59003-00301	CN950	HPS0380-01-110	HOS	54311-00200
<TERMINALS>				<SPARK GAPS>			
TB907	62409-1	AMP	30801-02400	SK1	Y08Z-230B	SKO	59003-00301
TB908	62409-1	AMP	30801-02400	SK2	Y08Z-230B	SKO	59003-00301
<TEST POINTS>				SK3	Y08Z-230B	SKO	59003-00301
TP1	TBPS			SK4	Y08Z-230B	SKO	59003-00301
TP2	TBPS			<TERMINALS>			
TP3	TBPS			TB907	62409-1	AMP	30801-02400
TP4	TBPS			TB908	62409-1	AMP	30801-02400
TP5	TBPS			<TEST POINTS>			
TP6	TBPS			TP1	TBPS	IKE	
TP7	TBPS			TP2	TBPS	IKE	
TP8	TBPS			TP3	TBPS	IKE	
				TP4	TBPS	IKE	
				TP5	TBPS	IKE	
				TP6	TBPS	IKE	
				TP7	TBPS	IKE	
				TP8	TBPS	IKE	

 • 20SERIES •
 • FBT BOARD •

 • 30SERIES •
 • HV UNIT •

No.	DESCRIPTION	MFD. PARTS - CODE	No.	DESCRIPTION	MFD. PARTS - CODE
<TRANSFORMER>			<CONNECTORS>		
T1 Δ ★	ST-901691	IKE 40995-16910	CN903	840582-1	AMP 30803-00580
<COIL>			CN928 Δ	PFMH0022	AMP
L1	ST-901565A	IKE 40995-15651		LFF-01T-2.5C	NAT 30245-00050
<CONNECTORS>			CN931	1-480340-0	AMP 30552-00300
CN903 Δ	840582-1	AMP 30803-00580	CN932 Δ	PFMH0022	AMP
CN931	TS-80P-04-V1	TAD 30423-00300		LFF-01T-2.5C	NAT 30245-00050
			<TRANSFORMERS>		
			T901 Δ ★	ST-9467A	IKE 40995-94671
			<MULTIPLIER>		
			MF901	ST-902355	IKE
			<OTHERS>		
				H3V UR54 α	TDK
				KE1206RTV C-RQ 4%?*	SKK
				NO. 3484-1000	MMM
				170121-4	AMP 30560-00170

.....
 * 20/30SERIES *
 * CONNECTOR BOARD *













No.	DESCRIPTION	MFD. PARTS - CODE	No.	DESCRIPTION	MFD. PARTS - CODE
<hr style="border-top: 1px dashed black;"/>					
<CONNECTORS>					
CN205	H1F3BA-34PA-2.54DS	HIR 30164-05700			
CN801	DF1-11P-2.5DSA	HIR 30079-01100			
CN904	TRC16R10F1	TAJ 30422-00300			
CN906	P-2171A	EMD 31010-00201			
CN907	P-2171A	EMD 31010-00201			
CN908	P-2171A	EMD 31010-00201			
CN909	P-2171A	EMD 31010-00201			
CN910	P-2171A	EMD 31010-00201			
CN911	P-2171A	EMD 31010-00201			
CN912	P-2171A	EMD 31010-00201			
CN913	P-2171A	EMD 31010-00201			
CN914	P-2171A	EMD 31010-00201			
CN915	P-2171A	EMD 31010-00201			
CN916	P-2171A	EMD 31010-00201			
CN917	P-2171A	EMD 31010-00201			
CN918	P-2171A	EMD 31010-00201			
CN919	P-2171A	EMD 31010-00201			

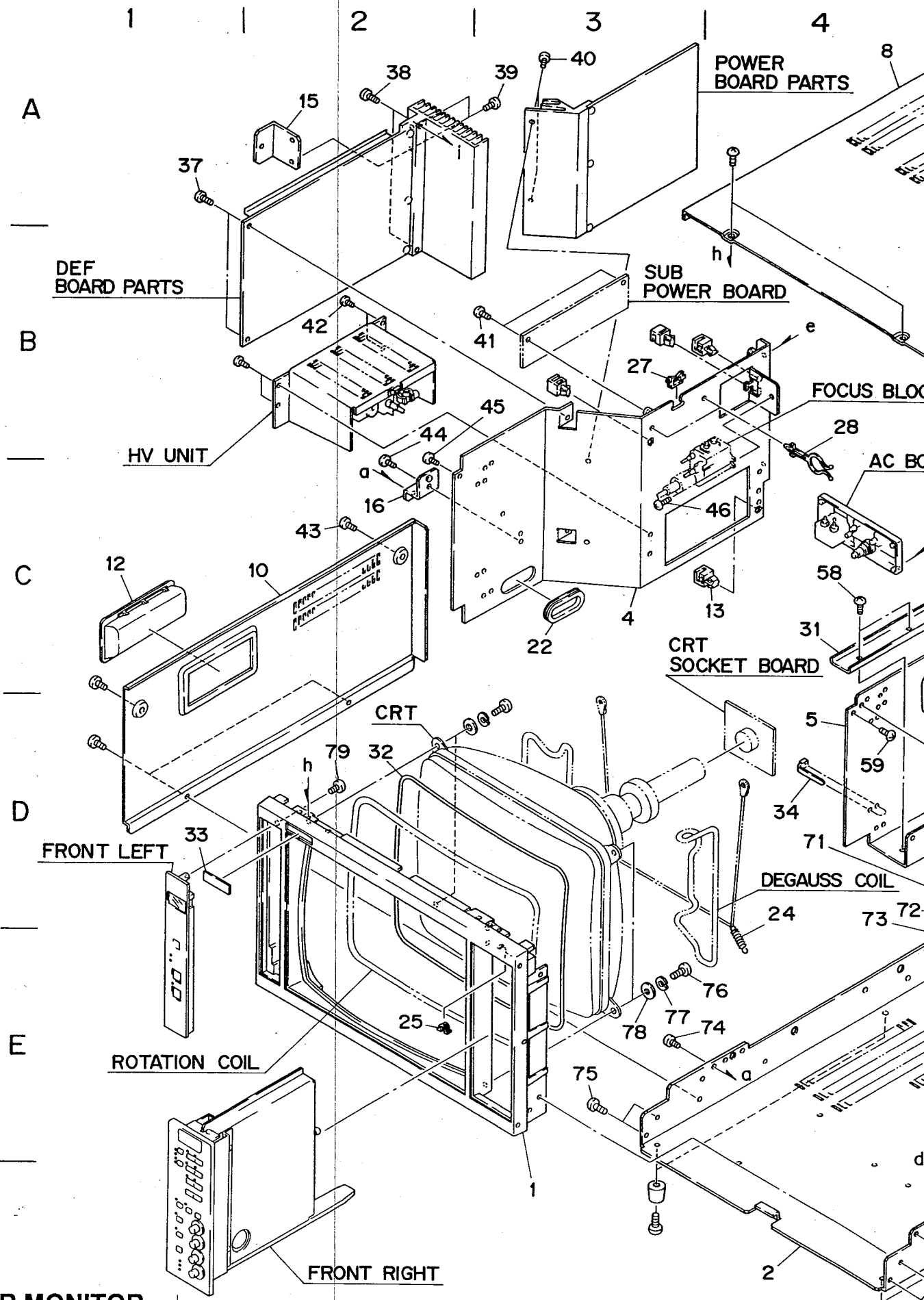
 • 20/30SERIES •
 • FRONT LEFT BOARD •

 • 20/30SERIES •
 • FRONT PANEL BOARD •

No.	DESCRIPTION	MFD. PARTS - CODE	No.	DESCRIPTION	MFD. PARTS - CODE
<INTEGRATED CIRCUIT>			<DIODES>		
IC2	NJM78L05A	JRC 01392-00301	D1	TLG226	TOS 03572-00330
<TRANSISTOR>			D2	TLG226	TOS 03572-00330
Tr1	2SC1815Y-TPE2	TOS 02824-05702	D3	TLG226	TOS 03572-00330
<DIODES>			D4	TLG226	TOS 03572-00330
D1	TLR226	TOS 03575-01000	D5	TLG226	TOS 03572-00330
D2	TLR226	TOS 03575-01000	D6	TLG226	TOS 03572-00330
D3	TLG226	TOS 03572-00330	D7	TLG226	TOS 03572-00330
D4	LT9230D (only 14")	SRP 03336-00130	D8	TLG226	TOS 03572-00330
<RESISTORS>			D9	TLG226	TOS 03572-00330
R1	ERDS1VJ 221T	MAT 12106-22133	D10	TLG226	TOS 03572-00330
R2	ERDS2TJ 152T	MAT 12108-15213	D11	TLG226	TOS 03572-00330
R3	ERDS2TJ 152T	MAT 12108-15213	D12	TLG226	TOS 03572-00330
R4	ERDS2TJ 152T	MAT 12108-15213	D13	TLG226	TOS 03572-00330
R5	ERDS2TJ 471T	MAT 12108-47113	D14	TLG226	TOS 03572-00330
<CAPACITORS>			D15	TLG226	TOS 03572-00330
C1	ECQ-V1H 104JZ2	MAT 22137-10450	D16	TLY226	TOS 03576-01000
C2	ECQ-V1H 104JZ2	MAT 22137-10450	D17	TLY226	TOS 03576-01000
<SWITCHES>			D18	TLY226	TOS 03576-01000
S1	DPL4-200	FJS 36001-03000	D19	TLY226	TOS 03576-01000
	MD8050103	FJS 36001-01302	D20	TLG226	TOS 03572-00330
	MD0340162	FJS 36001-00016	D21	TLG226	TOS 03572-00330
S2	DP4-200	FJS 36001-02900	D22	TLG226	TOS 03572-00330
	MD8050109	FJS 36001-02901	<RESISTOR>		
	MD0340162	FJS 36001-00016	R1	ERDS2TJ 821	MAT
<CONNECTORS>			<SWITCHES>		
CN922	00-9021-0212-00-339 (only 20")	ELC 30508-00190	S9	SKHHAH	ALP 34267-01009
CN935	DF1-10P-2.5DSA	HIR 30079-01000	S10	SKHHAH	ALP 34267-01009
			S11	SKHHAH	ALP 34267-01009
			S12	SKHHAH	ALP 34267-01009
			S17	SKHHAH	ALP 34267-01009
			S18	SKHHAH	ALP 34267-01009
			S19	SKHHAH	ALP 34267-01009
			S20	SKHHAH	ALP 34267-01009
			S25	SKHHAH	ALP 34267-01009
			S26	SKHHAH	ALP 34267-01009
			S27	SKHHAH	ALP 34267-01009
			S28	SKHHAH	ALP 34267-01009
			S33	SKHHAH	ALP 34267-01009
			S34	SKHHAH	ALP 34267-01009
			S35	SKHHAH	ALP 34267-01009
			S36	SKHHAH	ALP 34267-01009
			<CONNECTOR>		
			CN223	H1F3FC-34PA-2.54DSA	HIR 30164-18340

6-3 MECHANICAL PARTS LIST AND EXPLODED VIEW

1	なべ小ねじ NM BNM		PAN HEAD SCREW PAN HEAD SCREW (BLACK)
2	さら小ねじ SM BSM		FLAT HEAD SCREW FLAT HEAD SCREW (BLACK)
3	丸さら小ねじ MM BMM		OVAL COUNTERSUNK HEAD SCREW OVAL COUNTERSUNK HEAD SCREW (BLACK)
4	トラスねじ TM BTM		TRUSS HEAD SCREW TRUSS HEAD SCREW (BLACK)
5	バインドねじ NMB		BINDING HEAD SCREW
8	セムスねじ NMS		PAN HEAD SCREW AND WASHER ASS'Y
9	トラスタッピングねじ TH-TS		TRUSS HEAD TAPPING SCREW
10	バインド タッピング ねじ NMB-TS		BINDING HEAD TAPPING SCREWS
11	ホロセット HM		HEXAGON SOCKET SET SCREW
12	ソケット ヘッド キャップ スクリュー SHM		HEXAGON SOCKET HEAD CAP SCREW
13	なべタッピングねじ NM-TS		PAN HEAD TAPPING SCREWS
14	セムスねじ NMXW		PAN HEAD SCREW AND DOUBLE WASHER ASS'Y



DEF BOARD PARTS

POWER BOARD PARTS

SUB POWER BOARD

FOCUS BLOCK

HV UNIT

AC BOARD

CRT SOCKET BOARD

CRT

FRONT LEFT

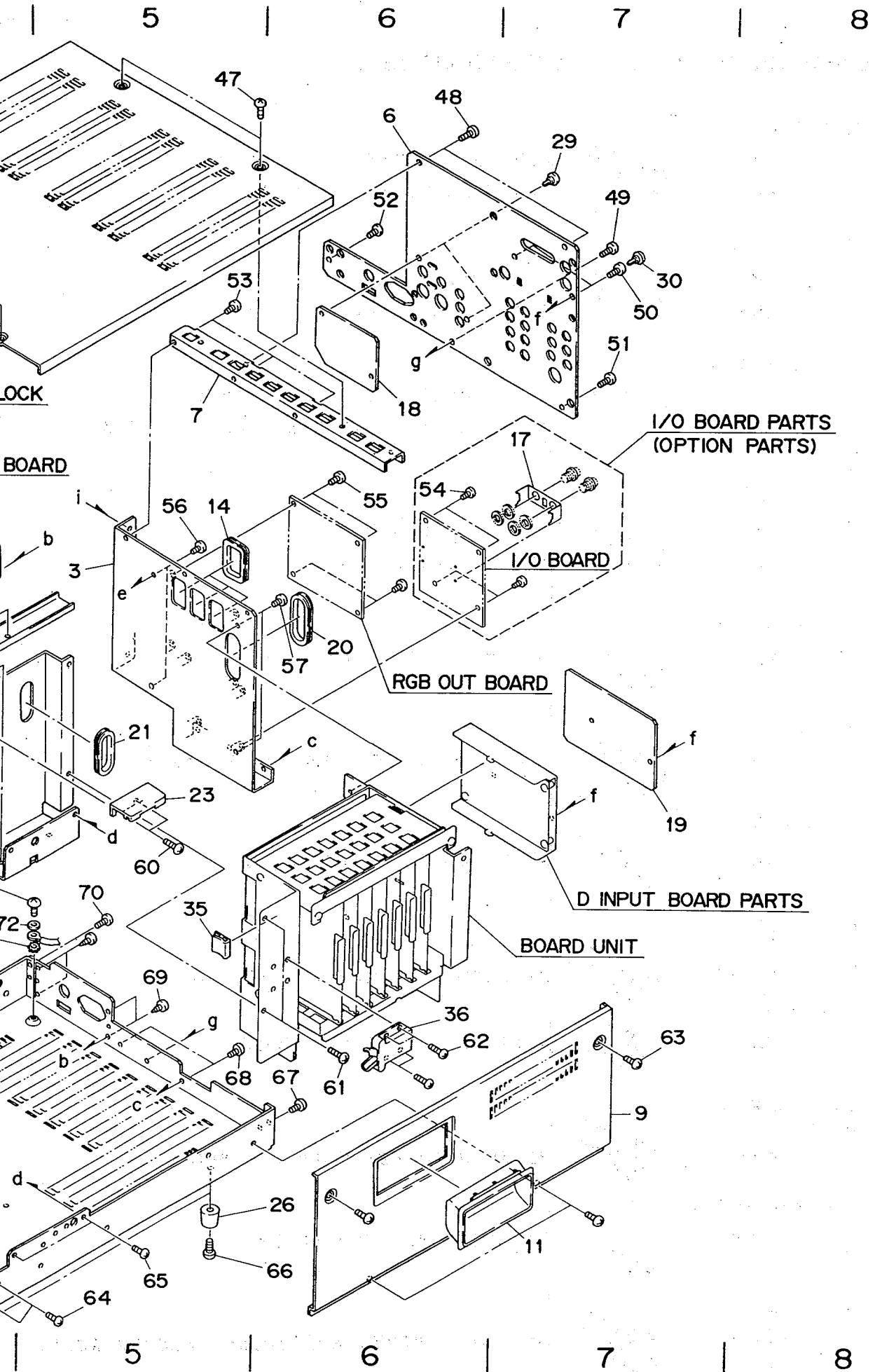
DEGAUSS COIL

ROTATION COIL

FRONT RIGHT

14" COLOR MONITOR BODY

K3-950094



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C

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NO.	INDEX	COMPONENTS	PARTS NO.	Q'ty	NO.	INDEX	COMPONENTS
46	4-C	SCREW	NMB 3-8	1	1	3-F	ESCUTC
47	5-A	SCREW	NMB 4-6	4	2	4-F	CHASSI
48	6-A	SCREW	NMB 3-6	2	3	5-C	REAR C
49	7-A	SCREW	NMB 3-6	1	4	3-C	LEFT F
50	7-B	SCREW	NMB 3-6	2	5	4-D	RIGHT
51	7-B	SCREW	NMB 3-6	1	6	6-A	REAR P
52	6-A	SCREW	NMB 3-6	1	7	5-B	REAR C
53	5-B	SCREW	NMB 3-6	2	8	4-A	TOP CO
54	6-C	SCREW	NMB 3-6	4	9	7-E	SIDE C
55	6-C	SCREW	NMB 3-6	4	10	2-C	SIDE C
56	5-C	SCREW	NMB 3-6	2	11	7-F	HANDLE
57	6-C	SCREW	NMB 3-6	2	12	1-C	HANDLE
58	4-C	SCREW	NMB 4-8	2	13	4-C	PCB HO
59	4-D	SCREW	NMB 4-8	1	14	5-C	BUSHIN
60	5-D	SCREW	NMB 3-6	2	15	2-A	COVER
61	6-E	SCREW	NMB 3-6	2	16	2-C	LEFT M
62	6-E	SCREW	NMB 3-8	4	17	7-B	CONNEC
63	7-E	SCREW	NMB 4-6	4	18	6-B	BLANK
64	5-F	SCREW	NMB 4-8	2	19	7-D	BLANK
65	5-F	SCREW	NMB 3-6	2	20	6-C	BUSHIN
66	5-F	SCREW	NMB 4-14	4	21	5-D	BUSHIN
67	6-E	SCREW	NMB 3-6	1	22	3-C	BUSHIN
68	5-E	SCREW	NMB 3-6	2	23	5-D	GUIDE (
69	5-E	TAPPING SCREW	NMB-TS 3-10	4	24	4-D	EARTH
70	5-D	SCREW	NMB 3-6	1	25	2-E	RAIL G
71	4-D	SCREW	NMB 4-8	1	26	6-F	MONITOR
72	4-D	PLANE WASHER	HW 4	1	27	3-B	EDGING
73	4-D	TOOTHED LOCK WASHER	TW 4	1	28	4-B	CABLE
74	4-E	SCREW	NMB 3-6	1	29	7-A	PUSH R
75	3-E	SCREW	NMB 4-8	2	30	7-B	PUSH R
76	4-E	SCREW	NMB 6-16	4	31	4-C	SLIDES
77	3-E	SPRING LOCK WASHER	SW 6	4	32	2-D	ESCUTC
78	3-E	PLANE WASHER	HW 6	4	33	1-D	NAME PI
79	2-D	SCREW	NMB 3-10	1	34	4-D	CABLE
					35	5-D	CABLE
					36	6-E	TOUCH
					37	1-A	SCREW
					38	2-A	SCREW
					39	3-A	SCREW
					40	3-A	SCREW
					41	3-B	SCREW
					42	2-B	SCREW
					43	2-C	SCREW
					44	2-B	SCREW
					45	3-B	SCREW

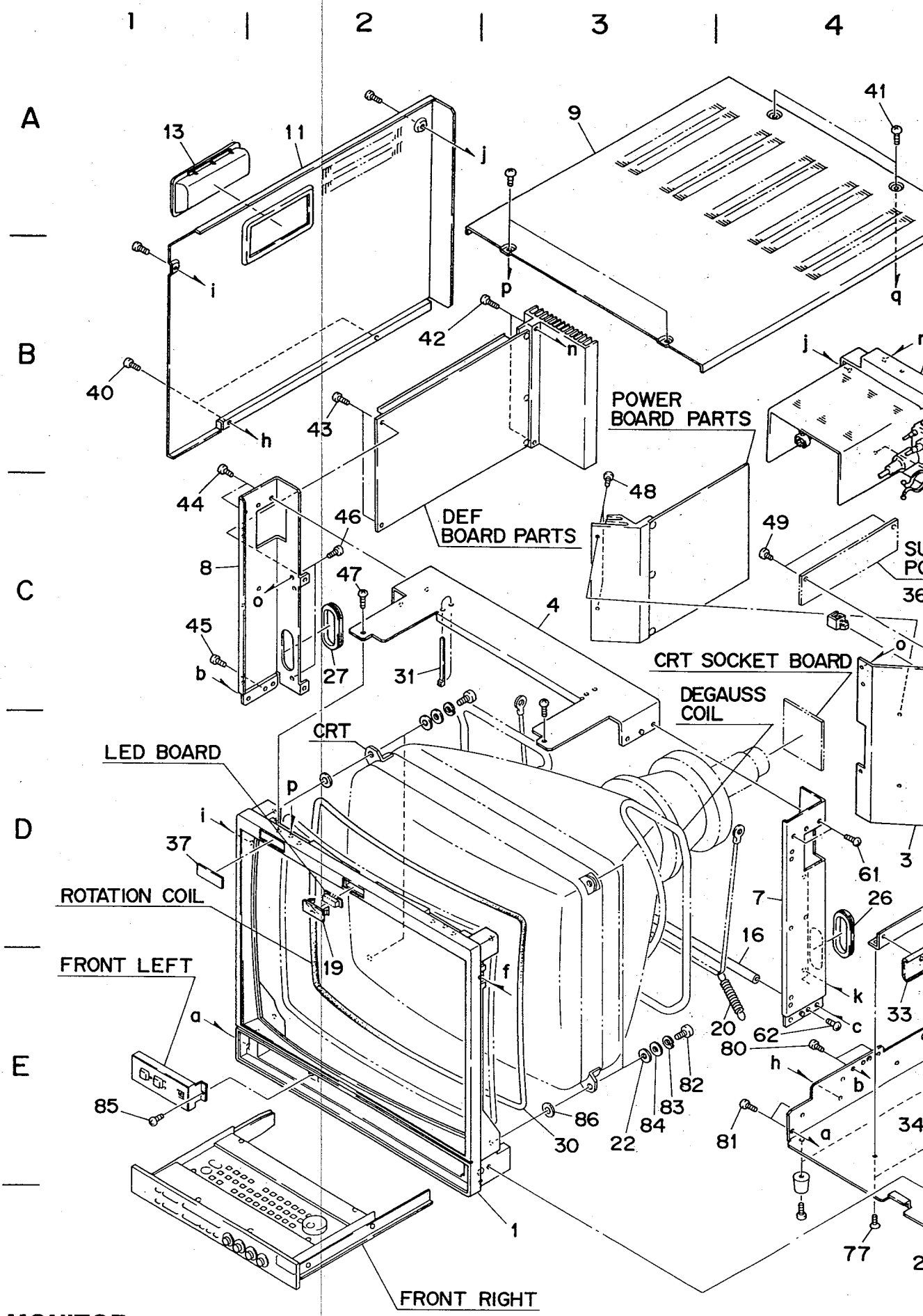
14" COLOR MONITOR
BODY

K3-950094

Q' ty	NO.	INDEX	COMPONENTS	PARTS NO.	Q' ty
1	1	3-F	ESCUTCHEON	M0-950139	1
4	2	4-F	CHASSIS	M0-920392	1
2	3	5-C	REAR CHASSIS (1)	M1-920397	1
1	4	3-C	LEFT FRAME	M1-920596	1
2	5	4-D	RIGHT FRAME (2)	M1-920597	1
1	6	6-A	REAR PANEL	M1-950350	1
1	7	5-B	REAR CHASSIS (2)	M2-950234	1
2	8	4-A	TOP COVER	M2-920401	1
4	9	7-E	SIDE COVER	M2-920549-A	1
4	10	2-C	SIDE COVER	M2-920549-B	1
2	11	7-F	HANDLE	M2-911020	1
2	12	1-C	HANDLE	M2-911020	1
2	13	4-C	PCB HOLDER	M3-908268	4
1	14	5-C	BUSHING BTYPE	M3-916296	3
2	15	2-A	COVER METAL	M4-920599	1
2	16	2-C	LEFT METAL	M4-920600	1
4	17	7-B	CONNECTOR METAL	M4-915728	1
4	18	6-B	BLANK PANEL (2)	M4-950368	1
2	19	7-D	BLANK PANEL (1)	M4-950369	1
2	20	6-C	BUSHING (2)	M4-916295	1
4	21	5-D	BUSHING (2)	M4-916295	1
1	22	3-C	BUSHING (2)	M4-916295	1
2	23	5-D	GUIDE (1)	M4-912830	1
4	24	4-D	EARTH SPRING	M4-279433A	4
1	25	2-E	RAIL GUIDE	M4-914254	1
1	26	6-F	MONITOR FOOT	M4-908267	4
1	27	3-B	EDGING SADDLE	EDS-3	1
1	28	4-B	CABLE CLIP D	1F55	2
1	29	7-A	PUSH RIVET	P3545	2
2	30	7-B	PUSH RIVET	P3545	2
4	31	4-C	SLIDES RAIL (OUTER)	C-203-413	1
4	32	2-D	ESCUTCHEON PACKING	KG-CR5754	1
4	33	1-D	NAME PLATE	D45	1
1	34	4-D	CABLE TIES	SG-100	1
1	35	5-D	CABLE CLAMP	3484-1000	1
1	36	6-E	TOUCH LATCH (CATCHER)	TTL-00562	1
1	37	1-A	SCREW	NMB 3-6	2
1	38	2-A	SCREW	NMB 4-16	2
1	39	3-A	SCREW	NMB 3-6	2
1	40	3-A	SCREW	NMB 4-10	2
1	41	3-B	SCREW	NMB 3-6	2
1	42	2-B	SCREW	NMB 3-6	4
1	43	2-C	SCREW	NMB 4-6	4
1	44	2-B	SCREW	NMB 3-6	1
1	45	3-B	SCREW	NMB 4-8	1

14" COLOR MONITOR
BODY

K3-950094



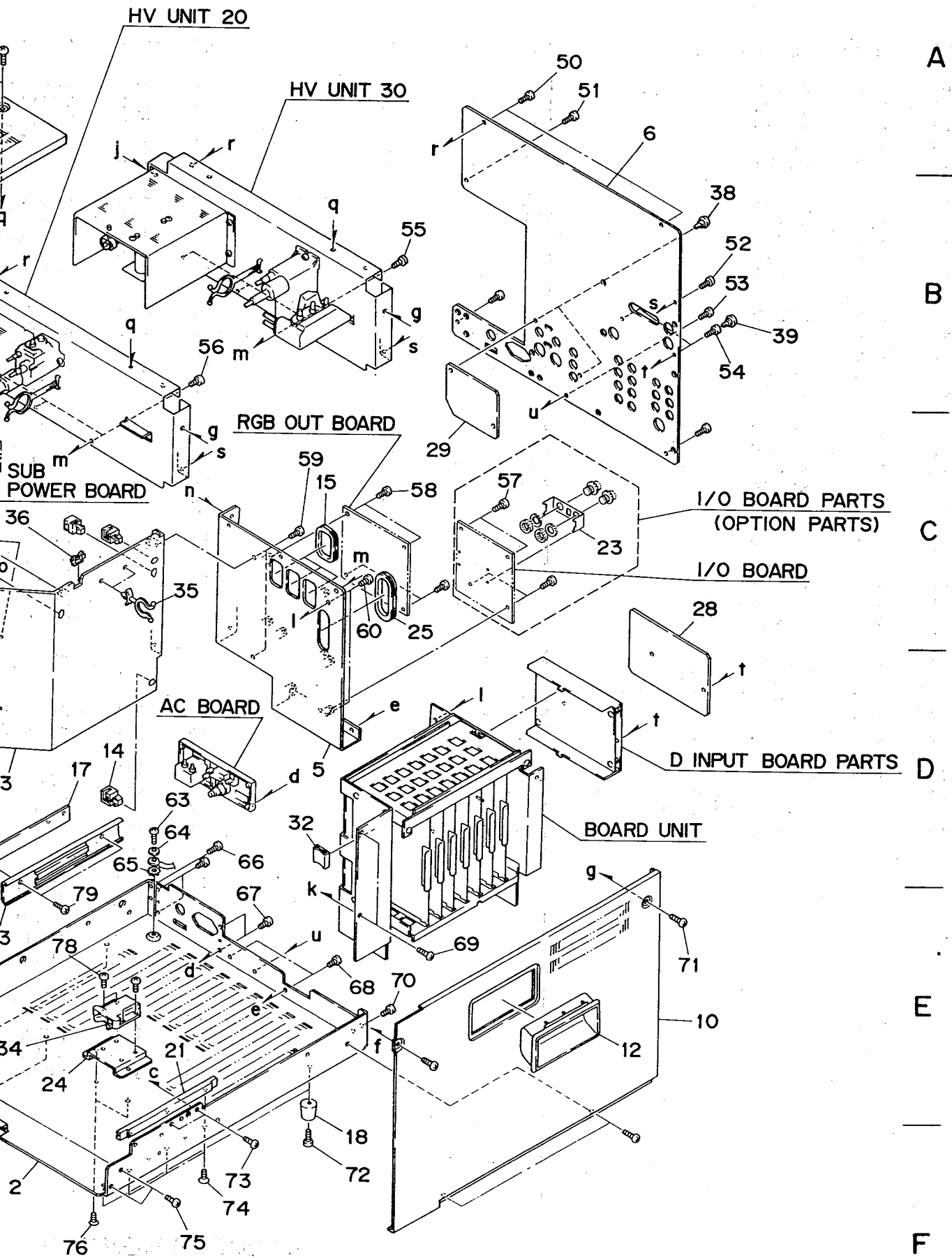
20" COLOR MONITOR
 BODY
 K3-950075

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NO.	INDEX	COMPONENTS	PARTS NO.	Q'ty	NO.	INDEX	COMPONENTS
46	2-C	SCREW	NMB 3-6	2	1	3-F	ESCUTCH
47	2-C	SCREW	SM 4-4	2	2	4-F	CHASSIS
48	3-C	SCREW	NMB 4-10	2	3	4-D	LEFT FR
49	4-C	SCREW	NMB 3-6	2	4	3-C	CENTER
50	7-A	SCREW	NMB 3-6	2	5	6-D	REAR CH
51	7-A	SCREW	NMB 3-6	1	6	7-A	REAR PA
52	7-B	SCREW	NMB 3-6	1	7	4-D	RIGHT F
53	7-B	SCREW	NMB 3-6	3	8	1-C	LEFT FR
54	7-B	SCREW	NMB 3-6	2	9	3-A	TOP COV
55	6-B	SCREW	NMB 3-6	3	10	7-E	SIDE CO
56	5-B	SCREW	NMB 3-6	3	11	2-A	SIDE CO
57	6-C	SCREW	NMB 3-6	4	12	7-E	HANDLE
58	6-C	SCREW	NMB 3-6	4	13	1-A	HANDLE
59	6-C	SCREW	NMB 3-6	2	14	5-D	PCB HOL
60	6-C	SCREW	NMB 3-6	2	15	6-C	BUSHING
61	4-D	SCREW	NMB 3-6	2	16	4-D	SHAFT
62	4-E	SCREW	NMB 3-6	1	17	5-D	LEFT RA
63	5-D	SCREW	NMB 4-8	1	18	6-F	MONITOR
64	5-D	PLANE WASHER	HW 4	1	19	2-E	TALLY
65	5-D	TOOTHED LOCK WASHER	TW 4	1	20	4-E	EARTH S
66	5-D	SCREW	NMB 3-6	1	21	5-E	GUIDE R
67	5-E	TAPPING SCREW	NMB-TS 3-10	4	22	3-E	CRT WAS
68	6-E	SCREW	NMB 3-6	2	23	7-C	CONNECT
69	6-E	SCREW	NMB 3-6	2	24	5-E	TOUCH L
70	6-E	SCREW	NMB 3-6	1	25	6-C	BUSHING
71	7-E	SCREW	NMB 4-6	4	26	4-D	BUSHING
72	6-F	SCREW	NMB 4-14	4	27	2-C	BUSHING
73	5-F	SCREW	NMB 3-6	2	28	7-C	BLANK P
74	5-F	TAPPING SCREW	ST 2.6-6	3	29	6-C	BLANK P
75	5-F	SCREW	NMB 4-8	2	30	3-E	ESCUTCH
76	5-F	SCREW	SM 2.6-6	2	31	2-C	CABLE T
77	4-F	SCREW	SM 2.6-6	2	32	6-D	CABLE C
78	4-E	SCREW	NMB 3-8	4	33	4-E	SLIDES
79	5-E	SCREW	NMB 4-6	2	34	4-E	TOUCH L
80	4-E	SCREW	NMB 3-6	2	35	5-C	CABLE C
81	4-E	SCREW	NMB 4-8	2	36	4-C	EDGING
82	3-E	SCREW	NM 6-16	4	37	1-D	NAME PL
83	3-E	SPRING LOCK WASHERS	SW 6	4	38	7-B	PUSH RI
84	3-E	PLANE WASHERS	HW 6	4	39	8-B	PUSH RI
85	1-E	SCREW	NMB 2-4	1	40	1-B	SCREW
86	3-E	PLANE WASHERS	HW 6 t=0.8	4	41	4-A	SCREW
					42	2-B	SCREW
					43	2-B	SCREW
					44	1-C	SCREW
					45	1-C	SCREW

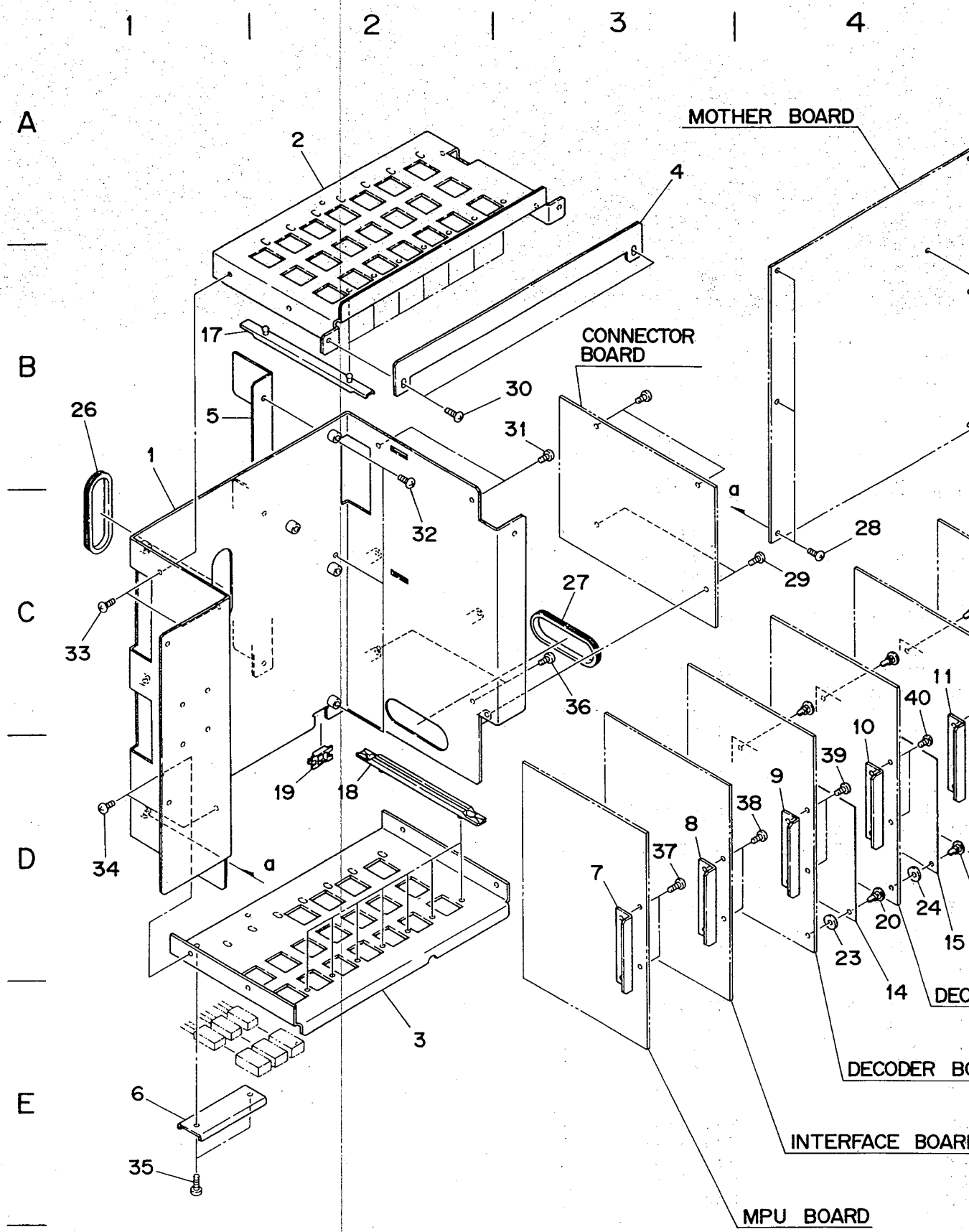
20" COLOR MONITOR
BODY

K3-950075

NO.	INDEX	COMPONENTS	PARTS NO.	Q' ty
1	3-F	ESCUTCHEON	M0-950063	1
2	4-F	CHASSIS	M0-920392	1
3	4-D	LEFT FRAME (2)	M1-950169	1
4	3-C	CENTER FRAME	M1-950211	1
5	6-D	REAR CHASSIS	M1-920397	1
6	7-A	REAR PANEL	M1-950364	1
7	4-D	RIGHT FRAME (1)	M2-920393	1
8	1-C	LEFT FRAME (1)	M2-920394	1
9	3-A	TOP COVER	M2-920401	1
10	7-E	SIDE COVER	M2-920400-A	1
11	2-A	SIDE COVER	M2-920400-B	1
12	7-E	HANDLE	M2-911020	1
13	1-A	HANDLE	M2-911020	1
14	5-D	PCB HOLDER	M3-908268	4
15	6-C	BUSHING BTYPE	M3-916296	3
16	4-D	SHAFT	M4-919638	1
17	5-D	LEFT RAIL METAL	M4-920521	1
18	6-F	MONITOR FOOT	M4-908267	4
19	2-E	TALLY	M4-912378	1
20	4-E	EARTH SPRING	M4-279433A	2
21	5-E	GUIDE RAIL	M4-908515	1
22	3-E	CRT WASHERES	M4-281236A	4
23	7-C	CONNECTOR METAL	M4-915728	1
24	5-E	TOUCH LATCH METAL	M4-920403	1
25	6-C	BUSHING (2)	M4-916295	1
26	4-D	BUSHING (2)	M4-916295	1
27	2-C	BUSHING (2)	M4-916295	1
28	7-C	BLANK PANEL (1)	M4-950369	1
29	6-C	BLANK PANEL (2)	M4-950368	1
30	3-E	ESCUTCHEON PACKING	KG-CR5890	1
31	2-C	CABLE TIES	SG-100	1
32	6-D	CABLE CLAMP	3484-1000	1
33	4-E	SLIDES RAIL (OUTER)	C-203-413	1
34	4-E	TOUCH LATCH (CATCHER)	TTL-00562	1
35	5-C	CABLE CLIP D	1F55	2
36	4-C	EDGING SADDLE	EDS-3	1
37	1-D	NAME PLATE	D45	1
38	7-B	PUSH RIVET	P3545	2
39	8-B	PUSH RIVET	P3545	2
40	1-B	SCREW	NMB 4-6	4
41	4-A	SCREW	NMB 4-6	4
42	2-B	SCREW	NMB 4-16	2
43	2-B	SCREW	NMB 3-6	2
44	1-C	SCREW	NMB 3-6	2
45	1-C	SCREW	NMB 3-6	1

20" COLOR MONITOR
BODY

K3-950075



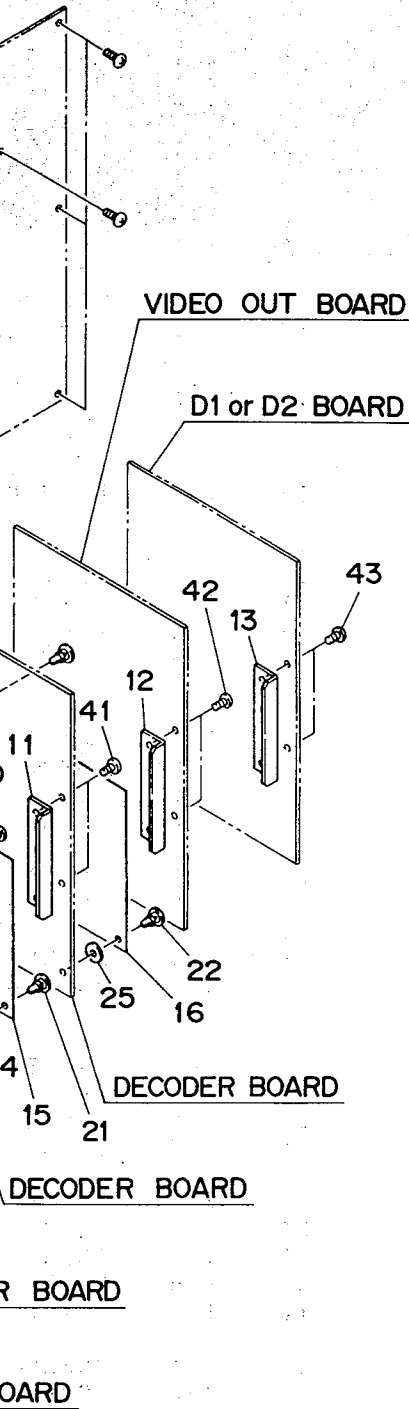
COLOR MONITOR BOARD UNIT
K3-950077

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NO.	INDEX	COMPONENTS	PARTS NO.	Q'ty	
1	1-B	RIGHT FRAME	M1-920406	1	A
2	2-A	RAIL CHASSIS (1)	M2-920407	1	
3	2-E	RAIL CHASSIS (2)	M2-950165	1	
4	3-A	BOARD STOPPER	M3-950143	1	
5	1-B	RIGHT METAL	M3-920402	1	
6	1-E	Tr FIXING METAL	M4-909466	1	—
7	3-D	BOARD METAL	M4-920363-A	1	
8	3-D	BOARD METAL	M4-920363-B	1	
9	4-D	BOARD METAL	M4-920363-C	1	
10	4-C	BOARD METAL	M4-920363-C	1	
11	4-C	BOARD METAL	M4-920363-C	1	B
12	5-C	BOARD METAL	M4-920363-D	1	
13	5-C	BOARD METAL	M4-920363-E	1	
14	4-E	SHIELD PANEL	M4-950237	1	
15	4-D	SHIELD PANEL	M4-950237	1	
16	5-D	SHIELD PANEL	M4-950237	1	—
17	1-B	GUIDE RAIL	GR-120S	7	
18	2-D	GUIDE RAIL	GR-120S	7	
19	2-D	EDGING SADDLE	EDS-3	1	
20	4-D	PUSH RIVET	P4070B	2	
21	5-D	PUSH RIVET	P4070B	2	
22	5-D	PUSH RIVET	P4070B	2	C
23	4-D	SPACER	C403	2	
24	4-D	SPACER	C403	2	
25	5-D	SPACER	C403	2	
26	1-B	BUSHING (2)	M4-916295	1	—
27	3-C	BUSHING (2)	M4-916295	1	
28	4-C	SCREW	NMB 3-6	7	
29	4-C	SCREW	NMB 3-6	4	
30	3-B	SCREW	NMB 3-6	2	
31	3-B	SCREW	NMB 3-6	2	
32	2-C	SCREW	NMB 3-6	2	D
33	1-C	SCREW	NMB 3-6	2	
34	1-D	SCREW	NMB 3-6	2	
35	1-E	SCREW	NMB 3-6	2	
36	3-C	SCREW	NMB 3-6	2	—
37	3-D	SCREW	NMB 3-6	2	
38	4-D	SCREW	NMB 3-6	2	
39	4-D	SCREW	NMB 3-6	2	
40	4-C	SCREW	NMB 3-6	2	
41	5-C	SCREW	NMB 3-6	2	
42	5-C	SCREW	NMB 3-6	2	E
43	5-C	SCREW	NMB 3-6	2	

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FRONT MAIN BO

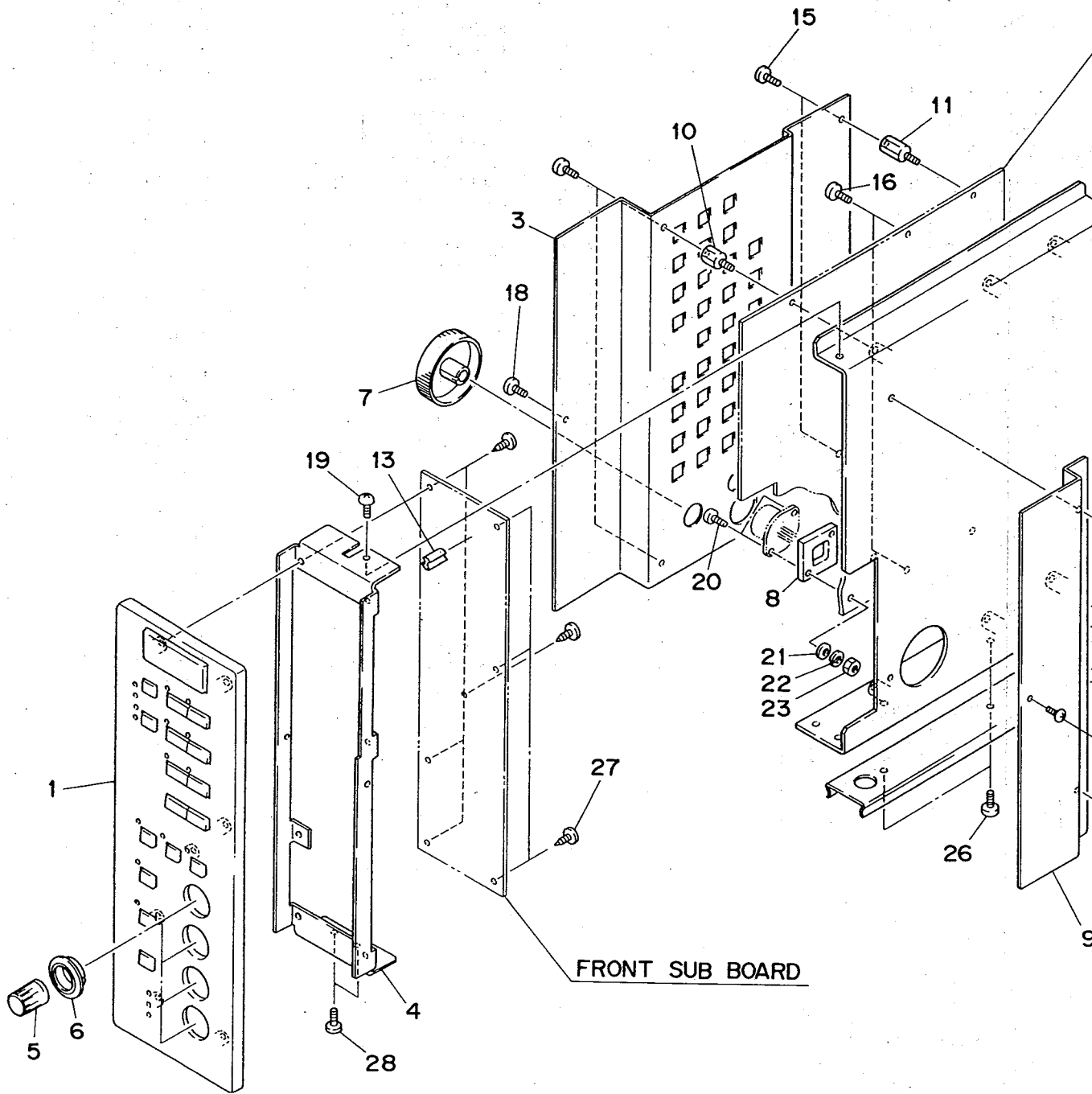
B

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D

E

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14" COLOR MONITOR
FRONT RIGHT

K3-950095

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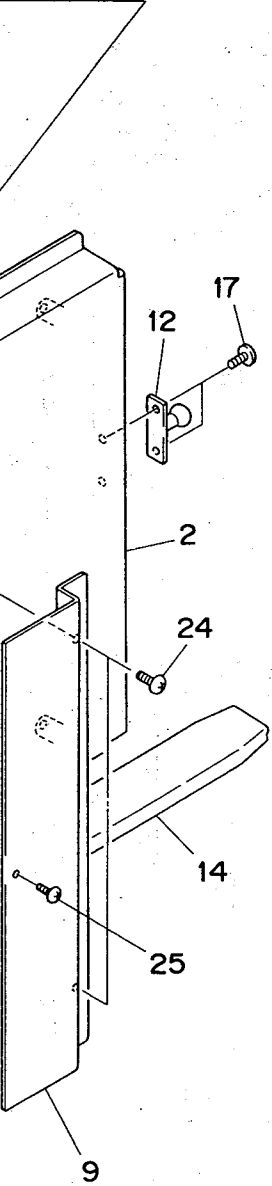
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NO.	INDEX	COMPONENTS	PARTS NO.	Q'ty
1	1-D	RIGHT PANEL	M1-950164	1
2	5-C	FRONT CHASSIS	M2-920629	1
3	3-B	FRONT COVER (1)	M2-920630	1
4	2-E	FRONT METAL	M3-920633	1
5	1-E	VR KNOB	M3-950094	4
6	1-E	VR GUIDE	M3-950095	4
7	2-C	ROTARY KNOB (2)	M4-912735	1
8	4-C	CONNECTOR SPACER	M4-911716	1
9	5-E	FRONT COVER (2)	M4-920631	1
10	3-B	METAL SUPPORT	PNC 8	2
11	4-B	METAL SUPPORT	PNC 12	2
12	5-B	TOUCH LATCH (STRIKE)	TTL-00562	1
13	2-C	LED SPACER	LH-5-10	22
14	5-D	SLIDES RAIL (INNER)	C-203-413	1
15	4-A	SCREW	NMB 3-6	4
16	4-B	SCREW	NMB 3-6	2
17	5-B	SCREW	NMB 3-6	2
18	3-B	SCREW	NMB 2.6-6	1
19	2-C	SCREW	NMB 3-6	1
20	3-C	SCREW	NMB 2.6-10	2
21	4-D	PLANE WASHER	HW 2.6	2
22	4-D	SPRING LOCK WASHER	SW 2.6	2
23	4-D	HEXAGON NUTS	N 2.6	2
24	5-C	SCREW	NMB 3-6	2
25	5-D	SCREW	NMB 2.6-6	1
26	4-D	SCREW	NMB 4-6	2
27	3-D	TAPPING SCREW	NMB-TS 2.6-10	7
28	2-E	SCREW	NMB 3-6	2

MAIN BOARD



A

B

C

D

E

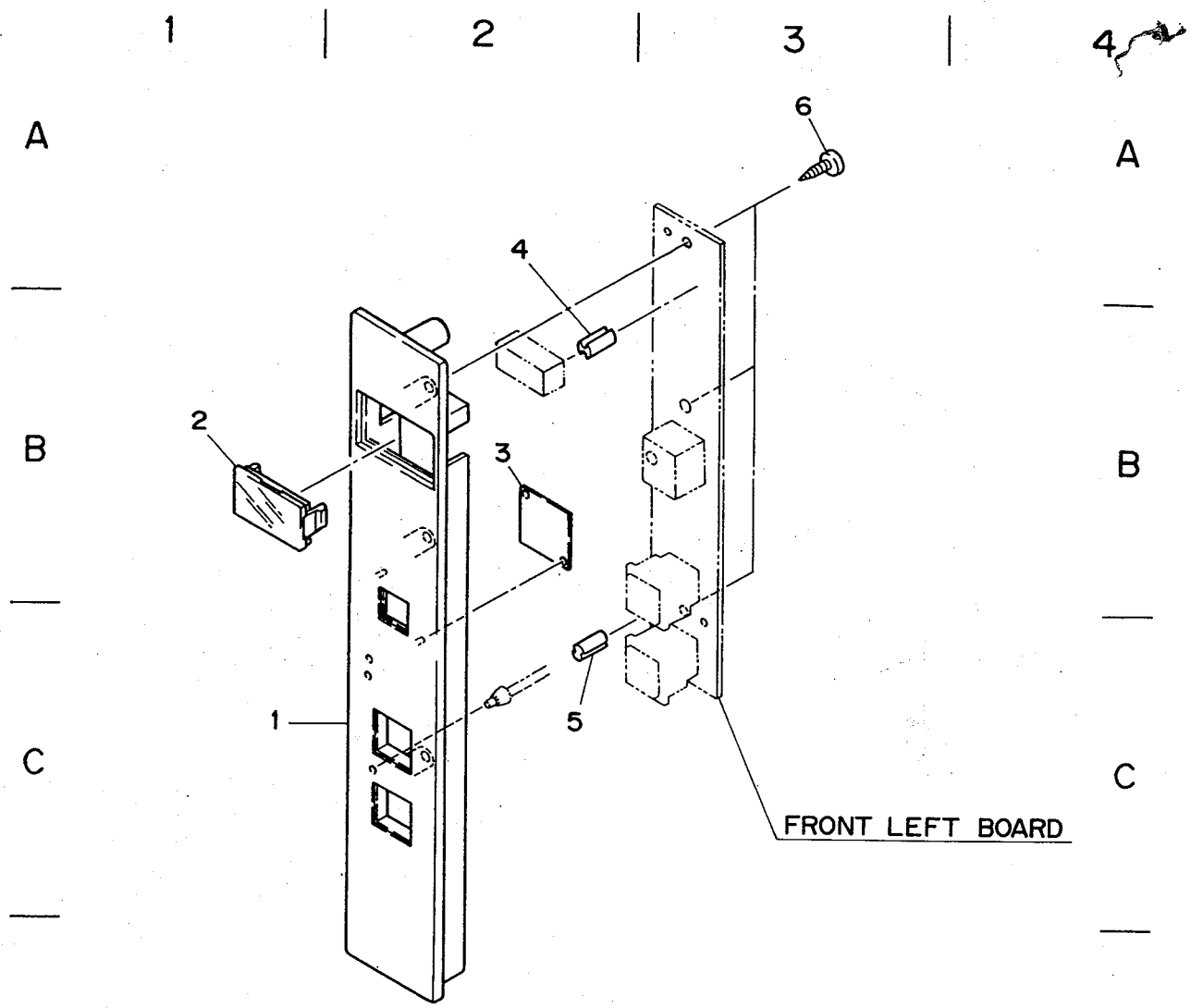
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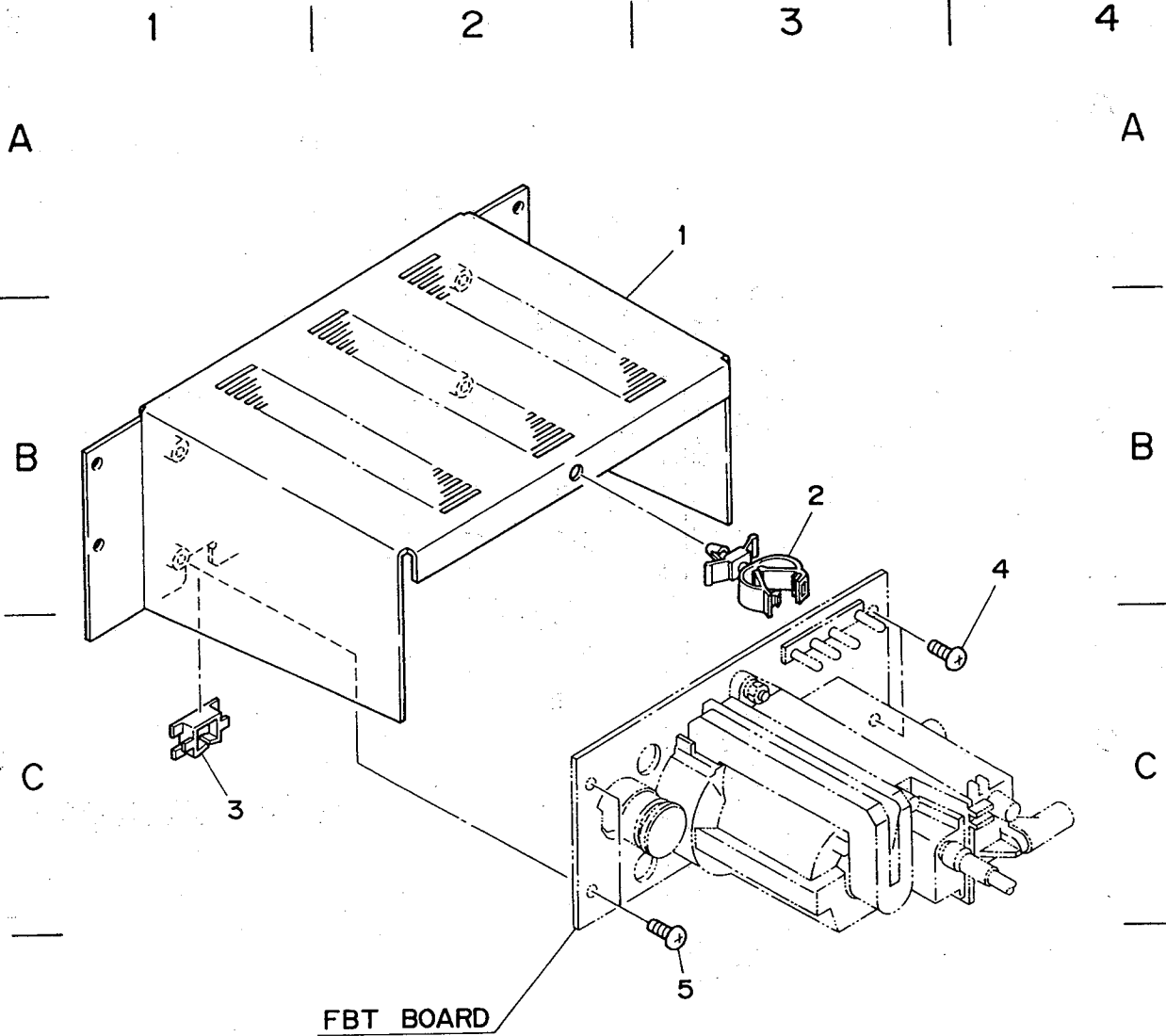
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NO.	INDEX	COMPONENTS	PARTS NO.	Q' ty
1	1-C	LEFT PANEL	M1-950153	1
2	1-B	TALLY	M4-909001	1
3	2-B	FILTER	M4-920504	1
4	2-A	LED SPACER	LH-5-5	2
5	2-C	LED SPACER	LH-5-10	3
6	3-A	TAPPING SCREW	NMB-TS 3-10	3

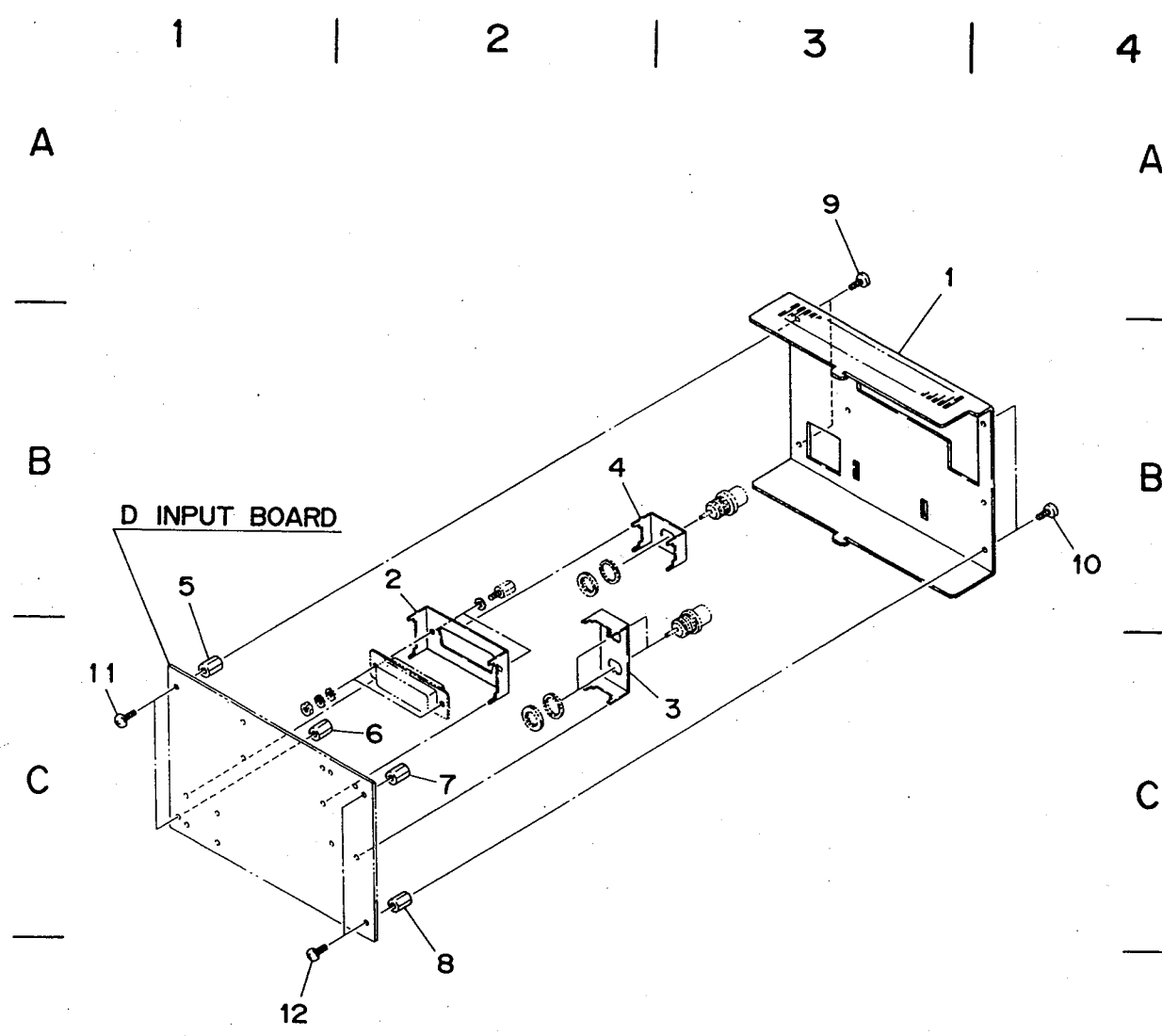
14" COLOR MONITOR
FRONT LEFT
K4-950096



NO.	INDEX	COMPONENTS	PARTS NO.	Q'ty
1	3-A	FBT BASE	M2-920639	1
2	3-B	SPACE CLIP	VSC-10	1
3	1-C	EDGING SADDLE	EDS-3	1
4	4-B	SCREW	NMB 3-6	2
5	3-D	SCREW	NMB 3-6	2

14" COLOR MONITOR
HV UNIT

K4-950097



NO.	INDEX	COMPONENTS	PARTS NO.	Q' ty
1	3-A	D INPUT COVER	M3-950365	1
2	2-B	CONNECTOR METAL	M4-950367	1
3	3-C	BNC METAL	M4-950024	1
4	2-B	BNC METAL (2)	M4-950366	1
5	1-B	METAL SUPPORT	PSC 10	1
6	2-C	METAL SUPPORT	PSC 10	1
7	2-C	METAL SUPPORT	PSC 10	1
8	2-D	METAL SUPPORT	PSC 10	1
9	3-A	SCREW	NMB 3-6	2
10	4-B	SCREW	NMB 3-6	2
11	1-C	SCREW	NMB 3-6	2
12	1-D	SCREW	NMB 3-6	2

**COLOR MONITOR
D INPUT BOARD PARTS
K4-950083**

**TM14-20RH/RP
TM20-20RH/RP
TM20-30RH/RP
COLOR MONITOR
Service Manual**

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